Innovative Service Around the Globe

DATA SHEET

SURFACE-MOUNT CERAMIC MULTILAYER CAPACITORS

01005

NP0/X5R/X7R 4 V TO 25 V

0.5 pF to 470 nF

RoHS compliant & Halogen Free



YAGEO Phícomp



SCOPE

This specification describes 01005 NP0/X5R series chip capacitors with lead-free terminations.

APPLICATIONS

- Mobile
- Module

FEATURES

- Supplied in tape on reel
- Nickel-barrier end termination
- RoHS compliant
- Halogen Free compliant

ORDERING INFORMATION-GLOBAL PART NUMBER, PHYCOMP

CTC & 12NC

All part numbers are identified by the series, size, tolerance, TC material, packing style, voltage, process code, termination and capacitance value.

YAGEO BRAND ordering code

GLOBAL PART NUMBER (PREFERRED)

CC <u>xxxx x x xxx x B x xxx</u>

(1) (2) (3) (4) (5) (6) (7)

(I) SIZE - INCH BASED (METRIC)

0100(0402)

(2) TOLERANCE

 $B = \pm 0.1 pF$

 $C = \pm 0.25 pF$

 $D = \pm 0.5 pF$

 $| = \pm 5\%$

 $K = \pm 10\%$

 $M = \pm 20\%$

(3) PACKING STYLE

R = Paper/PE taping reel; Reel 7 inch

(4) TC MATERIAL

NPO

X5R

X7R

(5) RATED VOLTAGE

 $4 = 4 \ \lor$

5 = 6.3 V

6 = 10 V

7 = 16 V

8 = 25 V

(6) PROCESS

N = NP0

B = Class 2 MLCC

(7) CAPACITANCE VALUE

2 significant digits+number of zeros

The 3rd digit signifies the multiplying factor, and letter R is decimal point

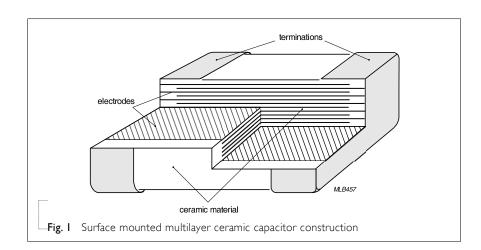
Example: $121 = 12 \times 10^{1} = 120 \text{ pF}$

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CONSTRUCTION

The capacitor consists of a rectangular block of ceramic dielectric in which a number of interleaved metal electrodes are contained. This structure gives rise to a high capacitance per unit volume.

The inner electrodes are connected to the two end terminations and finally covered with a layer of plated tin (NiSn). The terminations are lead-free. A cross section of the structure is shown in Fig. I.

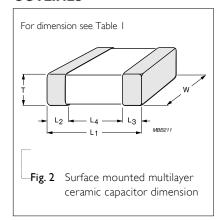


DIMENSION

Table I For outlines see fig. 2

TVDE	(NA / ()	T ()	L ₂ / L ₃	(mm)	L ₄ (mm)
TIPE	L _I (mm)	W (mm)	T (mm)	min.	max.	min.
01005	0.4 ±0.02	0.2 ±0.02	0.2 ±0.02	0.07	0.14	0.13

OUTLINES





CAPACITANCE RANGE & THICKNESS

Surface-Mount Ceramic Multilayer Capacitors

		& THICKNES	<u>S</u>					
Table 2 0	1005 Sizes NP0 16 V / 25 V	CAP.	X5R 4V	6.3V	10V	CAP.	X7R 6.3V / 10V	16V
0.5 pF	0.2±0.02	100 pF	0.2±0.02	0.2±0.02	0.2±0.02	100 pF	0.2±0.02	0.2±0.02
0.6 pF	0.2±0.02	150 pF	0.2±0.02	0.2±0.02	0.2±0.02	150 pF	0.2±0.02	0.2±0.02
0.7 pF	0.2±0.02	220 pF	0.2±0.02	0.2±0.02	0.2±0.02	220 pF	0.2±0.02	0.2±0.02
0.75 pF	0.2±0.02	330 pF	0.2±0.02	0.2±0.02	0.2±0.02	330 pF	0.2±0.02	0.2±0.02
0.8 pF	0.2±0.02	470 pF	0.2±0.02	0.2±0.02	0.2±0.02	470 pF	0.2±0.02	0.2±0.02
0.9 pF	0.2±0.02	680 pF	0.2±0.02	0.2±0.02	0.2±0.02	680 pF	0.2±0.02	0.2±0.02
1.0 pF	0.2±0.02	1 000 pF	0.2±0.02	0.2±0.02	0.2±0.02	1 000 pF	0.2±0.02	0.2±0.02
1.2 pF	0.2±0.02	2.2 nF	0.2±0.02	0.2±0.02	0.2±0.02	2.2 nF		
1.5 pF	0.2±0.02	4.7 nF	0.2±0.02	0.2±0.02	0.2±0.02	4.7 nF		
1.8 pF	0.2±0.02	10 nF	0.2±0.02	0.2±0.02	0.2±0.02	10 nF		
2.2 pF	0.2±0.02	22nF	0.2±0.02	0.2±0.02		22nF		
2.7 pF	0.2±0.02	47 nF	0.2±0.02	0.2±0.02		47 nF		
3.3 pF	0.2±0.02	100 nF	0.2±0.02	0.2±0.02	0.2±0.02	100 nF		
3.9 pF	0.2±0.02	220 nF	0.2±0.02	0.2±0.02		220 nF		
4.7 pF	0.2±0.02	470 nF	0.2±0.02	0.2±0.02		Tape width	8 mr	m
5.6 pF	0.2±0.02	Tape width	<u>-</u>	8 mm				
6.8 pF	0.2±0.02							
8.2 pF	0.2±0.02							
10 pF	0.2±0.02							
12 pF	0.2±0.02							
15 pF	0.2±0.02							



18 pF

22 pF 27 pF

33 pF

39 pF

47 pF

56 pF

68 pF 82 pF

100 pF

Tape width

0.2±0.02 0.2±0.02

0.2±0.02

0.2±0.02

0.2±0.02

0.2±0.02

0.2±0.02 0.2±0.02

 0.2 ± 0.02

0.2±0.02

8 mm

NP0/X5RX7R

4V to 25V

THICKNESS CLASSES AND PACKING QUANTITY

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	a	bl	le	3

SIZE	THICKNESS	TAPE WIDTH -	Ø180 M	M/7INCH	Ø330 MI	M / 13 INCH	OUANTITY
CODE	CLASSIFICATION	., =	Paper/PE	Blister	Paper/	Blister	PER BULK CASE
01005	0.2 ±0.02 mm	8 mm	20,000				

ELECTRICAL CHARACTERISTICS

NP0/X5R DIELECTRIC CAPACITORS; NISN TERMINATIONS

Unless otherwise specified, all test and measurements shall be made under standard atmospheric conditions for testing as given in 5.3 of IEC 60068-1:

Temperature: 15 °C to 35 °C
Relative humidity: 25% to 75%
Air pressure: 86 kPa to 106 kPa

Before the measurements are made, the capacitor shall be stored at the measuring temperature for a time sufficient to allow the entire capacitor to reach this temperature.

The period as prescribed for recovery at the end of a test is normally sufficient for this purpose.

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	ı	h	Δ	4

DESCRIPT	ION	VALUE
Capacitanc	e range	0.5 pF to 470 nF
Capacitanc	e tolerance	
	C< 10 pF	±0.1 pF, ±0.25pF, ±0.5pF
NP0	C ≥ 10 pF	±5%, ±10%
X5R / X7	R	±10%, ±20%
Dissipation	factor (D.F.)	
NP0	C < 30 _P F	≤ I / (400 + 20C)
	C ≥ 30 pF	≤ 0.1 %
X5R / X7	R	≤ 10 %
		$R_{ins} \ge 10 \text{ G}\Omega \text{ or } R_{ins} \times C \ge 500\Omega \cdot F \text{ whichever is less}$
Insulation r	resistance after I minute at U _r (DC)	X5R/X7R > 10nF:
Massimum	capacitance change as a function of temperature	Rins \times C \geq 50 Ω · F
	re characteristic/coefficient):	
NP0	,	±30 ppm/°C
X5R / X7	R	±15%
Operating 1	temperature range:	
NP0		–55 °C to +125 °C
X5R		−55 °C to +85 °C
X7R		-55 °C to +125 °C

SOLDERING RECOMMENDATION

Table 5	
SOLDERING METHOD	SIZE 01005
Reflow	Reflow only
Reflow/Wave	

TESTS AND REQUIREMENTS

Table 6 Test procedures and requirements

TEST	TEST MET	HOD	PROCEDURE	REQUIREMENTS
Mounting	IEC 60384- 21/22	4.3	The capacitors may be mounted on printed-circuit boards or ceramic substrates	No visible damage
Visual Inspection and Dimension Check		4.4	Any applicable method using × 10 magnification	In accordance with specification
Capacitance		4.5.1	Class I: $f = I \text{ MHz for C} \le I \text{ nF, measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C}$ $f = I \text{ KHz for C} > I \text{ nF, measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C}$	Within specified tolerance
			Class 2:	
			$C \le I$ nF f = I KHz, measuring at voltage I Vrms at 20 °C	
			C > I nF $f = I$ KHz, rated voltage \leq 6.3 V, measuring at voltage 0.5 Vrms at 20 °C $f = I$ KHz, rated voltage > I0 V, measuring at voltage I Vrms at 20 °C	
Dissipation Factor (D.F.)		4.5.2	Class I: $f = I \text{ MHz for } C \leq I \text{ nF , measuring at voltage } I \text{ V}_{ms} \text{ at } 20 \text{ °C}$ $f = I \text{ KHz for } C > I \text{ nF, measuring at voltage } I \text{ V}_{ms} \text{ at } 20 \text{ °C}$	In accordance with specification
			Class 2: $C \le I \text{ nF}$ f = I KHz, measuring at voltage I Vrms at 20 °C	
			C > I nF $f = I$ KHz, rated voltage ≤ 6.3 V, measuring at voltage 0.5 Vrms at 20 °C f = I KHz, rated voltage > I0 V,	
			measuring at voltage Vrms at 20 °C	
Insulation Resistance		4.5.3	At Ur (DC) for I minute	In accordance with specification

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TEST **TEST METHOD PROCEDURE**

Temperature coefficient

4.6 Capacitance shall be measured by the steps shown

in the following table.

The capacitance change should be measured after 5 min at each specified temperature stage.

Step	Temperature(°C)
a	25±2
Ь	Lower temperature±3°C
С	25±2
d	Upper Temperature±2℃
е	25±2

(I) Class I

Temperature Coefficient shall be calculated from the formula as below

Temp, Coefficient =
$$\frac{C2 - C1}{C1 \times \Delta T} \times 10^6 \text{ [ppm/°C]}$$

C1: Capacitance at step c

C2: Capacitance at 125℃

 ΔT : 100°C(=125°C-25°C)

Measuring Voltage: 0.5 to 5 Vrms

(2) Class II

Capacitance Change shall be calculated from the formula as below

$$\Delta C = \frac{C2 - C1}{C1} \times 100\%$$

C1: Capacitance at step c

C2: Capacitance at step b or d

Adhesion

IEC 60384-21/22

A force applied for 10 seconds to the line joining

the terminations and in a plane parallel to the

substrate

REQUIREMENTS

Class I (NP0):

±30ppm

 Δ C/C

Class 2: (X7R/X5R):

±15%

In case of applying voltage, the capacitance change should be measured after I more min. with applying

voltage in equilibration of each temp. stage.

CC0100MRX5R4(5)BB104(224): 0.2V±0.1Vrms

Bending Strength

4.8

4.7

Mounting in accordance with IEC 60384-22

paragraph 4.3

Conditions: bending I mm at a rate of I mm/s, radius jig 5 mm

Force

size 01005: 1N

No visible damage

Δ C/C

Class I (NP0):

within ±1% or 0.5 pF, whichever is greater

Class2 (X5R/X7R):

±10%

ion	8
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TEST	TEST METH	HOD	PROCEDURE	REQUIREMENTS
Resistance to Soldering		4.9	Precondition: I50 +0/-10 °C for I hour, then keep for 24 ±1 hours at room	Dissolution of the end face plating shall not exceed 25% of the length of the edge concerned
Heat			temperature Preheating: 120 °C to 150 °C for 1 minute and 170 °C to 200 °C for 1 minute. Solder bath temperature: 260 ±5 °C Dipping time: 10 ±0.5 seconds Recovery time: 24 ±2 hours	ΔC/C Class I (NP0): within ±0.5% or 0.5 pF, whichever is greater Class2 (X5R/X7R): ±10% D.F. within initial specified value
				R _{ins} within initial specified value
Solderability		4.10	Preheated the temperature of 80 °C to 140 °C and maintained for 30 seconds to 60 seconds.	The solder should cover over 95% of the critical area of each termination
			Test conditions for leadfree containing solder alloy	
			Temperature: 245 ± 5 °C Dipping time: 3 ± 0.3 seconds Depth of immersion: 10 mm	
Rapid Change of	IEC 60384- 21/22	4.11	Preconditioning; 150 +0/–10 °C for I hour, then keep for	No visual damage
Temperature			24 ±1 hours at room temperature	ΔC/C
			5 cycles with following detail: 30 minutes at lower category temperature	Class I (NP0): within ±2.5% or 0.25 pF, whichever is greater
			30 minutes at upper category temperature	Class2 (X5R/X7R):
			Recovery time 24 ±2 hours	±15%
			Necovery time 24 ±2 hours	D.F. meet initial specified value
				R _{ins} meet initial specified value
			recovery time 21 ±2 hours	'

Surface-Mount Ceramic Multilayer Capacitors

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NP0/X5RX7R

4V to 25V

TEST TEST METHOD PROCEDURE F	REQUIREMENTS
Ur load 1. Preconditioning, class 2 only: 150 +0/-10 °C /1 hour, then keep for 24 ± 1 hour at room temp 2. Initial measure: Spec: refer initial spec C, D, IR 3. Damp heat test: 500 ± 12 hours at 40 ± 2 °C; 90 to 95% R, H; 1.0 Ur applied. 4. Recovery: Class 1: 6 to 24 hours Class 2: 24 ± 2 hours 5. Final measure: C, D, IR P.S. If the capacitance value is less than the minimum value permitted, then after the other measurements have been made the capacitor shall be precondition according to "IEC 60384 4.1" and then the requirement shall be met.	No visual damage after recovery Class I (NP0): $\Delta C/C$ within $\pm 7.5\%$ or 0.75 pF, whichever is greater D.F. $\leq 2 \times \text{specified value}$ I.R. $\geq 2,500 \text{ M}\Omega$ or $R_{\text{ins}} \times \text{Cr} \geq 25\Omega \cdot \text{F}$ whichever is less Class2 (X5R/X7R): $C \leq \text{InF}$ $\Delta C/C$ $\pm 15\%$ D.F. $\leq 10\%$ I.R. $\geq 500 \text{ M}\Omega$ $10\text{nF} \geq C > \text{InF}$ $\Delta C/C$ $\pm 20\%$ D.F. $\leq 10\%$ I.R. $\geq 500 \text{ M}\Omega$ $C > 10\text{nF}$ $\Delta C/C$ $\pm 20\%$ D.F. $\leq 10\%$ I.R. $\geq 500 \text{ M}\Omega$

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4V to 25V

TEST	TEST METHOD		PROCEDURE	REQUIREMENTS	
Endurance		4.14	 Preconditioning, class 2 only: 150 +0/-10 °C /I hour, then keep for 24 ±1 hour at room temp Initial measure: Spec: refer initial spec C, D, IR Endurance test: Temperature: NP0: 125 °C Specified stress voltage applied for 1,000 hours:	No visual damage Class I (NP0): $\Delta C/C$ within $\pm 3\%$ or 0.3 pF, whichever is greater D.F. $\leq 2 \times \text{specified value}$ I.R. $\geq 4,000 \text{ M}\Omega \text{ or } R_{\text{ins}} \times \text{Cr} \geq 40\Omega \cdot \text{F} \text{ whichever}$ is less Class2 (X5R/X7R): $C \leq \text{InF}$ $\Delta C/C$ $\pm 15\%$ D.F. $\leq 10\%$ I.R. $\geq 1G\Omega$ IOnF $\geq C > \text{InF}$ $\Delta C/C$ $\pm 15\%$ D.F. $\leq 10\%$ I.R. $\geq 1G\Omega$ C > 10nF $\Delta C/C$ $\pm 25\%$ D.F. $\leq 20\%$ I.R. $R_{\text{ins}} \times \text{Cr} \geq 10\Omega \cdot \text{F}$	
Voltage Proof	IEC 60384-1	4.5.4	Specified stress voltage applied for 1~5 seconds	No breakdown or flashover	
			Ur ≤ 100 V: series applied 2.5 Ur		
			Charge/Discharge current is less than 50 mA		

REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 10	May 5, 2017	-	- Rated voltage of NPO series extend to 25 V
			- Add X5R, 470nF, 4V to 6.3V and 100nF, 10V
Version 9	Jan. 17, 2017	-	- Test condition updated
Version 8	Jan. 12, 2016	-	- Capacitance range & thickness update
Version 7	Oct. 31, 2015	-	- Capacitance range & thickness update
Version 6	Jun. 29, 2015	-	- Test procedures and requirements
Version 5	Jun. 06, 2013	-	- Test procedures and requirements
Version 4	Mar. 27, 2013	-	- Change Tolerance
Version 3	Jan. 15, 2013	-	- Change Range
Version 2	Oct. 23, 2012	-	- Change Range
Version I	July 03, 2012	-	- Change Range
Version 0	Apr 16, 2012	-	- New

Mouser Electronics

Authorized Distributor

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Yageo:

CC0100KRX5R5BB104	CC0100KRX5R5BB103	CC0100MRX5R5BB104	CC0100JRNPO7BN220
CC0100JRNPO6BN220	CC0100JRNPO7BN120	CC0100KRX5R5BB102	CC0100JRNPO6BN120
CC0100JRNPO6BN330	CC0100JRNPO6BN100	CC0100JRNPO6BN101	CC0100JRNPO7BN100
CC0100JRNPO7BN101	CC0100MRX5R5BB224	CC0100KRX5R5BB101	CC0100KRX5R5BB222
CC0100JRNPO7BN150	CC0100JRNPO7BN330	CC0100JRNPO7BN470	CC0100KRX5R6BB471
CC0100JRNPO7BN270	CC0100JRNPO8BN220	CC0100MRX5R5BB474	CC0100CRNPO7BN1R0
CC0100JRNPO8BN100	CC0100JRNPO8BN330	CC0100MRX5R5BB223	CC0100JRNPO8BN101
CC0100JRNPO8BN120	CC0100JRNPO8BN180	CC0100KRX5R5BB223	CC0100KRX5R6BB102
CC0100KRX7R6BB221	CC0100KRX5R6BB103	CC0100KRX5R6BB472	CC0100KRX7R6BB102
CC0100MRX5R4BB224	CC0100MRX5R5BB473	CC0100CRNPO7BN6R8	CC0100CRNPO7BN2R7
CC0100JRNPO6BN180	CC0100CRNPO7BN3R9	CC0100CRNPO7BN2R	CC0100CRNPO7BN5R0
CC0100CRNPO7BN3R3	CC0100CRNPO7BN3R0	CC0100JRNPO7BN390	CC0100JRNPO6BN560
CC0100JRNPO7BN820	CC0100JRNPO6BN680	CC0100KRX5R5BB221	CC0100CRNPO7BN2R4
CC0100KRX5R5BB682	CC0100CRNPO7BN5R6	CC0100JRNPO6BN470	CC0100CRNPO7BN8R2
CC0100KRX5R5BB471	CC0100BRNPO7BNR75	CC0100JRNPO8BN390	CC0100JRNPO7BN680
CC0100JRNPO8BN560	CC0100KRX7R5BB101	CC0100JRNPO8BN470	CC0100KRX5R5BB331
CC0100CRNPO7BN7R0	CC0100CRNPO7BN2R2	CC0100JRNPO7BN180	CC0100JRNPO7BN560
CC0100CRNPO7BN4R0	CC0100BRNPO7BNR50	CC0100KRX7R5BB221	CC0100CRNPO7BN8R0
CC0100CRNPO7BN1R5	CC0100KRX5R6BB101	CC0100JRNPO6BN270	CC0100JRNPO7BN300
CC0100KRX5R5BB332	CC0100JRNPO6BN390	CC0100CRNPO7BN4R7	CC0100KRX5R5BB472
CC0100JRNPO6BN820	CC0100CRNPO7BN1R2	CC0100JRNPO6BN150	CC0100CRNPO7BN1R8