

Thyristor Module

= 2x 1600 V

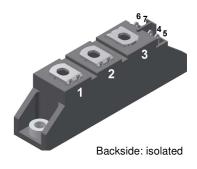
110 A

 V_{T} 1.21 V

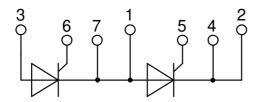
Phase leg

Part number

MCMA110P1600TA







Features / Advantages:

- Thyristor for line frequency
- Planar passivated chip
- Long-term stability
- Direct Copper Bonded Al2O3-ceramic

Applications:

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- AC power control
- Lighting and temperature control

Package: TO-240AA

- Isolation Voltage: 4800 V~
- Industry standard outline
- RoHS compliant
- Soldering pins for PCB mounting
- Base plate: DCB ceramic
- · Reduced weight
- Advanced power cycling

Terms and Conditions of Usage

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact your local sales office.

Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact your local sales office.

Should you intend to use the product in aviation, in health or life endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747 and per semiconductor unless otherwise specified

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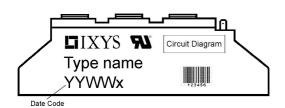


Thyristo	r				Ratings	3	
Symbol	Definition	Conditions		min.	typ.	max.	Un
V _{RSM/DSM}	max. non-repetitive reverse/forwa	rd blocking voltage	$T_{VJ} = 25^{\circ}C$			1700	
V _{RRM/DRM}	max. repetitive reverse/forward bl	ocking voltage	$T_{VJ} = 25^{\circ}C$			1600	,
I _{R/D}	reverse current, drain current	V _{R/D} = 1600 V	$T_{VJ} = 25^{\circ}C$			100	μ
		$V_{R/D} = 1600 \text{ V}$	$T_{VJ} = 140$ °C			10	m
V _T	forward voltage drop	I _T = 110 A	$T_{VJ} = 25^{\circ}C$			1.24	,
		$I_{T} = 220 \text{ A}$				1.52	,
		$I_{T} = 110 \text{ A}$	T _{vJ} = 125°C			1.21	,
		$I_{T} = 220 \text{ A}$				1.57	,
I _{TAV}	average forward current	$T_{C} = 85^{\circ}C$	T _{vJ} = 140°C			110	1
I _{T(RMS)}	RMS forward current	180° sine				170	,
V _{T0}	threshold voltage		T _{vJ} = 140°C			0.85	,
r _T	slope resistance	oss calculation only				3.3	m۵
R _{thJC}	thermal resistance junction to cas	e				0.3	K/V
R _{thCH}	thermal resistance case to heatsing	nk			0.20		K/V
P _{tot}	total power dissipation		T _C = 25°C			380	٧
I _{TSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			1.90	k/
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			2.05	k٨
		t = 10 ms; (50 Hz), sine	T _{v.i} = 140°C			1.62	k/
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			1.75	k
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			18.1	kA ²
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			17.5	kA ²
		t = 10 ms; (50 Hz), sine	T _{v.i} = 140°C			13.0	kA ²
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			12.7	kA2
C _J	junction capacitance	$V_R = 400 V$ f = 1 MHz	$T_{VJ} = 25^{\circ}C$		95		pl
P _{GM}	max. gate power dissipation	t _P = 30 μs	T _C = 140°C			10	٧
Cili	• , ,	t _P = 300 μs				5	٧
P_{GAV}	average gate power dissipation					0.5	٧
(di/dt) _{cr}	critical rate of rise of current	$T_{VJ} = 140 ^{\circ}\text{C}; f = 50 \text{Hz}$ re	epetitive, $I_T = 330 \text{ A}$			150	Α/μ
	$t_P = 200 \mu s; di_G/dt = 0.45 A/\mu s;$						
		$I_{G} = 0.45 \text{ A}; V = \frac{2}{3} V_{DRM}$ no	on-repet., $I_{T} = 110 \text{ A}$			500	Α/μ
(dv/dt) _{cr}	critical rate of rise of voltage	$V = \frac{2}{3} V_{DRM}$	T _{v.i} = 140°C			1000	<u> </u>
		R _{GK} = ∞; method 1 (linear volta	ge rise)				
V _{GT}	gate trigger voltage	V _D = 6 V	$T_{VJ} = 25^{\circ}C$			1.5	١
		-	$T_{VJ} = -40$ °C			1.6	١
I _{GT}	gate trigger current	$V_D = 6 V$	$T_{VJ} = 25^{\circ}C$			150	m/
~ .		_	$T_{VJ} = -40$ °C			200	m/
V _{GD}	gate non-trigger voltage	$V_D = \frac{2}{3} V_{DRM}$	T _{vJ} = 140°C			0.2	-
I _{GD}	gate non-trigger current					10	m
I _L	latching current	t _p = 10 μs	$T_{VJ} = 25$ °C			200	m
-		$I_{\rm G} = 0.45 \text{A}; \text{di}_{\rm G}/\text{dt} = 0.45 \text{A}/\mu \text{s}$					
I _H	holding current	$V_D = 6 \text{ V } R_{GK} = \infty$	T _{vJ} = 25°C			200	m
t _{gd}	gate controlled delay time	$V_D = \frac{1}{2} V_{DRM}$	T _{VJ} = 25°C			2	μ
gu	-	$I_{\rm G} = 0.45 \text{A}; \text{di}_{\rm G}/\text{dt} = 0.45 \text{A}/\mu \text{s}$					r
t _q	turn-off time	$V_R = 100 \text{ V}; I_T = 110 \text{ A}; V = \frac{2}{3}$			185		μ
- 9		$di/dt = 10 \text{ A}/\mu \text{s} \text{ dv/dt} = 20 \text{ V}$.00		μ



MCMA110P1600TA

Package TO-240AA				Ratings				
Symbol	Definition	Conditions			min.	typ.	max.	Unit
RMS	RMS current	per terminal					200	Α
T _{VJ}	virtual junction temperature	9			-40		140	°C
Top	operation temperature				-40		125	°C
T _{stg}	storage temperature				-40		125	°C
Weight						81		g
M _D	mounting torque				2.5		4	Nm
$\mathbf{M}_{_{T}}$	terminal torque				2.5		4	Nm
d _{Spp/App}	oroonaga diatanaa an aurfe	and Latriking distance through air	terminal to terminal	13.0	9.7			mm
$d_{Spb/Apb}$	creepage distance on surface striking distance thro		terminal to backside	16.0	16.0			mm
V _{ISOL}	isolation voltage	t = 1 second			4800			٧
		t = 1 minute	50/60 Hz, RMS; I _{ISOL} ≤ 1 mA		4000			٧



Part description

M = Module

M = Module
C = Thyristor (SCR)
M = Thyristor
A = (up to 1800V)
110 = Current Rating [A]
P = Phase leg
1600 = Reverse Voltage [V]

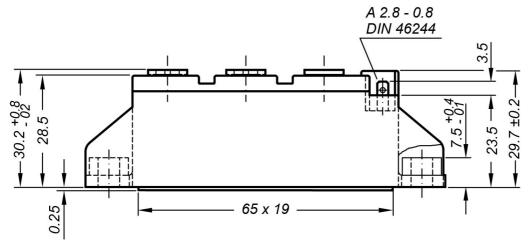
TA = TO-240AA-1B

Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	MCMA110P1600TA	MCMA110P1600TA	Box	36	513383

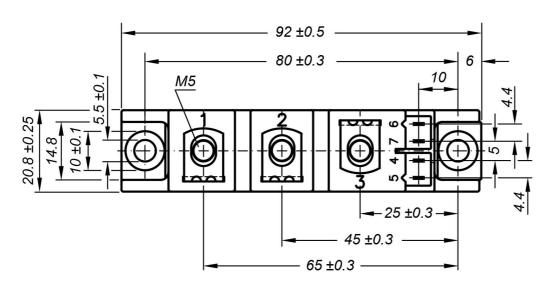
Equivalent Circuits for Simulation			* on die level	$T_{VJ} = 140 ^{\circ}\text{C}$
$I \rightarrow V_0$)— <u>R</u> o—	Thyristor		
V _{0 max}	threshold voltage	0.85		V
$R_{0 \; \text{max}}$	slope resistance *	2.1		$m\Omega$



Outlines TO-240AA

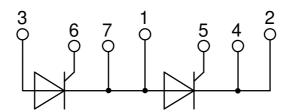


General tolerance: DIN ISO 2768 class "c"



Optional accessories: Keyed gate/cathode twin plugs Wire length: 350 mm, gate = white, cathode = red UL 758, style 3751

Type **ZY 200L** (L = Left for pin pair 4/5) Type **ZY 200R** (R = Right for pin pair 6/7)





Thyristor

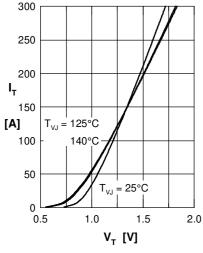


Fig. 1 Forward characteristics

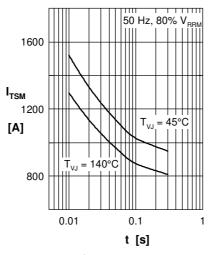


Fig. 2 Surge overload current I_{TSM} : crest value, t: duration

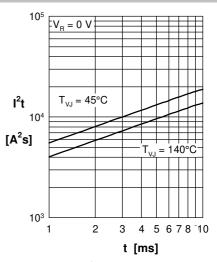


Fig. 3 I²t versus time (1-10 s)

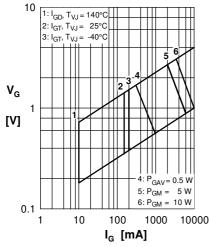


Fig. 4 Gate voltage & gate current

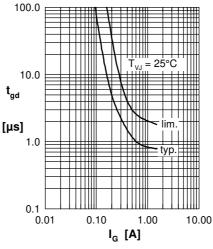


Fig. 5 Gate controlled delay time t_{ad}

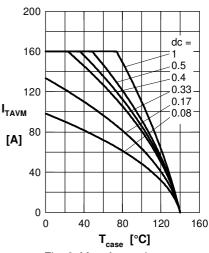


Fig. 6 Max. forward current at case temperature

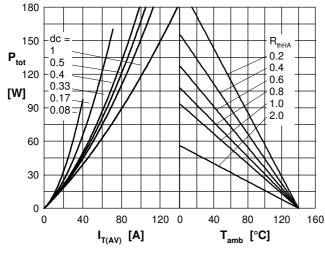


Fig. 7a Power dissipation versus direct output current Fig. 7b and ambient temperature

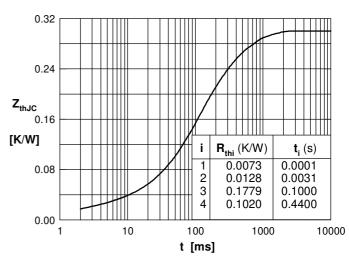


Fig. 8 Transient thermal impedance junction to case

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