SN

- Operation From Very Slow Edges
- Improved Line-Receiving Characteristics
- High Noise Immunity

#### description

Each circuit functions as an inverter, but because of the Schmitt action, it has different input threshold levels for positive-going ( $V_{T+}$ ) and negative-going ( $V_{T-}$ ) signals.

These circuits are temperature compensated and can be triggered from the slowest of input ramps and still give clean, jitter-free output signals.

SN7414 SN74LS14	S14 J OR W PACKAGE D, N, OR NS PACKAGE . D, DB, OR N PACKAGE (TOP VIEW)
1A [ 1Y [ 2A [ 2Y [ 3A [ 3Y [ GND [	4 11 <b>5</b> A
	14 FK PACKAGE (TOP VIEW)
2A ] 4 NC ] 5 2Y ] 6 NC ] 7 3A ] 8	$\begin{array}{c} & \downarrow & \downarrow & \downarrow \\ & \downarrow & \downarrow & \downarrow & \downarrow \\ & \downarrow & \downarrow$

NC - No internal connection

TA	PACI	KAGE <sup>†</sup>	ORDERABLE PART NUMBER	TOP-SIDE MARKING
	PDIP – N	Tube	SN7414N	SN7414N
	PDIP – N	Tube	SN74LS14N	SN74LS14N
		Tube	SN7414D	7414
0°C to 70°C	SOIC – D	Tape and reel	SN7414DR	7414
	50IC - D	Tube	SN74LS14D	LS14
		Tape and reel	SN74LS14DR	L314
	SOP – NS	Tape and reel	SN7414NSR	SN7414
	SSOP – DB	Tape and reel	SN74LS14DBR	LS14
		Tube	SN5414J	SN5414J
	CDIP – J	Tube	SNJ5414J	SNJ5414J
	CDIP – J	Tube	SN54LS14J	SN54LS14J
–55°C to 125°C		Tube	SNJ54LS14J	SNJ54LS14J
	CFP – W	Tube	SNJ5414W	SNJ5414W
		Tube	SNJ54LS14W	SNJ54LS14W
	LCCC – FK	Tube	SNJ54LS14FK	SNJ54LS14FK

#### ORDERING INFORMATION

<sup>†</sup> Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

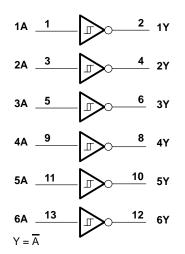
PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



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### SN5414, SN54LS14, SN7414, SN74LS14 HEX SCHMITT-TRIGGER INVERTERS SDLS049B – DECEMBER 1983 – REVISED FEBRUARY 2002

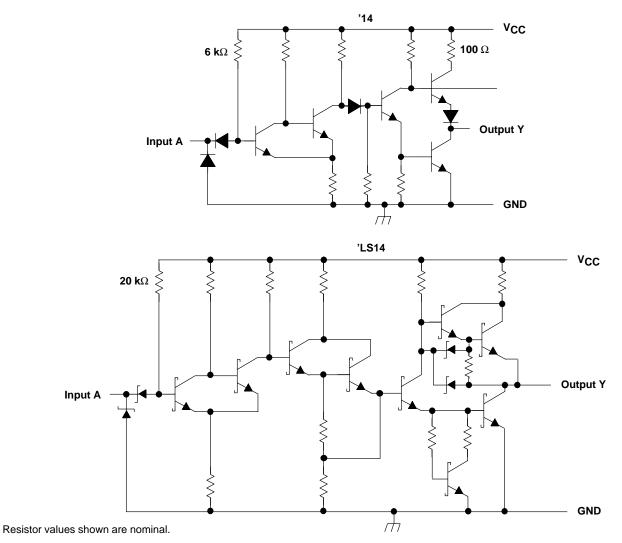
# logic diagram (positive logic)



Pin numbers shown are for the D, DB, J, N, NS, and W packages.



### schematic





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#### absolute maximum ratings over operating free-air temperature (unless otherwise noted)<sup>†</sup>

Supply voltage, V <sub>CC</sub> (see Note 1)		
Package thermal impedance, $\theta_{JA}$ (see Note	2): D package	
	DB package	
	N package	80°C/W
	NS package	
Storage temperaturerange, T <sub>stg</sub>		–65°C to 150°C

† Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. Voltage values are with respect to network ground terminal.

2. The package termal impedance is calculated in accordance with JESD 51-7

#### recommended operating conditions

			SN5414			UNIT		
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
ЮН	High-level output current			-0.8			-0.8	mA
IOL	Low-level output current			16			16	mA
Τ <sub>Α</sub>	Operating free-air temperature	-55		125	0		70	°C

#### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDIT		SN5414 SN7414			
				MIN	ΤΥΡ§	MAX	
V <sub>T+</sub>	$V_{CC} = 5 V$			1.5	1.7	2	V
V <sub>T-</sub>	$V_{CC} = 5 V$			0.6	0.9	1.1	V
Hysteresis (V <sub>T+</sub> – V <sub>T–</sub> )	V <sub>CC</sub> = 5 V			0.4	0.8		V
VIK	V <sub>CC</sub> = MIN,	lj = -12 mA				-1.5	V
VOH	$V_{CC} = MIN,$	V <sub>I</sub> = 0.6 V,	I <sub>OH</sub> = -0.8 mA	2.4	3.4		V
V <sub>OL</sub>	V <sub>CC</sub> = MIN,	V <sub>I</sub> = 2 V,	I <sub>OL</sub> = 16 mA		0.2	0.4	V
I <sub>T+</sub>	V <sub>CC</sub> = 5 V,	$V_{I} = V_{T+}$			-0.43		mA
I <sub>T-</sub>	$V_{CC} = 5 V,$	$V_I = V_{T-}$			-0.56		mA
Ц	$V_{CC} = MAX,$	VI = 5.5 V				1	mA
ΙΗ	$V_{CC} = MAX,$	VIH = 2.4 V				40	μA
۱ <sub>IL</sub>	$V_{CC} = MAX,$	V <sub>IL</sub> = 0.4 V			-0.8	-1.2	mA
IOS	V <sub>CC</sub> = MAX			-18		-55	mA
Іссн	$V_{CC} = MAX$				22	36	mA
ICCL	V <sub>CC</sub> = MAX				39	60	mA

<sup>‡</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

§ All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^{\circ}C$ .

I Not more than one output should be shorted at a time.



#### SN5414, SN54LS14, SN7414, SN74LS14 HEX SCHMITT-TRIGGER INVERTERS SDLS049B – DECEMBER 1983 – REVISED FEBRUARY 2002

# switching characteristics, $V_{CC} = 5 V$ , $T_A = 25^{\circ}C$ (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS			SN5414 SN7414		UNIT
	(INFOT)	(001-01)			MIN	TYP	MAX	
<sup>t</sup> PLH	А	Y	$R_1 = 400 \Omega$ ,	C <sub>1</sub> = 15 pF		15	22	ns
<sup>t</sup> PHL	~	I	11 = 400 32,	0 <u>[</u> = 10 pi		15	22	113

#### recommended operating conditions

		S	N54LS1	4	S	N74LS14	4	UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
ЮН	High-level output current			-0.4			-0.4	mA
IOL	Low-level output current			4			8	mA
Т <sub>А</sub>	Operating free-air temperature	-55		125	0		70	°C

# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETED	TEST CONDITIONS <sup>†</sup>			S	N54LS1	4	SN74LS14			UNIT
PARAMETER					TYP‡	MAX	MIN	TYP‡	MAX	UNIT
V <sub>T+</sub>	$V_{CC} = 5 V$			1.4	1.6	1.9	1.4	1.6	1.9	V
V <sub>T-</sub>	$V_{CC} = 5 V$			0.5	0.8	1	0.5	0.8	1	V
Hysteresis (V <sub>T+</sub> – V <sub>T–</sub> )	V <sub>CC</sub> = 5 V			0.4	0.8		0.4	0.8		V
VIK	$V_{CC} = MIN,$	lj = -18 mA				-1.5			-1.5	V
VOH	$V_{CC} = MIN,$	V <sub>I</sub> = 0.5 V,	I <sub>OH</sub> = -0.4 mA	2.5	3.4		2.7	3.4		V
Ve		Vj = -1.9 V	I <sub>OL</sub> = 4 mA		0.25	0.4		0.25	0.4	V
VOL	$V_{CC} = MIN,$	v]=-1.9 v	I <sub>OL</sub> = 8 mA					0.35	0.5	v
I <sub>T+</sub>	V <sub>CC</sub> = 5 V,	$V_I = V_{T+}$			-0.14			-0.14		mA
I <sub>T-</sub>	V <sub>CC</sub> = 5 V,	$V_I = V_{T-}$			-0.18			-0.18		mA
Ц	$V_{CC} = MAX,$	V <sub>I</sub> = 7 V				0.1			0.1	mA
IН	$V_{CC} = MAX,$	V <sub>IH</sub> = 2.7 V				20			20	μΑ
۱ <sub>IL</sub>	$V_{CC} = MAX,$	$V_{IL} = 0.4 V$				-0.4			-0.4	mA
los§	$V_{CC} = MAX$			-20		-100	-20		-100	mA
Іссн	$V_{CC} = MAX$				8.6	16		8.6	16	mA
ICCL	$V_{CC} = MAX$				12	21		12	21	mA

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

<sup>‡</sup> All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ .

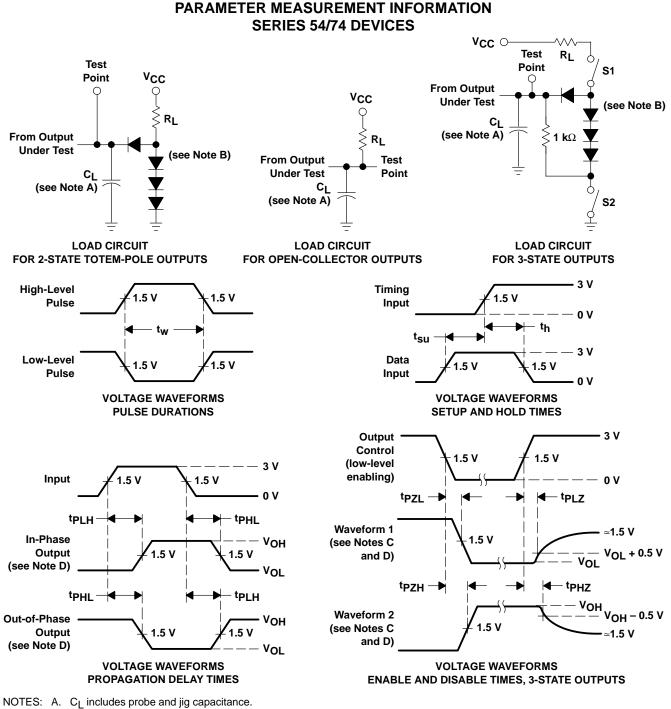
§ Not more than one output should be shorted at a time, and duration of the short-circuit should not exceed one second.

# switching characteristics, $V_{CC}$ = 5 V, $T_A$ = 25°C (see Figure 2)

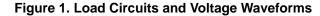
PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	ТҮР	МАХ	UNIT
<sup>t</sup> PLH	۵	v	$R_L = 2 k\Omega$ , $C_L = 15 pF$		15	22	ns
<sup>t</sup> PHL	n n		$N_{L} = 2 N_{22},  O_{L} = 10 \text{ pr}$		15	22	113



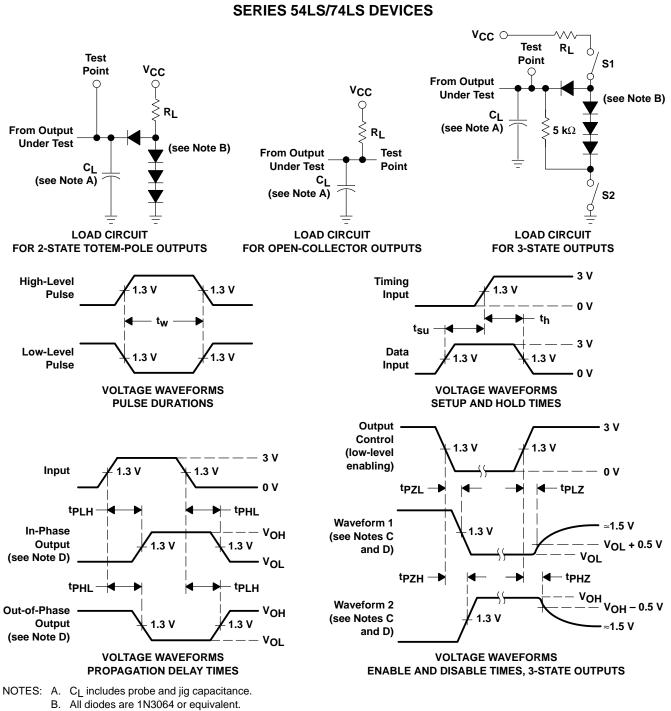
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- B. All diodes are 1N3064 or equivalent.
- C. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- D. S1 and S2 are closed for tpLH, tpHL, tpHZ, and tpLZ; S1 is open and S2 is closed for tpZH; S1 is closed and S2 is open for tpZL.
- E. All input pulses are supplied by generators having the following characteristics: PRR  $\leq$  1 MHz, Z<sub>O</sub>  $\approx$  50  $\Omega$ ; t<sub>r</sub> and t<sub>f</sub>  $\leq$  7 ns for Series
- 54/74 devices and  $t_r$  and  $t_f \le 2.5$  ns for Series 54S/74S devices.
- F. The outputs are measured one at a time with one input transition per measurement.

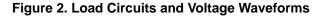






PARAMETER MEASUREMENT INFORMATION

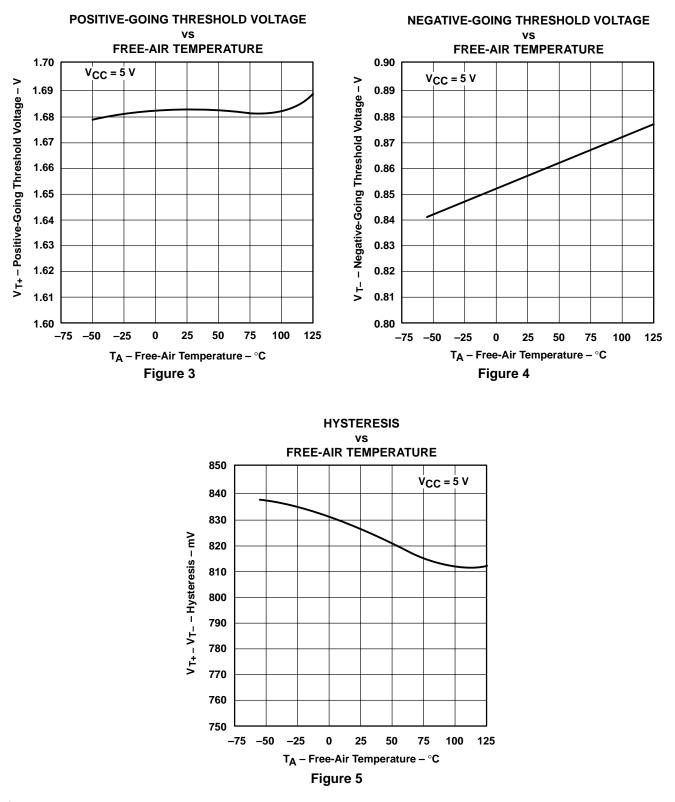
- C. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- D. S1 and S2 are closed for tpLH, tpHL, tpHZ, and tpLZ; S1 is open and S2 is closed for tpZH; S1 is closed and S2 is open for tpZL.
- E. Phase relationships between inputs and outputs have been chosen arbitrarily for these examples.
- F. All input pulses are supplied by generators having the following characteristics: PRR  $\leq$  1 MHz, Z<sub>O</sub>  $\approx$  50  $\Omega$ , t<sub>f</sub>  $\leq$  1.5 ns, t<sub>f</sub>  $\leq$  2.6 ns.
- G. The outputs are measured one at a time with one input transition per measurement.





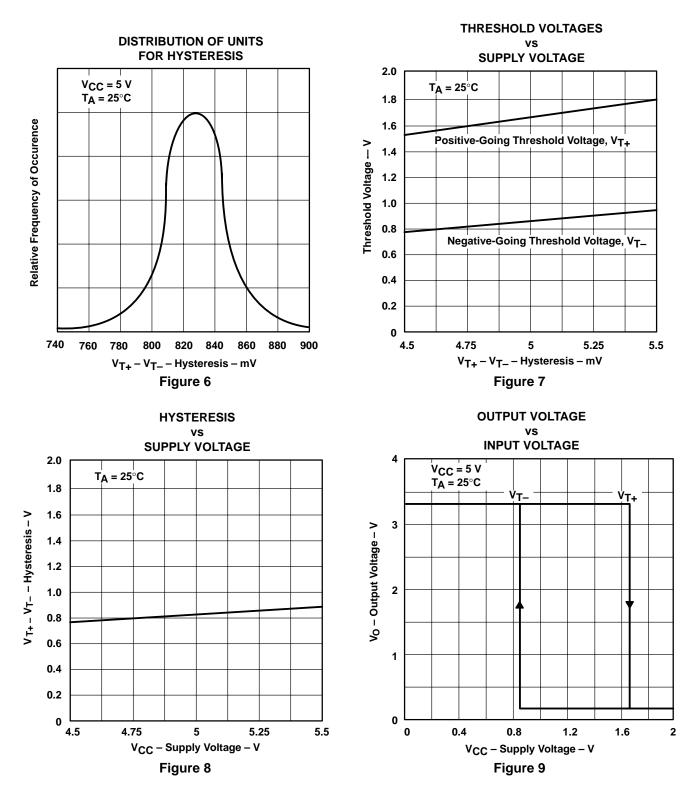
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# TYPICAL CHARACTERISTICS OF '14 CIRCUITS<sup>†</sup>





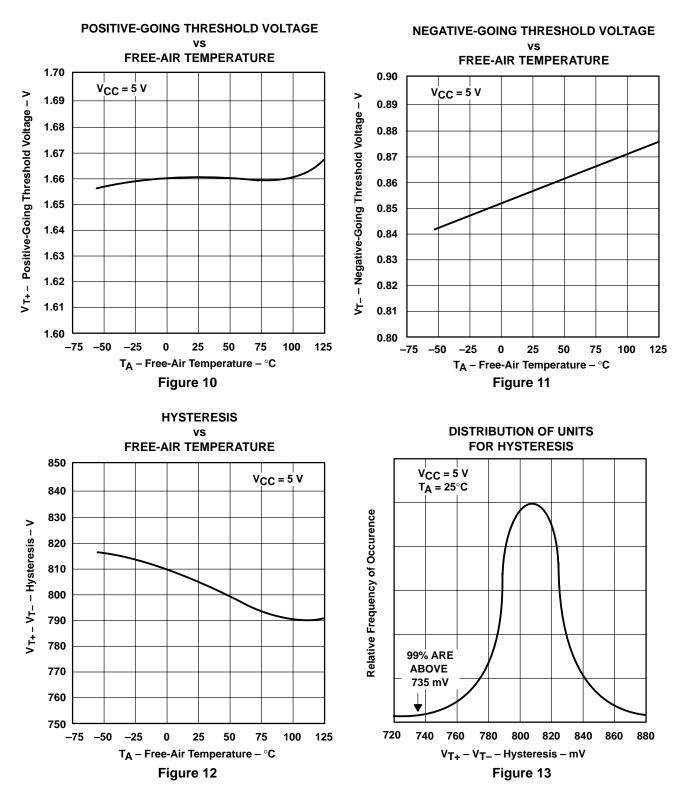
# TYPICAL CHARACTERISTICS OF '14 CIRCUITS<sup>†</sup>





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# TYPICAL CHARACTERISTICS OF 'LS14 CIRCUITS<sup>†</sup>





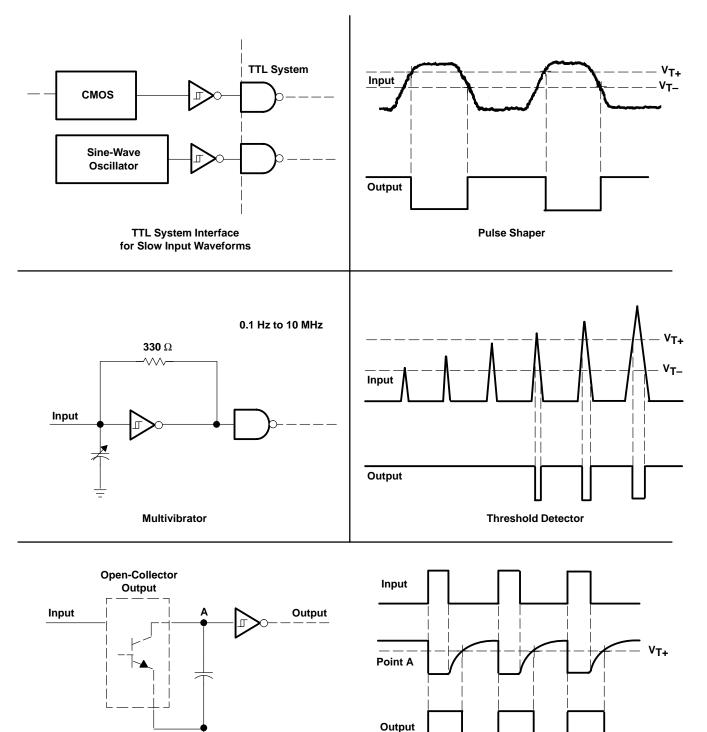
#### THRESHOLD VOLTAGES AND HYSTERESIS **OUTPUT VOLTAGE** vs vs SUPPLY VOLTAGE **INPUT VOLTAGE** 2.0 4 $V_{CC} = 5 V$ $T_A = 25^{\circ}C$ T<sub>A</sub> = 25°C 1.8 ν̈́τ– Vт+ 1.6 3 Positive-Going Threshold Voltage, VT+ V<sub>O</sub> – Output Voltage – V Threshold Voltage – V 1.4 1.2 Negative-Going Threshold Voltage, VT-1.0 2 0.8 Hysteresis, V<sub>T+</sub> – V<sub>T-</sub> 0.6 1 0.4 0.2 0 0 4.5 4.75 5 5.25 5.5 0 0.4 0.8 1.2 1.6 2 V<sub>CC</sub> – Supply Voltage – V VI – Input Voltage – V Figure 14 Figure 15

# **TYPICAL CHARACTERISTICS OF 'LS14 CIRCUITS<sup>†</sup>**



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# **TYPICAL APPLICATION DATA**



**Pulse Stretcher** 



TEXAS ISTRUMENTS www.ti.com

28-Feb-2005

## PACKAGING INFORMATION

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
5962-9665801Q2A	ACTIVE	LCCC	FK	20	1	None	Call TI	Level-NC-NC-NC
5962-9665801QCA	ACTIVE	CDIP	J	14	1	None	Call TI	Level-NC-NC-NC
5962-9665801QDA	ACTIVE	CFP	W	14	1	None	Call TI	Level-NC-NC-NC
5962-9665801VCA	ACTIVE	CDIP	J	14	1	None	Call TI	Level-NC-NC-NC
5962-9665801VDA	ACTIVE	CFP	W	14	1	None	Call TI	Level-NC-NC-NC
JM38510/31302BCA	ACTIVE	CDIP	J	14	1	None	Call TI	Level-NC-NC-NC
SN5414J	ACTIVE	CDIP	J	14	1	None	Call TI	Level-NC-NC-NC
SN54LS14J	ACTIVE	CDIP	J	14	1	None	Call TI	Level-NC-NC-NC
SN7414D	ACTIVE	SOIC	D	14	50	Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1 YEAR/ Level-1-235C-UNLIM
SN7414DR	ACTIVE	SOIC	D	14	2500	Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1 YEAR/ Level-1-235C-UNLIM
SN7414N	ACTIVE	PDIP	Ν	14	25	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
SN7414N3	OBSOLETE	PDIP	Ν	14		None	Call TI	Call TI
SN7414NSR	ACTIVE	SO	NS	14	2000	Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1 YEAR/ Level-1-235C-UNLIM
SN74LS14D	ACTIVE	SOIC	D	14	50	Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1 YEAR/ Level-1-235C-UNLIM
SN74LS14DBR	ACTIVE	SSOP	DB	14	2000	Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1 YEAR/ Level-1-235C-UNLIM
SN74LS14DR	ACTIVE	SOIC	D	14	2500	Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1 YEAR/ Level-1-235C-UNLIM
SN74LS14N	ACTIVE	PDIP	Ν	14	25	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
SN74LS14N3	OBSOLETE	PDIP	Ν	14		None	Call TI	Call TI
SNJ5414J	ACTIVE	CDIP	J	14	1	None	Call TI	Level-NC-NC-NC
SNJ5414W	ACTIVE	CFP	W	14	1	None	Call TI	Level-NC-NC-NC
SNJ54LS14FK	ACTIVE	LCCC	FK	20	1	None	Call TI	Level-NC-NC-NC
SNJ54LS14J	ACTIVE	CDIP	J	14	1	None	Call TI	Level-NC-NC-NC
SNJ54LS14W	ACTIVE	CFP	W	14	1	None	Call TI	Level-NC-NC-NC

<sup>(1)</sup> The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

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PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

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<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDECindustry standard classifications, and peak solder temperature.

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