# 74AHC1G07

# **Buffer with open-drain output**

Rev. 9 — 11 January 2022

**Product data sheet** 

## 1. General description

The 74AHC1G07 is a single buffer with open-drain output. Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

#### 2. Features and benefits

- Wide supply voltage range from 2.0 to 5.5 V
- Overvoltage tolerant inputs to 5.5 V
- · High noise immunity
- · CMOS low power dissipation
- Latch-up performance exceeds 100 mA per JESD 78 Class II Level A
- ESD protection:
  - HBM JESD22-A114E: exceeds 2000 V
  - MM JESD22-A115-A: exceeds 200 V
  - CDM JESD22-C101C: exceeds 1000 V
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

## 3. Ordering information

#### **Table 1. Ordering information**

| Type number | Package           |        |  |          |  |  |  |  |
|-------------|-------------------|--------|--|----------|--|--|--|--|
|             | Temperature range | Name   | Description  | Version  |  |  |  |  |
| 74AHC1G07GW | -40 °C to +125 °C | TSSOP5 | plastic thin shrink small outline package; 5 leads; body width 1.25 mm | SOT353-1 |  |  |  |  |
| 74AHC1G07GV | -40 °C to +125 °C | SC-74A | plastic surface-mounted package; 5 leads                               | SOT753   |  |  |  |  |

## 4. Marking

#### Table 2. Marking codes

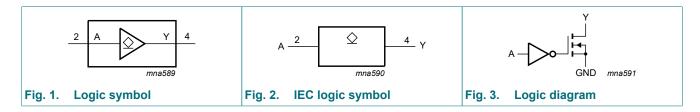
| Type number | Marking [1] |
|-------------|-------------|
| 74AHC1G07GW | AS          |
| 74AHC1G07GV | A07         |

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.



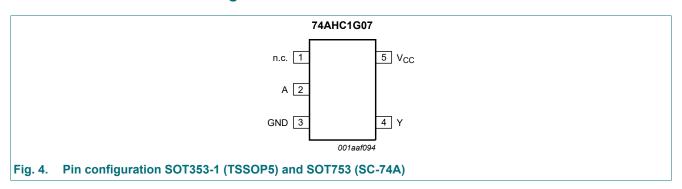
Buffer with open-drain output

## 5. Functional diagram



## 6. Pinning information

### 6.1. Pinning



### 6.2. Pin description

Table 3. Pin description

| Symbol          | Pin | Description    |  |  |  |  |  |
|-----------------|-----|----------------|--|--|--|--|--|
| n.c.            | 1   | not connected  |  |  |  |  |  |
| A               | 2   | data input     |  |  |  |  |  |
| GND             | 3   | ground (0 V)   |  |  |  |  |  |
| Υ               | 4   | data output    |  |  |  |  |  |
| V <sub>CC</sub> | 5   | supply voltage |  |  |  |  |  |

## 7. Functional description

#### **Table 4. Function table**

H = HIGH voltage level; L = LOW voltage level; Z = high-impedance OFF-state

| Input | Output |
|-------|--------|
| Α     | Υ      |
| L     | L      |
| Н     | Z      |

Buffer with open-drain output

## 8. Limiting values

#### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol           | Parameter               | Conditions                           |     | Min  | Max  | Unit |
|------------------|-------------------------|--------------------------------------|-----|------|------|------|
| $V_{CC}$         | supply voltage          |                                      |     | -0.5 | +7.0 | V    |
| VI               | input voltage           |                                      |     | -0.5 | +7.0 | V    |
| I <sub>IK</sub>  | input clamping current  | V <sub>I</sub> < -0.5 V              |     | -20  | -    | mA   |
| I <sub>OK</sub>  | output clamping current | V <sub>O</sub> < -0.5 V              | [1] | -    | ±20  | mA   |
| Io               | output current          | V <sub>O</sub> > -0.5 V              |     | -    | ±25  | mA   |
| Vo               | output voltage          | active mode                          | [1] | -0.5 | +7.0 | V    |
|                  |                         | high-impedance mode                  | [1] | -0.5 | +7.0 | V    |
| I <sub>CC</sub>  | supply current          |                                      |     | -    | 75   | mA   |
| $I_{GND}$        | ground current          |                                      |     | -75  | -    | mA   |
| T <sub>stg</sub> | storage temperature     |                                      |     | -65  | +150 | °C   |
| P <sub>tot</sub> | total power dissipation | T <sub>amb</sub> = -40 °C to +125 °C | [2] | -    | 250  | mW   |

<sup>[1]</sup> The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

## 9. Recommended operating conditions

#### Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

| Symbol           | Parameter                           | Conditions                                 | Min | Тур | Max             | Unit |
|------------------|-------------------------------------|--|-----|-----|-----------------|------|
| V <sub>CC</sub>  | supply voltage                      |  | 2.0 | 5.0 | 5.5             | V    |
| VI               | input voltage                       |  | 0   | -   | 5.5             | V    |
| Vo               | output voltage                      | active mode                                | 0   | -   | V <sub>CC</sub> | V    |
|                  |                                     | high-impedance mode                        | 0   | -   | 6.0             | V    |
| T <sub>amb</sub> | ambient temperature                 |  | -40 | +25 | +125            | °C   |
| Δt/ΔV            | input transition rise and fall rate | V <sub>CC</sub> = 3.3 V ± 0.3 V            | -   | -   | 100             | ns/V |
|                  |                                     | $V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$ | -   | -   | 20              | ns/V |

<sup>[2]</sup> For SOT353-1 (TSSOP5) package: P<sub>tot</sub> derates linearly with 3.3 mW/K above 74 °C. For SOT753 (SC-74A) package: P<sub>tot</sub> derates linearly with 3.8 mW/K above 85 °C.

Buffer with open-drain output

## 10. Static characteristics

#### **Table 7. Static characteristics**

Voltages are referenced to GND (ground = 0 V).

| Symbol          | Parameter                   | Conditions  |      | 25 °C |       |      | °C to<br>5 °C | -40 °C to<br>+125 °C |       | Unit |
|-----------------|-----------------------------|---|------|-------|-------|------|---------------|----------------------|-------|------|
|                 |                             |   | Min  | Тур   | Max   | Min  | Max           | Min                  | Max   |      |
| V <sub>IH</sub> | HIGH-level                  | V <sub>CC</sub> = 2.0 V   | 1.5  | -     | -     | 1.5  | -             | 1.5                  | -     | V    |
|                 | input voltage               | V <sub>CC</sub> = 3.0 V   | 2.1  | -     | -     | 2.1  | -             | 2.1                  | -     | V    |
|                 |                             | V <sub>CC</sub> = 5.5 V   | 3.85 | -     | -     | 3.85 | -             | 3.85                 | -     | V    |
| V <sub>IL</sub> | LOW-level                   | V <sub>CC</sub> = 2.0 V   | -    | -     | 0.5   | -    | 0.5           | -                    | 0.5   | V    |
|                 | input voltage               | V <sub>CC</sub> = 3.0 V   | -    | -     | 0.9   | -    | 0.9           | -                    | 0.9   | V    |
|                 |                             | V <sub>CC</sub> = 5.5 V   | -    | -     | 1.65  | -    | 1.65          | -                    | 1.65  | V    |
| V <sub>OL</sub> | LOW-level<br>output voltage | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>                     |      |       |       |      |               |                      |       |      |
|                 |                             | I <sub>O</sub> = 50 μA; V <sub>CC</sub> = 2.0 V                         | -    | 0     | 0.1   | -    | 0.1           | -                    | 0.1   | V    |
|                 |                             | I <sub>O</sub> = 50 μA; V <sub>CC</sub> = 3.0 V                         | -    | 0     | 0.1   | -    | 0.1           | -                    | 0.1   | V    |
|                 |                             | I <sub>O</sub> = 50 μA; V <sub>CC</sub> = 4.5 V                         | -    | 0     | 0.1   | -    | 0.1           | -                    | 0.1   | V    |
|                 |                             | I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V                        | -    | -     | 0.36  | -    | 0.44          | -                    | 0.55  | V    |
|                 |                             | $I_O = 8.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$                          | -    | -     | 0.36  | -    | 0.44          | -                    | 0.55  | V    |
| I <sub>I</sub>  | input leakage<br>current    | V <sub>I</sub> = 5.5 V or GND;<br>V <sub>CC</sub> = 0 V to 5.5 V        | -    | -     | 0.1   | -    | 1.0           | -                    | 2.0   | μA   |
| I <sub>OZ</sub> | OFF-state output current    | $V_I = V_{IH}$ or $V_{IL}$ ;<br>$V_O = V_{CC}$ or GND; $V_{CC} = 5.5$ V | -    | -     | ±0.25 |      | ±2.5          |                      | ±10.0 | μΑ   |
| I <sub>CC</sub> | supply current              | $V_I = V_{CC}$ or GND; $I_O = 0$ A;<br>$V_{CC} = 5.5 \text{ V}$         | -    | -     | 1.0   | -    | 10            | -                    | 20    | μΑ   |
| C <sub>I</sub>  | input<br>capacitance        |   | -    | 1.5   | 10    | -    | 10            | -                    | 10    | pF   |

## 11. Dynamic characteristics

#### **Table 8. Dynamic characteristics**

 $GND = 0 \ V; \ t_r = t_f = \le 3.0 \ ns. \ For test circuit see Fig. 6.$ 

| Symbol                            | Parameter         | Conditions   |    | 25 °C |     | -40 °C to<br>+85 °C |     | -40 °C to<br>+125 °C |     | Unit |    |
|-----------------------------------|-------------------|--|----|-------|-----|---------------------|-----|----------------------|-----|------|----|
|                                   |                   |  | Mi | n T   | ӯр  | Max                 | Min | Max                  | Min | Max  |    |
| t <sub>PZL</sub> OFF-state to LOW | ,                 |  |    |       |     |                     |     |                      |     |      |    |
|                                   | propagation delay | $V_{CC}$ = 3.0 V to 3.6 V; $C_L$ = 15 pF [1]                         | -  | 3     | 3.5 | 5.6                 | 1.0 | 6.3                  | 1.0 | 7.0  | ns |
|                                   |                   | $V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}; C_L = 50 \text{ pF}$ [1]  | -  | 5     | 5.0 | 8.0                 | 1.0 | 9.0                  | 1.0 | 10.0 | ns |
|                                   |                   | $V_{CC}$ = 4.5 V to 5.5 V; $C_L$ = 15 pF [2]                         | -  | 2     | 2.5 | 3.9                 | 1.0 | 4.6                  | 1.0 | 4.9  | ns |
|                                   |                   | $V_{CC} = 4.5 \text{ V to } 5.5 \text{ V; } C_L = 50 \text{ pF}$ [2] | -  | 3     | 3.6 | 5.5                 | 1.0 | 6.5                  | 1.0 | 7.0  | ns |

#### Buffer with open-drain output

| Symbol  | Parameter  | Conditions   |     | 25 °C |      | -40 °C to<br>+85 °C |      | -40 °C to<br>+125 °C |      | Unit |
|---|--|--|-----|-------|------|---------------------|------|----------------------|------|------|
|   |  |  | Min | Тур   | Max  | Min                 | Max  | Min                  | Max  |      |
| t <sub>PLZ</sub> LOW to OFF-state propagation delay | A to Y; see Fig. 5   |  |     |       |      |                     |      |                      |      |      |
|   | $V_{CC} = 3.0 \text{ V to } 3.6 \text{ V; } C_L = 15 \text{ pF}$ [1] | -  | 5.8 | 7.9   | 1.0  | 8.4                 | 1.0  | 8.9                  | ns   |      |
|   |  | $V_{CC} = 3.0 \text{ V to } 3.6 \text{ V; } C_L = 50 \text{ pF}$ [1] | -   | 8.3   | 11.5 | 1.0                 | 12.0 | 1.0                  | 12.5 | ns   |
|   |  | $V_{CC}$ = 4.5 V to 5.5 V; $C_L$ = 15 pF [2]                         | -   | 4.2   | 5.1  | 1.0                 | 5.6  | 1.0                  | 6.1  | ns   |
|   |  | $V_{CC} = 4.5 \text{ V to } 5.5 \text{ V; } C_L = 50 \text{ pF}$ [2] | -   | 6.0   | 7.5  | 1.0                 | 8.0  | 1.0                  | 8.5  | ns   |
| C <sub>PD</sub>                                     | power dissipation capacitance  | per buffer; $C_L$ = 50 pF; f = 1 MHz; [3]<br>$V_I$ = GND to $V_{CC}$ | -   | 5     | -    | -                   | -    | -                    | -    | pF   |

- Typical values are measured at  $V_{CC}$  = 3.3 V.
- Typical values are measured at  $V_{CC} = 5.0 \text{ V}$ .
- $C_{PD}$  is used to determine the dynamic power dissipation  $P_D$  ( $\mu$ W).  $P_D = C_{PD} \times V_{CC}^2 \times f_i + \Sigma (C_L \times V_{CC}^2 \times f_o)$  where:

 $f_i$  = input frequency in MHz;

f<sub>o</sub> = output frequency in MHz;

C<sub>L</sub> = output load capacitance in pF;

V<sub>CC</sub> = supply voltage in Volts

#### 11.1. Waveforms and test circuit

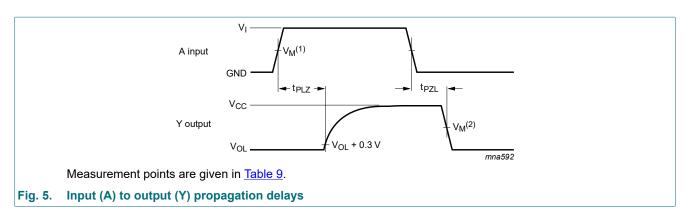
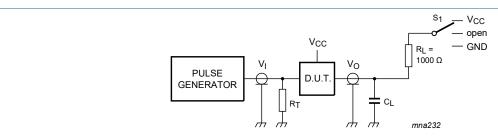


Table 9. Measurement point

| Input                  |                               | Output                |
|------------------------|-------------------------------|-----------------------|
| V <sub>I</sub>         | V <sub>M</sub> <sup>(1)</sup> | $V_{M}^{(2)}$         |
| GND to V <sub>CC</sub> | 0.5 × V <sub>CC</sub>         | 0.5 × V <sub>CC</sub> |



Test data is given in Table 8.

Definitions for test circuit:

 $C_L$  = Load capacitance including jig and probe capacitance.

 $R_T$  = Termination resistance should be equal to output impedance  $Z_0$  of the pulse generator.

For  $t_{PLZ}$ ,  $t_{PZL}$ ,  $S_1 = V_{CC}$ 

#### Test circuit for measuring switching times Fig. 6.

#### Buffer with open-drain output

## 12. Package outline

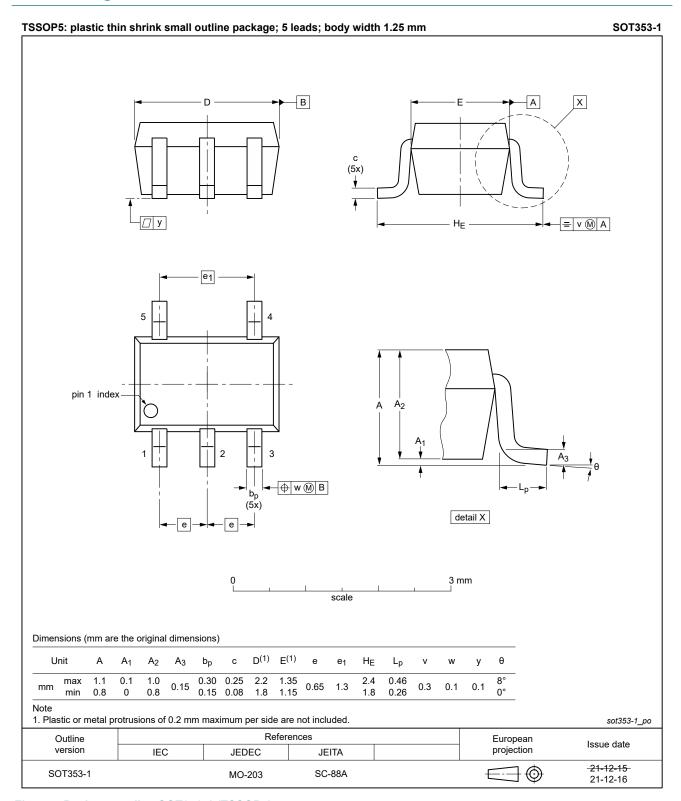


Fig. 7. Package outline SOT353-1 (TSSOP5)

#### Buffer with open-drain output

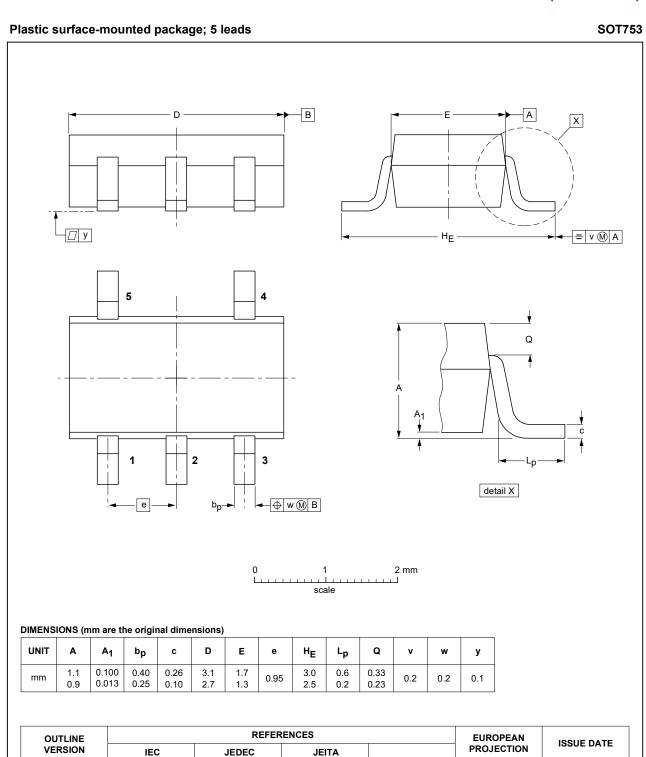


Fig. 8. Package outline SOT753 (SC-74A)

SOT753

**Product data sheet** 

SC-74A

02-04-16

06-03-16

 $\bigoplus \bigoplus$ 

### Buffer with open-drain output

## 13. Abbreviations

#### **Table 10. Abbreviations**

| Acronym | Description                             |
|---------|---|
| CDM     | Charged Device Model                    |
| CMOS    | Complementary Metal-Oxide Semiconductor |
| DUT     | Device Under Test                       |
| ESD     | ElectroStatic Discharge                 |
| НВМ     | Human Body Model                        |
| MM      | Machine Model                           |

# 14. Revision history

### Table 11. Revision history

| Document ID        | Release date   | Data sheet status  | Change notice                                | Supersedes           |  |  |  |  |  |
|--------------------|--|--|--|----------------------|--|--|--|--|--|
| 74AHC1G07 v.9      | 20220111   | Product data sheet   | -  | 74AHC1G07 v.8        |  |  |  |  |  |
| Modifications:     | • <u>Section 8</u> : De  | Section 1 and Section 2 updated.  Section 8: Derating values for P <sub>tot</sub> total power dissipation updated.  Fig. 7: Package outline drawing SOT353-1 (TSSOP5) updated. |  |                      |  |  |  |  |  |
| 74AHC1G07 v.8      | 20190225   | Product data sheet   | -  | 74AHC_AHCT1G07 v.7   |  |  |  |  |  |
| Modifications:     | <ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Type numbers 74AHCT1G07GW (SOT353-1) and 74AHCT1G07GV (SOT753) removed.</li> </ul> |  |  |                      |  |  |  |  |  |
| 74AHC_AHCT1G07 v.7 | 20141118   | Product data sheet   | -  | 74AHC_AHCT1G07 v.6   |  |  |  |  |  |
| Modifications:     | Section 4: tal   | ole note added.  |  |                      |  |  |  |  |  |
| 74AHC_AHCT1G07 v.6 | 20070607   | Product data sheet   | -  | 74AHC_AHCT1G07 v.5   |  |  |  |  |  |
| Modifications:     | guidelines of Legal texts h Package SO   | f this data sheet has been in NXP Semiconductors. ave been adapted to the new T353 changed to SOT353-ince data and Soldering sec   | ew company name<br>1 in <u>Section 3</u> and | e where appropriate. |  |  |  |  |  |
| 74AHC_AHCT1G07 v.5 | 20021002   | Product specification  | -  | 74AHC_AHCT1G07 v.4   |  |  |  |  |  |
| 74AHC_AHCT1G07 v.4 | 20020606   | Product specification  | -  | 74AHC_AHCT1G07 v.3   |  |  |  |  |  |
| 74AHC_AHCT1G07 v.3 | 20020221   | Product specification  | -  | 74AHC_AHCT1G07 v.2   |  |  |  |  |  |
| 74AHC_AHCT1G07 v.2 | 20010209   | Product specification  | -  | 74AHC_AHCT1G07 v.1   |  |  |  |  |  |
| 74AHC_AHCT1G07 v.1 | 20000502   | Product specification  | -  | -                    |  |  |  |  |  |

#### Buffer with open-drain output

### 15. Legal information

#### **Data sheet status**

| Document status [1][2]         | Product<br>status [3] | Definition  |
|--------------------------------|-----------------------|---|
| Objective [short] data sheet   | Development           | This document contains data from the objective specification for product development. |
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### Buffer with open-drain output

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