



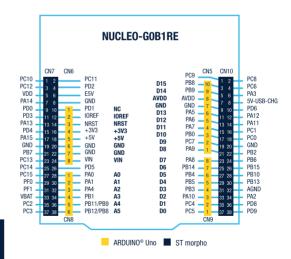
## Nucleo-64



#### STM32G0B1RET6 64 PINS

- Arm® Cortex®-M0+ core at 64 MHz
- 512 Kbytes of Flash memory, 144 Kbytes of SRAM
- Two extension types:
  - ARDUINO® Uno V3 connectivity
  - ST morpho extension pin header footprints for full access to all STM32 I/Os
  - Embedded ST-LINK/V2-1 debugger/programmer

By using or installing (as applicable) this evaluation kit you accept all the terms of the EVALUATION LICENCE AGREEMENT available at: www.st.com/epla



ST part number: NUCLEO-G0B1RE

www.st.com/stm32nucleo



# STM32 Nucleo-64 for STM32G0B1 mainstream MCUs **GETTING STARTED**

- 1/ Check the jumper positions on the board: CN4 (STLK) on [1-2], [3-4], JP2 (PWR) on [1-2], JP3 (IDD) on.
- 2/ For a correct identification of all device interfaces from the host PC, install the Nucleo USB driver available from the www.st.com/stm32nucleo webpage, prior to connecting the board.
- 3/ To power the board, connect the STM32 NUCLEO-GOB1RE to a PC with a USB cable Type-A to Micro-B through USB connector CN2. As a result green LED LD3 (PWR) lights up. LD1 (COM) blinks.
- 4/ Press user button B1 (blue).
- 5/ Observe that the blinking frequency of the green LED LD4 changes, by clicking on the button B1.
- 6/ The demo software and several software examples that allow you to use the STM32 Nucleo features are available at www.st.com/en/product/nucleo-q0b1re

#### SYSTEM REQUIREMENTS

- Windows® OS (7, 8 and 10), Linux® 64-bit or macOS®
- USB Type-A to USB Type-A or USB Type-C® to Micro-B cable

### DEVELOPMENT TOOL CHAINS

- IAR Systems IAR Embedded Workbench®1
- Keil® MDK-ARM¹
- STMicroelectronics STM32CubeIDF

On Windows<sup>®</sup> only

## **EMBEDDED SOFTWARE**

STM32CubeG0 MCU Package featuring drivers, RTOS, file system and examples for this board.





This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation

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