MPX10 Series

10 kPa Uncompensated Pressure Sensors

Rev. 15 — 22 April 2021

Product data sheet

1 General Description

The MPX10 series device is a silicon piezoresistive pressure sensor providing a very accurate and linear voltage output directly proportional to the applied pressure. This standard, low cost, uncompensated sensor permits manufacturers to design and add their own external temperature compensation and signal conditioning networks. Compensation techniques are simplified because of the predictability of NXP's single element strain gauge design.

2 Features and Benefits

- Low Cost
- Patented Silicon Shear Stress Strain Gauge Design
- · Ratiometric to Supply Voltage
- · Differential and Gauge Options
- Durable Epoxy Unibody Element or Thermoplastic (PPS) Surface Mount Package

3 Applications

- Air Movement Control
- Environmental Control Systems
- Level Indicators
- Leak Detection
- · Medical Diagnostics
- Industrial Controls
- · Pneumatic Control Systems
- Robotics



4 Ordering Information

Table 1. Ordering information

Device Name	Package	Case number	Number of ports			Pressure type			Device marking
	options		None	Single	Dual	Gauge	Differential	Absolute	
Small outline pack	age (MPX10 S	Series)					,		
MPXV10GC6U	Rail	<u>482A</u>		•		•			MPXV10G
Unibody package	(MPX10 Serie	es)						1	,
MPX10D	Tray	344	•				•		MPX10D
MPX10DP	Tray	344C			•		•		MPX10DP
MPX10GP	Tray	<u>344B</u>		•		•			MPX10GP

Small outline package



MPXV10GC6U Case 482A-01

Unibody packages



MPX10D Case 344-15



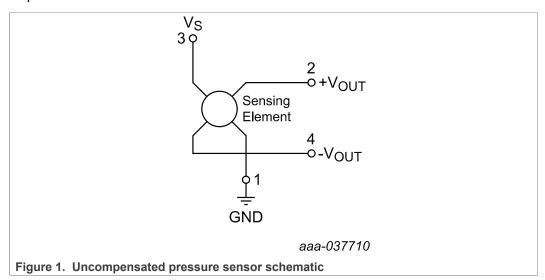
MPX10DP Case 344C-01



MPX10GP Case 344B-01

5 Block Diagram

<u>Figure 1</u> shows a schematic of the internal circuitry on the stand-alone pressure sensor chip.



6 Pin Information

6.1 MPXV10GC6U

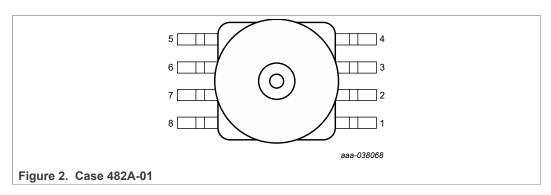


Table 2. Pin definitions - MPXV10GC6U

Symbol	Pin	Description
GND	1	Ground
+V _{OUT}	2	+Voltage output
Vs	3	Power supply
-V _{OUT}	4	-Voltage output
n.c.	5	Not connected
n.c.	6	Not connected
n.c.	7	Not connected
n.c.	8	Not connected

6.2 MPX10D

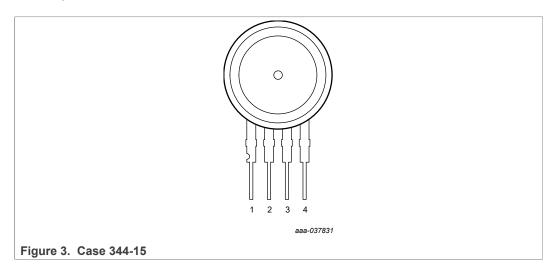


Table 3. Pin definitions - MPX10D

Symbol	Pin	Description
GND	1	Ground
+V _{OUT}	2	+ Voltage output
Vs	3	Power supply
-V _{OUT}	4	- Voltage output

6.3 MPX10DP

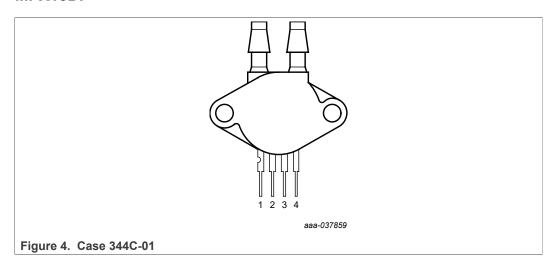


Table 4. Pin definitions - MPX10DP

Symbol	Pin	Description
GND	1	Ground
+V _{OUT}	2	+ Voltage output
V _S	3	Power supply
-V _{OUT}	4	- Voltage output

6.4 MPX10GP

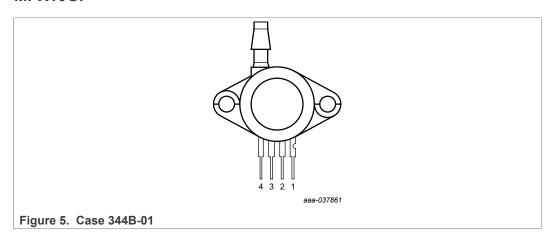


Table 5. Pin definitions - MPX10GP

Symbol	Pin	Description
GND	1	Ground
+V _{OUT}	2	+ Voltage output
Vs	3	Power supply
-V _{OUT}	4	- Voltage output

7 Maximum Ratings

Table 6. Maximum ratings

Exposure beyond the specified limits may cause permanent damage or degradation to the device. In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
P _{max}	Overpressure	P1 > P2	_	_	75	kPa
P _{burst}	Burst Pressure	P1 > P2	_	_	100	kPa
T _{stg}	Storage Temperature		-40	_	+125	°C
T _A	Operating Temperature		-40	_	+125	°C

8 Operating Characteristics

Table 7. Operating Characteristics ($V_S = 3.0 \text{ Vdc}$, $T_A = 25 \,^{\circ}\text{C}$ unless otherwise noted, P1 > P2)

Characteristic		Symbol	Min	Тур	Max	Unit
Operating Pressure Range	[1]	P _{OP}	0	_	10	kPa
Supply Voltage	[2]	Vs	_	3.0	6.0	V _{DC}
Supply Current		lo	_	6.0	_	mAdc
Full Scale Span	[3]	V_{FSS}	20	35	50	mV
Offset	[4]	V_{off}	0	20	35	mV
Sensitivity		ΔV/ΔΡ	_	3.5	_	mV/kPa
Linearity	[5]	_	-1.0	_	1.0	%V _{FSS}
Pressure Hysteresis (0 kPa to 10 kPa)	[5]	_	_	±0.1	_	%V _{FSS}
Temperature Hysteresis (-40 °C to +125 °C)	[5]	_	_	±0.5	_	%V _{FSS}
Temperature Coefficient of Full Scale Span	[5]	TCV _{FSS}	-0.22	_	-0.16	%V _{FSS} /°C
Temperature Coefficient of Offset	[5]	TCV _{off}	_	±15	_	μV/°C
Temperature Coefficient of Resistance	[5]	TCR	0.21	_	0.27	%Z _{in} /°C
Input Impedance		Z _{in}	400	_	550	Ω
Output Impedance		Z _{out}	750	_	1250	Ω
Response Time (10% to 90%)	[6]	t _R	_	1.0	_	ms
Warm-Up Time	[7]	_	_	20	_	ms
Offset Stability	[8]	_	_	±0.5	_	%V _{FSS}

- [1] 1.0 kPa equals 0.145 PSI.
- [2] Device is ratiometric within this specified excitation range. Operating the device above the specified excitation range may induce additional error due to device self-heating.
- [3] Full Scale Span (V_{FSS}) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure
- [4] Offset (V_{off}) is defined as the output voltage at the minimum rated pressure.
- [5] Accuracy (error budget) consists of the following:
 - · Linearity: Output deviation from a straight line relationship with pressure, using the end point method, over the specified pressure range.
 - Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to and from the minimum or maximum operating temperature points, with zero differential pressure applied.
 - Pressure Hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from the minimum or maximum rated pressure, at 25 °C.
 - TcSpan: Output deviation at full rated pressure over the temperature range of 0 °C to 85 °C, relative to 25 °C
 - TcOffset: Output deviation with minimum rated pressure applied, over the temperature range of 0 °C to 85 °C, relative to 25 °C
 - TCR: Zin deviation with minimum rated pressure applied, over the temperature range of -40 °C to +125 °C, relative to 25 °C
- [6] Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.
- [7] Warm-Up Time is defined as the time required for the product to meet the specified output voltage after the pressure has been stabilized.
- [8] Offset Stability is the product's output deviation when subjected to 1000 hours of Pulsed Pressure Temperature Cycling with Bias test.

9 Characteristics

9.1 Voltage output versus applied differential pressure

The output voltage of the differential or gauge sensor increases with increasing pressure applied to the pressure side (P1) relative to the vacuum side (P2). Similarly, output voltage increases as increasing vacuum is applied to the vacuum side (P2) relative to the pressure side (P1).

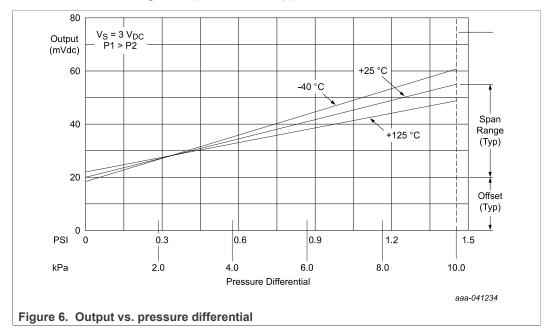
9.2 Temperature compensation

Figure 6 shows the typical output characteristics of the MPX10 series over temperature.

Because this strain gauge is an integral part of the silicon diaphragm, there are no temperature effects due to differences in the thermal expansion of the strain gauge and the diaphragm, as are often encountered in bonded strain gauge pressure sensors. However, the properties of the strain gauge itself are temperature dependent, requiring that the device be temperature compensated if it is to be used over an extensive temperature range.

Temperature compensation and offset calibration can be achieved rather simply with additional resistive components, or by designing your system using the MPX2010 series sensor.

Several approaches to external temperature compensation over both -40 °C to +125 °C and 0 °C to +80 °C ranges are presented in Application Note AN840



9.3 Linearity

Linearity refers to how well a transducer's output follows the equation $V_{out} = V_{off} + Sensitivity \times P$ over the operating pressure range (Figure 7). There are two basic methods for calculating nonlinearity:

• End point straight line fit

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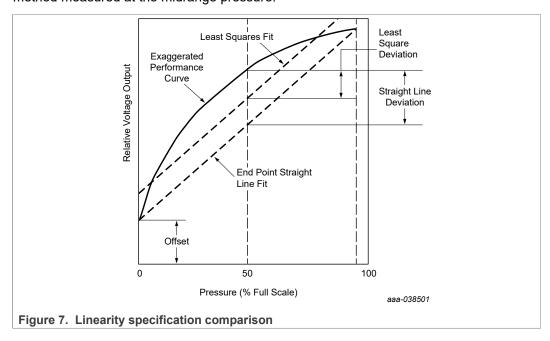
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· Least squares best line fit

While a least squares fit gives the "best case" linearity error (lower numerical value), the calculations required are burdensome.

Conversely, an end point fit will give the "worst case" error (often more desirable in error budget calculations) and the calculations are more straightforward for the user.

NXP's specified pressure sensor linearities are based on the end point straight line method measured at the midrange pressure.



9.4 Pressure (P1) / Vacuum (P2) side identification

NXP designates the two sides of the pressure sensor as the Pressure (P1) side and the Vacuum (P2) side. The Pressure (P1) side is the side containing silicone gel that isolates the die from the environment. The NXP MPX pressure sensor is designed to operate with positive differential pressure applied, P1 > P2.

The Pressure (P1) side may be identified by using Table 8.

Table 8. Pressure (P1) side delineation table

Part Number	Case Type	Pressure (P1) Side Identifier
MPX10D	344	Stainless Steel Cap
MPX10DP	344C	Side with Part Marking
MPX10GP	344B	Side with Port Attached
MPXV10GC6U	482A	Side with Port Attached

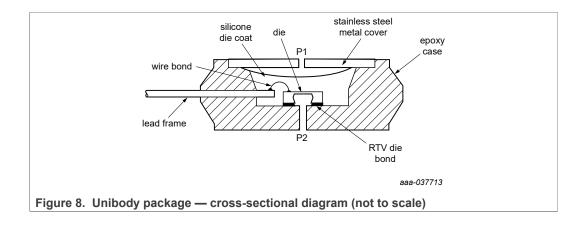
9.5 Media compatibility

<u>Figure 8</u> illustrates the differential or gauge configuration in a typical chip carrier. A silicone gel isolates the die surface and wire bonds from the environment while allowing the pressure signal to be transmitted to the silicon diaphragm.

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The MPX10 series pressure sensor operating characteristics, internal reliability and qualification tests are based on the use of dry clean air as the pressure medium. Media other than dry clean air may have adverse effects on sensor performance and long term reliability. Contact the factory for information regarding media compatibility in your application.

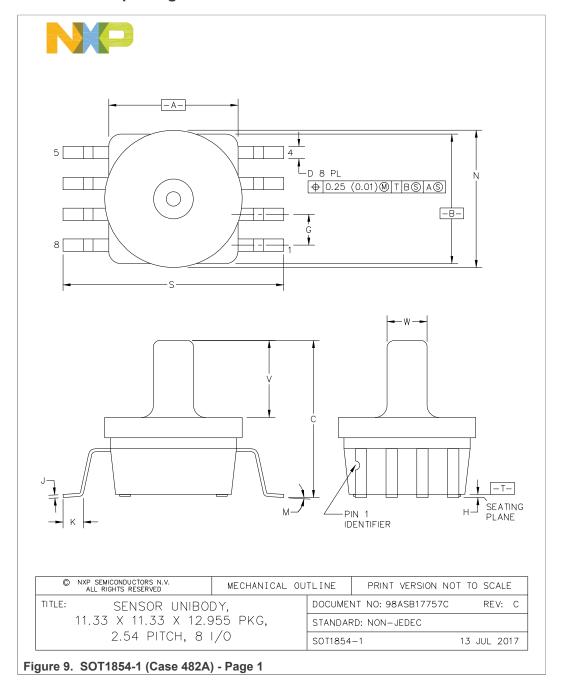
For more information, refer to application note AN3728.



10 Package Outlines

Package dimensions are provided in package drawings. To find the most current package outline drawing, go to https://www.nxp.com/ and perform a keyword search for the drawing's document number.

10.1 Small outline packages





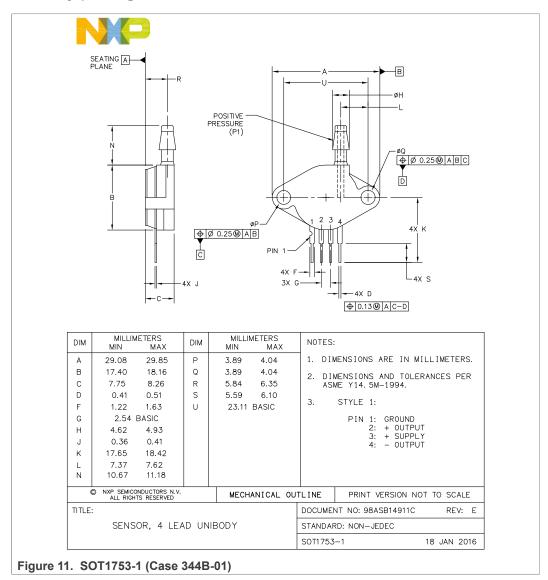
NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M-1982.
- 2. CONTROLLING DIMENSION: INCH.
- 3. DIMENSION 'A' AND 'B' DO NOT INCLUDE MOLD PROTUSION.
- 4. MAXIMUM MOLD PROTRUSION 0.15 (0.006).
- 5. ALL VERTICAL SURFACES 5' TYPICAL DRAFT.

	INC	HES	MILLI	METERS			
DIM	MIN	MAX	MIN	MAX			
Α	0. 415	0. 425	10. 54	10. 79			
В	0. 415	0.425	10. 54	10. 79			
С	0. 500	0. 520	12. 70	13. 21			
D	0. 038	0.042	0. 96	1. 07			
G	0. 100	BSC	2. 54	BSC			
Н	0. 002	0.010	0.05	0.25			
J	0.009	0.011	0. 23	0. 28			
K	0.061	0.071	1. 55	1.80			
М	0°	7°	0°	7°			
N	0. 444	0.448	11. 28	11. 38			
S	0. 709	0. 725	18. 01	18.41			
V	0. 245	0. 255	6. 22	6. 48			
w	0. 115	0. 125	2. 92	3. 17			
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Figure 10. SOT1854-1 (Case 482A) - Page 2

10.2 Unibody packages



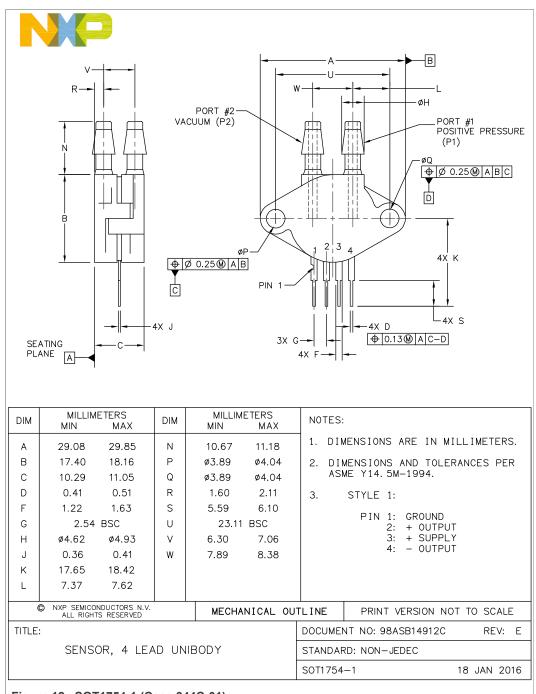
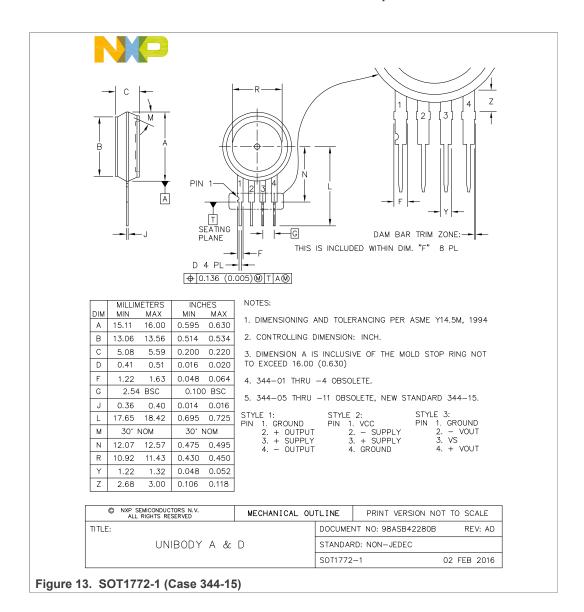


Figure 12. SOT1754-1 (Case 344C-01)



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11 References

- [1] AN840 Temperature Compensation Methods For The Motorola X-ducer Pressure Sensor Element https://www.nxp.com/docs/en/application-note/AN840.pdf
- [2] AN1984 Handling Freescale Pressure Sensors https://www.nxp.com/docs/en/application-note/AN1984.pdf
- [3] AN3150 Soldering Recommendations for Pressure Sensor Devices https://www.nxp.com/docs/en/application-note/AN3150.pdf
- [4] AN1318 Interfacing Semiconductor Pressure Sensors to Microcomputers https://www.nxp.com/docs/en/application-note/AN1318.pdf
- [5] AN3728 Media Compatibility for IPS PRT Pressure Sensors https://www.nxp.com/docs/en/application-note/AN3728.pdf

12 Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
MPX10 v.15	20210422	Product data sheet	-	MPX10 v.14		
Modifications	 Redesigned the data sheet to comply with the new identity guidelines of NXP Semiconductors. Adapted legal texts to the new company name where appropriate. Removed the following discontinued part numbers throughout: MPXV10GC7U 					
MPX10 v.14	200810	Product data sheet	-	MPX10 v.13		

13 Legal information

13.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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