

Rev. V2

Features

High Gain: 27.5 dBP1dB: 35.5 dBmP3dB: 36.0 dBm

IM3 Level: -35 dBc @ Pout = +23 dBm/tone
 Power Added Efficiency: 28% @ P3dB

Temperature Compensated Output Power Detector

Lead-Free 5 mm AQFN 32-lead Package

• RoHS* Compliant

Applications

- Point-to-Point
- VSAT

Description

The MAAP-011317 is a 4 W, 4-stage power amplifier assembled in a lead-free 5 mm 32-lead air cavity QFN plastic package. This power amplifier operates from 27.5 to 31.0 GHz and provides 27.5 dB of linear gain, 4 W saturated output power and 28% efficiency while biased at 6 V.

The MAAP-011317 can be used as a power amplifier stage or as a driver stage in higher power applications.

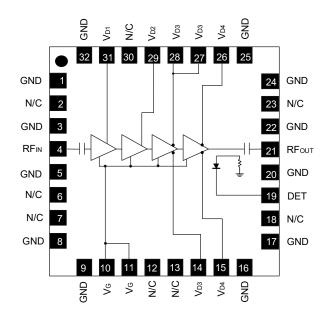
This product is fabricated using a GaAs pHEMT process which features full passivation for enhanced reliability.

Ordering Information¹

Part Number	Package	
MAAP-011317	Bulk part	
MAAP-011317-TR0500	500 part reel	
MAAP-011317-001SMB	Sample Board	

1. Reference Application Note M513 for reel size information.

Functional Schematic



Pin Configuration^{2,3}

Pin #	Pin Name	Description
1, 3, 5, 8, 9, 16, 17, 20, 22, 24, 25, 32	GND	Ground
2, 6, 7, 12, 13, 18, 23, 30	N/C	No Connection
4	RF _{IN}	RF Input
10, 11	V_{G}	Gate Voltage
14, 27, 28	V_{D3}	Drain Voltage 3
15, 26	V_{D4}	Drain Voltage 4
19	DET	Power Detector
21	RF _{out}	RF Output
29	V_{D2}	Drain Voltage 2
31	V_{D1}	Drain Voltage 1

- MACOM recommends connecting all No Connection (N/C) pins to ground.
- The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.

^{*} Restrictions on Hazardous Substances, compliant to current RoHS EU directive.



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Electrical Specifications: $T_A = +25^{\circ}C$, $V_D = 6 V$, $Z_0 = 50 \Omega$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Gain	27.5 GHz 31.0 GHz	dB	24 23	27.5 25.0	_
Output Power (@ Pin = +12dBm)	27.5 GHz 31.0 GHz	dBm	34.5 34.5	36.0 36.0	_
IM3 Level	P _{OUT} = 23 dBm / tone	dBc	_	-35	_
Power Added Efficiency	P _{IN} = 15 dBm	%	_	28	_
Input Return Loss	_	dB	_	12	_
Output Return Loss	_	dB	_	12	_
Quiescent Current	I _{DSQ} (see bias conditions, page 4)	mA	_	1700	_
Drain Current (V _{D1} + V _{D2} + V _{D3} + V _{D4})	P _{IN} = 15 dBm	mA	_	2700	_

Maximum Operating Conditions

Parameter	Rating	
Input Power	P _{IN} ≤ 3 dB Compression	
Junction Temperature ^{4,5}	+160°C	
Operating Temperature	-40°C to +85°C	

- Operating at nominal conditions with junction temperature ≤ +160°C will ensure MTTF > 1 x 10⁶ hours.
- 5. Junction Temperature (T_J) = T_C + Θ _{JC} * ((V * I) (P_{OUT} P_{IN})) Typical thermal resistance (Θ _{JC}) = 3.8 °C/W.
 - a) For T_C = +25°C

 $T_{\rm J}$ = +70°C @ 6 V, 2.8 A, $P_{\rm OUT}$ = 37 dBm, $P_{\rm IN}$ = 15 dBm b) For $T_{\rm C}$ = +85°C

 T_J = 132°C @ 6 V, 2.8 A, P_{OUT} = 36.5 dBm, P_{IN} = 15 dBm

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these HBM Class 1A devices.

Absolute Maximum Ratings^{6,7}

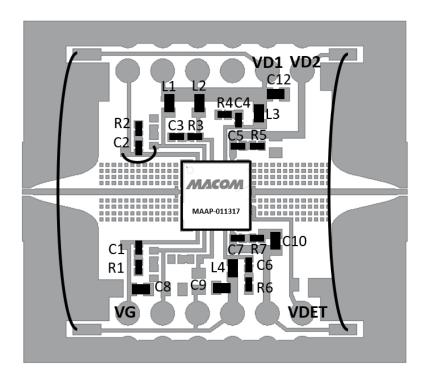
Parameter	Absolute Maximum
Input Power	18 dBm
Drain Voltage	+6.5 V
Gate Voltage	-3 to 0 V
Junction Temperature ⁸	+175°C
Storage Temperature	-65°C to +125°C

- 6. Exceeding any one or combination of these limits may cause permanent damage to this device.
- MACOM does not recommend sustained operation near these survivability limits.
- Junction temperature directly affects device MTTF. Junction temperature should be kept as low as possible to maximize lifetime.

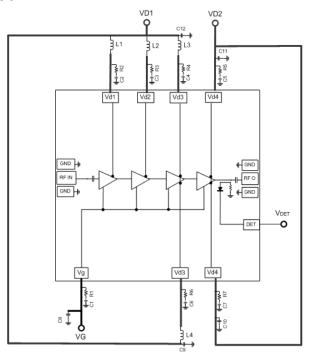


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Sample Board Layout



Application Schematic



Parts List

Part	Value	Case Style
C1 - C7	0.01 μF	0402
C8 - C12	22 µF	0603
R1 - R7	10 Ω	0402
L1 - L4	Ferrite bead Murata BLM18HE601SN1D	0603

Sample Board Material Specifications

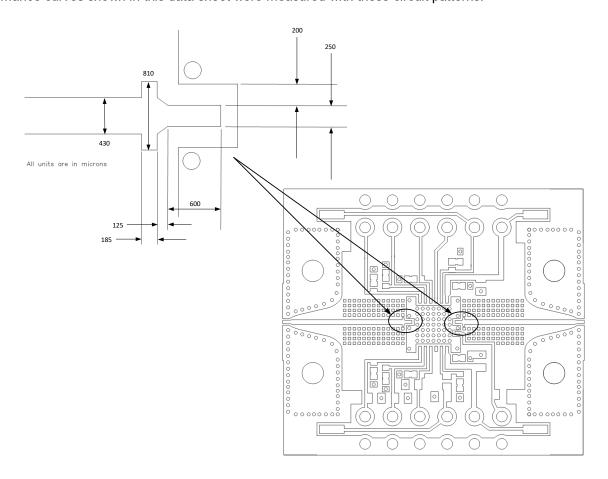
Top Layer: 1/2 oz Copper Cladding, 0.017 mm thickness Dielectric Layer: Rogers RO4003C 0.203 mm thickness Bottom Layer: 1/2 oz Copper Cladding, 0.017 mm thickness Finished overall thickness: 0.238 mm



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Recommended PCB Layout Detail:

RF input and output pre-matching circuit patterns are identical and are designed to compensate packaging effects. Transmission line dimensions apply to a PCB with 0.203 mm thick Rogers RO4003C laminate dielectric. Performance curves shown in this data sheet were measured with these circuit patterns.



Biasing Conditions

Recommended biasing conditions are V_D = 6 V, I_{DSQ} = 1700 mA (controlled with V_G). The drain bias voltage range is 5.5 to 6.5 V.

 $V_{\rm G}$ pins 10 and 11 are connected internally; choose either pin for layout convenience. Muting can be accomplished by setting the $V_{\rm G}$ to the pinched off voltage ($V_{\rm G}$ = -2 V).

 V_D bias must be applied to V_D1 , V_D2 , V_D3 , and V_D4 pins. V_D3 pins 27 and 28 are connected internally: choose pin 14, 27 or 28 for layout convenience. Two V_D4 pins 15 and 26 (not connected internally) are required for current symmetry.

Operating the MAAP-011317

Turn-on

- 1. Apply V_G (-1.5 V).
- 2. Apply V_D (6.0 V typical).
- 3. Set I_{DQ} by adjusting V_G more positive (typically -0.9 to -1.0 V for I_{DSQ} = 1700 mA).
- 4. Apply RF_{IN} signal.

Turn-off

- 1. Remove RFIN signal.
- 2. Decrease V_G to -1.5 V.
- 3. Decrease V_D to 0 V.

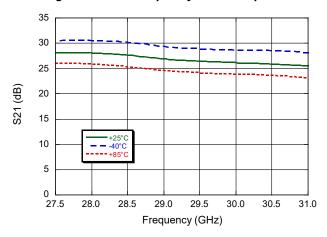
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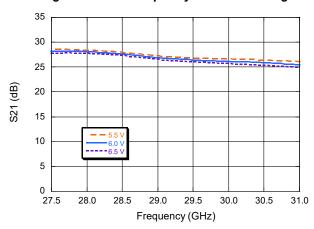
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Typical Performance Curves: $V_D = 6 \text{ V}$, $I_{DSQ} = 1700 \text{ mA}$, $V_G = -0.9 \text{ V}$ typical

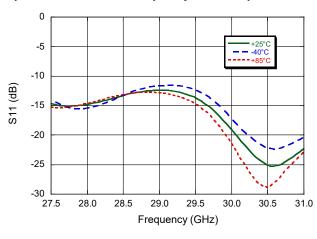
Small Signal Gain vs. Frequency over Temperature



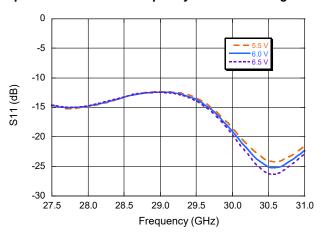
Small Signal Gain vs. Frequency over Bias Voltage



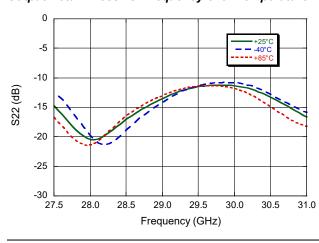
Input Return Loss vs. Frequency over Temperature



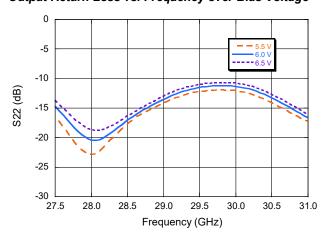
Input Return Loss vs. Frequency over Bias Voltage



Output Return Loss vs. Frequency over Temperature



Output Return Loss vs. Frequency over Bias Voltage



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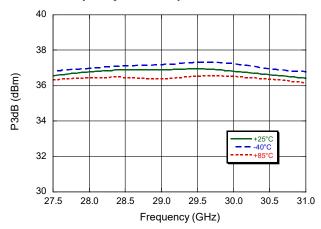
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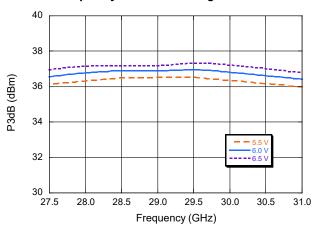
Rev. V2

Typical Performance Curves: $V_D = 6 \text{ V}$, $I_{DSQ} = 1700 \text{ mA}$, $V_G = -0.9 \text{ V}$ typical

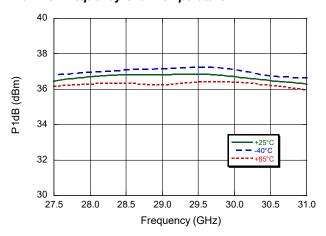
P3dB vs. Frequency over Temperature



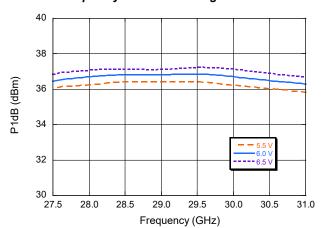
P3dB vs. Frequency over Bias Voltage



P1dB vs. Frequency over Temperature



P1dB vs. Frequency over Bias Voltage

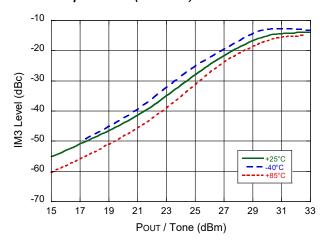




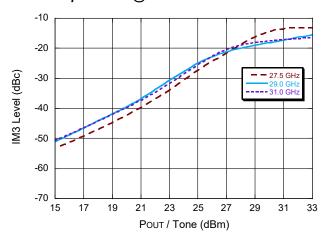
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Typical Performance Curves: $V_D = 6 \text{ V}$, $I_{DSQ} = 1700 \text{ mA}$, $V_G = -0.9 \text{ V}$ typical

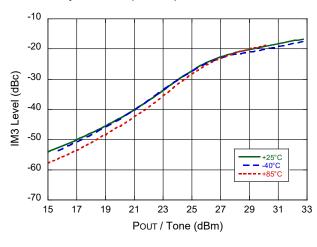
IM3 vs. Output Power (27.5 GHz)



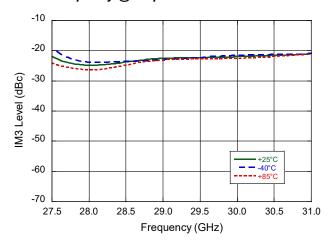
IM3 vs. Output Power @ 25°C



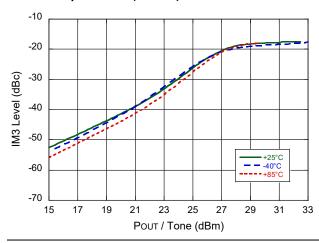
IM3 vs. Output Power (29 GHz)



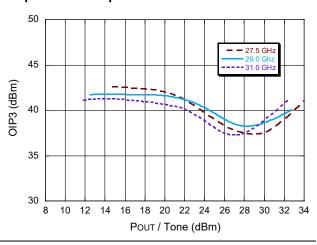
IM3 vs. Frequency @ Output Power = 27 dBm/tone



IM3 vs. Output Power (31 GHz)



Output IP3 vs. Output Power



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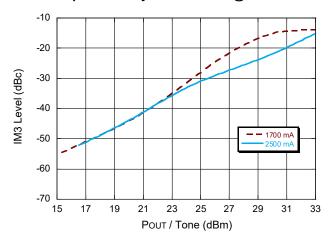
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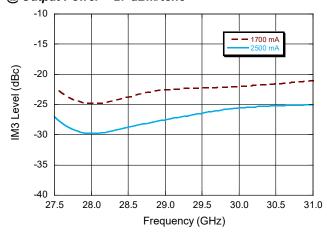
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Typical Performance Curves: V_D = 6 V, 25°C

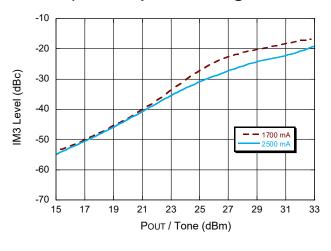
IM3 vs. Output Power by Drain Current @ 27.5 GHz



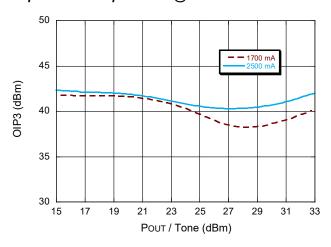
IM3 vs. Frequency by Drain Current @ Output Power = 27 dBm/tone



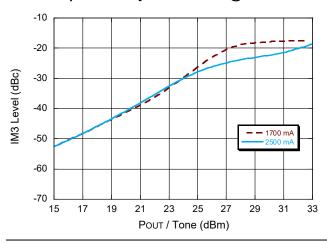
IM3 vs. Output Power by Drain Current @ 29 GHz



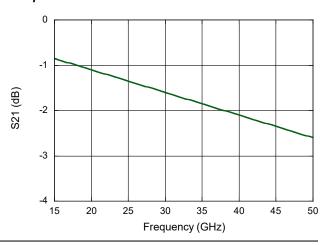
Output IP3 vs. Output Power @ 29 GHz



IM3 vs. Output Power by Drain Current @ 31 GHz



Sample Board Thru Loss



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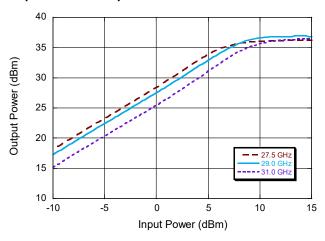
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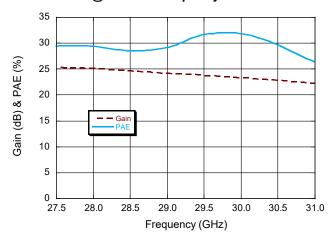
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Typical Performance Curves: V_D = 6 V, I_{DSQ} = 1700 mA, V_G = -0.9 V typical, 25°C

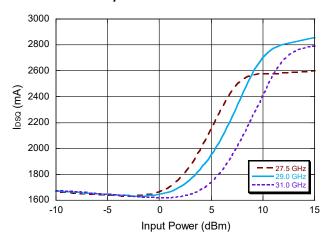
Output Power vs. Input Power



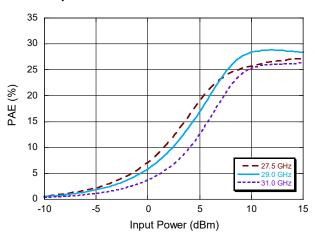
Gain and PAE @ P3dB vs. Frequency



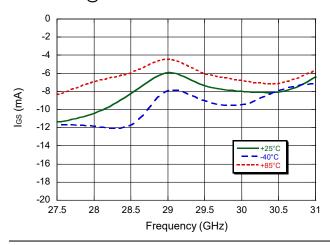
Bias Current vs. Input Power



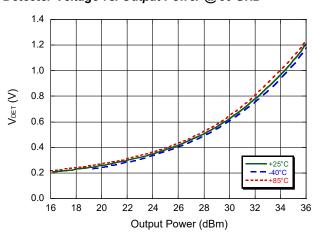
PAE vs. Input Power



Gate Current @ P3dB



Detector Voltage vs. Output Power @ 30 GHz



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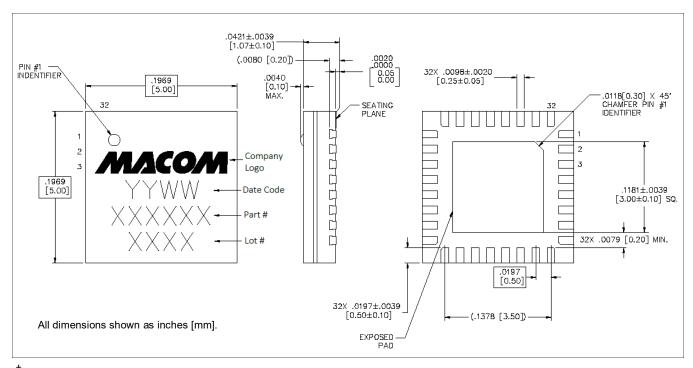
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Lead-Free 5 mm 32-Lead AQFN Package[†]



[†] Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 3 requirements. Plating is NiPdAu.

Power Amplifier, 4 W 27.5 - 31.0 GHz



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