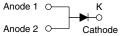
Vishay General Semiconductor

# Surface-Mount Low V<sub>F</sub> Standard Rectifiers



www.vishay.com

## SE12DX



## LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	12 A				
V <sub>RRM</sub>	400 V, 600 V				
I <sub>FSM</sub>	165 A				
$V_F$ at $I_F$ = 12 A ( $T_A$ = 125 °C)	0.83 V				
T <sub>J</sub> max.	175 °C				
Package	SMPD (TO-263AC)				
Circuit configuration	Single				

### **FEATURES**

- Very low profile typical height of 1.7 mm
- · Low forward voltage drop
- AEC-Q101 qualified available
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

## **TYPICAL APPLICATIONS**

General purpose, power line polarity protection, in both consumer and automotive applications.

#### **MECHANICAL DATA**

Case: SMPD (TO-263AC) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 gualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102 M3 and HM3 suffix meet JESD 201 class 2 whisker test

Polarity: as marked

<b>MAXIMUM RATINGS</b> ( $T_A = 25$ °C unless otherwise noted)					
PARAMETER		SE12DLG SE12DLJ		UNIT	
Device marking code		SE12DLG	SE12DLJ		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	400	600	V	
Maximum DC forward current	I <sub>F</sub> <sup>(1)</sup>	12		A	
Maximum DC forward current	I <sub>F</sub> <sup>(2)</sup>	3.7			
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	165		А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub> <sup>(3)</sup> -55 to +175		°C		

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Notes

<sup>(1)</sup> Mounted on infinite heatsink

<sup>(2)</sup> Free air, mounted on recommended copper pad area

<sup>(3)</sup> The heat generated must be less than the thermal conductivity from junction to ambient  $dP_D/dT_J < R_{thJA}$ 



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 6 A	– T <sub>A</sub> = 25 °C		0.86	-	- V
	I <sub>F</sub> = 12 A		V <sub>F</sub> <sup>(1)</sup>	0.93	1	
	I <sub>F</sub> = 6 A	– T <sub>A</sub> = 125 °C	VF	0.72	-	
	I <sub>F</sub> = 12 A			0.83	0.9	
Reverse current	Rated V <sub>B</sub>	T <sub>A</sub> = 25 °C	– I <sub>R</sub> <sup>(2)</sup> –	-	5	μA
	naleu v <sub>R</sub>	T <sub>A</sub> = 125 °C	= 125 °C	12	70	
Typical reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1.0 A, I <sub>rr</sub> = 0.25 A		t <sub>rr</sub>	300	-	ns
Typical junction capacitance	4.0 V, 1 MHz		CJ	96	-	pF

Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25$ °c unless otherwise noted)					
PARAMETER	SYMBOL	SE12DLG SE12DLJ		UNIT	
Typical thermal registeres	R <sub>0JA</sub> (1)(2)	55			
Typical thermal resistance		1.5			

#### Notes

 $^{(1)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

(2) Free air, mounted on recommended PCB, 2 oz. pad area; thermal resistance R<sub>0JA</sub> - junction to ambient to follow JEDEC® 51-2A

(3) Mounted on infinite heatsink thermal resistance R<sub>thJM</sub> - junction to mount to follow JEDEC<sup>®</sup> 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
SE12DLJ-M3/I	0.54	I	2000/reel	13" diameter plastic tape and reel		
SE12DLJHM3/I <sup>(1)</sup>	0.54		2000/reel	13" diameter plastic tape and reel		

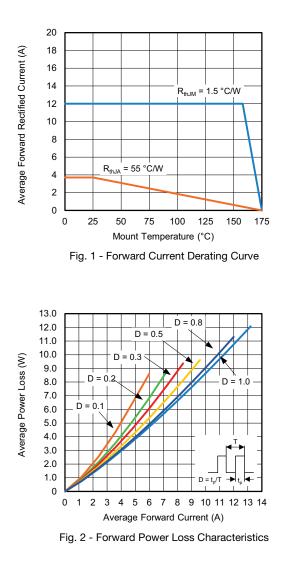
#### Note

<sup>(1)</sup> AEC-Q101 qualified



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## **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)



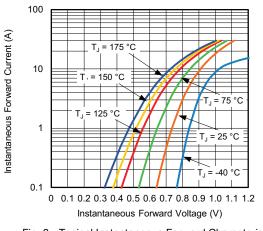


Fig. 3 - Typical Instantaneous Forward Characteristics

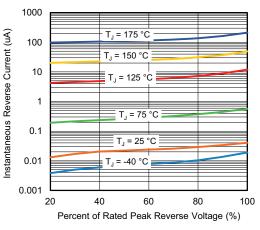
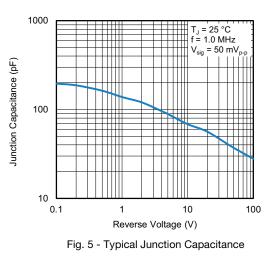


Fig. 4 - Typical Reverse Leakage Characteristics



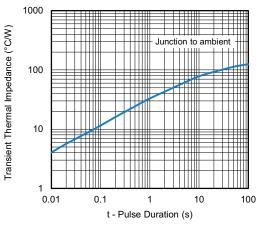


Fig. 6 - Typical Transient Thermal Impedance

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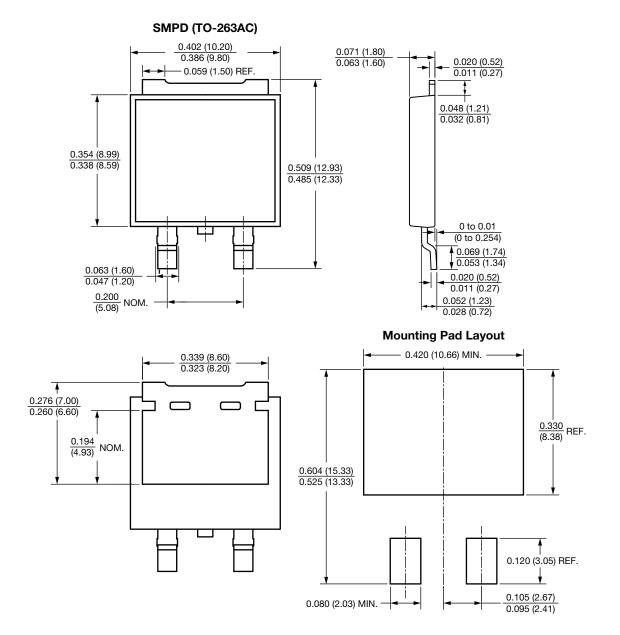
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### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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