### Possible IGBT Snubber Capacitors



Mitsubishi IGBT Type	RS Part Numbers	Suggested ICEL Snubber	Value Cn	Image (not to scale)	RS Part Numbers
CM450DY-24T#300G	207-4970	PMB2123390KSPB	0.39uF		207-4991 (low qty) and 207-4990 (higher qty)
207-4971					https://uk.rs-online.com/web/cp/2074990,2074991/?pst=PMB2123390KSPB_
		PMB2123560KSP	0.56uF	And Million	207-4993 (low qty) and 207-4992 (higher qty) https://uk.rs-online.com/web/cp/2074992,2074993/?pst=PMB2123560KSP
		PMB2124100KSP	1.0uF	Pite Name in case	207-4995 (low qty) and 207-4994 (higher qty) https://uk.rs-online.com/web/cp/2074994.2074995/?pst=PMB2124100KSP&sra=p&r=t
0		PMB2124150KSP	1.5uF	Direct screw mounting onto IGBT modules or busbars. Available for all main manufacturers IGBT packages.	207-4998 (low qty) and 207-4997 (higher qty) https://uk.rs-online.com/web/cp/2074997,207498/?pst=PMB2124150KSP&sra=p&r=t

#### POLYPROPYLENE FILM CAPACITORS

#### Features

Polypropylene film capacitors have superior electrical characteristics; Low dissipation factor and absorption. Very high insulation resistance and high dielectric strength. Excellent moisture resistance. Good long-term stability and excellent self-healing properties.

#### **Typical Applications**

Polypropylene film capacitors are typically used in AC and pulse applications at high frequencies and as DC-Link capacitors. They are further used in switched mode power supplies (SMPS), electronic ballasts and snubber applications, in frequency discrimination and filter circuits as well as in energy storage and sample and hold applications.

Suggested types are for Information and guidance only. Clients must select and verify parts for their own operating conditions and applcations.

CONTINUE FOR IGBT DATA SHEET

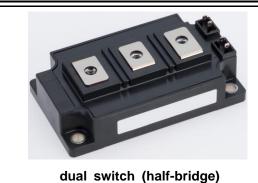




<IGBT Modules>

# CM450DY-24T

HIGH POWER SWITCHING USE INSULATED TYPE



**Dimension in mm** 

•UL Recognized under UL1557, File No.E323585

### APPLICATION

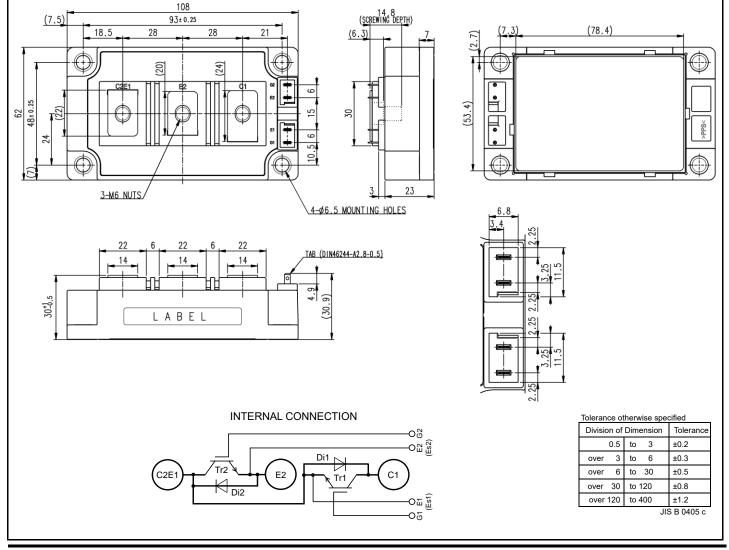
AC Motor Control, Motion/Servo Control, Power supply, etc.

**OPTION** (Below options are available.)

•PC-TIM (Phase Change Thermal Interface Material) pre-apply (Note8)

•VcEsat selection for parallel connection

### OUTLINE DRAWING & INTERNAL CONNECTION



### MAXIMUM RATINGS (Tvj=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Rating	Unit	
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	1200	V	
$V_{\text{GES}}$	Gate-emitter voltage	C-E short-circuited	± 20	V	
lc	Collector current	DC, T <sub>C</sub> =145 °C* (Note2, 4)	450	^	
I <sub>CRM</sub>	Collector current	Pulse, Repetitive (Note3)	900	- A	
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> =25 °C (Note2, 4)	4835	W	
IE (Note1)		DC (Note2)	450	•	
IERM (Note1)	Emitter current	Pulse, Repetitive (Note3)	900	A	
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V	
T <sub>vjmax</sub>	Maximum junction temperature	Instantaneous event (overload) (Note8)	175	- ℃	
T <sub>Cmax</sub>	Maximum case temperature	(Note4,8)	150*		
$T_{vjop}$	Operating junction temperature	Continuous operation (under switching) (Note8)	-40 ~ +150	- °C	
T <sub>stg</sub>	Storage temperature	-	-40 ~ +150*		

### ELECTRICAL CHARACTERISTICS (Tvj=25 °C, unless otherwise specified)

Symbol	ltom			Limits			Linit
Symbol	Item	Conditions		Min.	Тур.	Max.	Unit
I <sub>CES</sub>	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited		-	-	1.0	mA
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited		-	-	0.5	μA
$V_{GE(th)}$	Gate-emitter threshold voltage	I <sub>C</sub> =45 mA, V <sub>CE</sub> =10 V		5.4	6.0	6.6	V
		I <sub>C</sub> =450 A, V <sub>GE</sub> =15 V,	T <sub>vj</sub> =25 °C	-	1.70	2.00	v
V <sub>CEsat</sub>		Refer to the figure of test circuit	T <sub>vj</sub> =125 °C	-	1.95	-	
(Terminal)		(Note5)	T <sub>vj</sub> =150 °C	-	2.00	-	
	Collector-emitter saturation voltage	I <sub>C</sub> =450 A,	T <sub>vj</sub> =25 °C	-	1.55	1.80	
V <sub>CEsat</sub>		V <sub>GE</sub> =15 V,	T <sub>vj</sub> =125 °C	-	1.75	-	V
(Chip)		(Note5)	T <sub>vj</sub> =150 °C	-	1.80	-	
Cies	Input capacitance		-	-	92.3	nF	
C <sub>oes</sub>	Output capacitance	V <sub>CE</sub> =10 V, G-E short-circuited		-	-		2.7
Cres	Reverse transfer capacitance				-	1.1	
Q <sub>G</sub>	Gate charge	V <sub>CC</sub> =600 V, I <sub>C</sub> =450 A, V <sub>GE</sub> =15 V		-	3.0	-	μC
t <sub>d(on)</sub>	Turn-on delay time		-	-	500	ns	
tr	Rise time	V <sub>cc</sub> =600 V, I <sub>c</sub> =450 A, V <sub>GE</sub> =±15 V, R <sub>G</sub> =1.0 Ω, Inductive load		-	-		200
t <sub>d(off)</sub>	Turn-off delay time			-	-		600
t <sub>f</sub>	Fall time			-	-	300	
(Note 1)	- Emitter-collector voltage	I <sub>E</sub> =450 A, G-E short-circuited,	T <sub>vj</sub> =25 °C	-	1.80	2.20	V
V <sub>EC</sub> (Note.1)		Refer to the figure of test circuit	T <sub>vj</sub> =125 °C	-	1.95	-	
(Terminal)		(Note5)	T <sub>vj</sub> =150 °C	-	1.95	-	
() () () () () () () () () () () () () (		I <sub>E</sub> =450 A,	T <sub>vj</sub> =25 °C	-	1.65	2.00	
V <sub>EC</sub> (Note.1)		G-E short-circuited,	T <sub>vj</sub> =125 °C	-	1.65	-	V
(Chip)		(Note5)	T <sub>vj</sub> =150 °C	-	1.65	-	
t <sub>rr</sub> <sup>(Note1)</sup>	Reverse recovery time	V <sub>CC</sub> =600 V, I <sub>E</sub> =450 A, V <sub>GE</sub> =±15 V,		-	-	400	ns
Qrr (Note1)	Reverse recovery charge	$R_{G}$ =1.0 $\Omega$ , Inductive load		-	45	-	μC
Eon	Turn-on switching energy per pulse	V <sub>CC</sub> =600 V, I <sub>C</sub> =I <sub>E</sub> =450 A, V <sub>GE</sub> =±15 V, R <sub>G</sub> =1.0 Ω, T <sub>vj</sub> =150 °C,		-	40.9	-	
E <sub>off</sub>	Turn-off switching energy per pulse			-	47	-	mJ
Err (Note1)	Reverse recovery energy per pulse	Inductive load		-	31.6	-	mJ
R <sub>CC'+EE'</sub>	Internal lead resistance	Main terminals-chip, per switch, Tc=25	°C <sup>(Note4)</sup>	-	0.3	-	mΩ
r <sub>g</sub>	Internal gate resistance	Per switch	-	1.0	-	Ω	

\*: The value of PC-TIM applied module is limited by the heat resistant temperature of PC-TIM.

### THERMAL RESISTANCE CHARACTERISTICS

Symbol	Itom	Conditions	Limits			Unit
Symbol	Item	Conditions		Тур.	Max.	Unit
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	31	K/kW
$R_{th(j-c)D}$	Thermai resistance	Junction to case, per Inverter FWD (Note4)	-	-	54	r/kvv
R <sub>th(c-s)</sub>	Contact thermal resistance	Case to heat sink, per 1 module Thermal grease applied (Note4,6,8)	-	13.3	-	K/kW

### **MECHANICAL CHARACTERISTICS**

Symbol	Item	Conditions			1.1		
				Min.	Тур.	Max.	Unit
Mt	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N∙m
Ms	Mounting torque	Mounting to heat sink	M 6 screw	3.5	4.0	4.5	N∙m
d	Creepage distance	Terminal to terminal		17.3	-	-	mm
ds		Terminal to base plate		25.3	-	-	
	Clearance	Terminal to terminal		12.6	-	-	mm
da		Terminal to base plate		21.8	-	-	
ec	Flatness of base plate	On the centerline X, Y (Note7)		±0	-	+200	μm
m	mass	-		-	260	-	g

\*. This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/ 2011/65/EU and (EU) 2015/863.EU.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

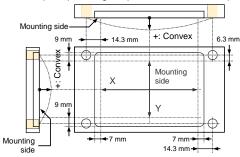
2. Junction temperature  $(T_{vj})$  should not increase beyond  $T_{vjmax}$  rating.

3. Pulse width and repetition rate should be such that the device junction temperature  $(T_{vj})$  dose not exceed  $T_{vjmax}$  rating.

4. Case temperature (T<sub>c</sub>) and heat sink temperature (T<sub>s</sub>) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.

5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

- 6. Typical value is measured by using thermally conductive grease of  $\lambda$ =3.0W/(m·K)/D<sub>(C-S)</sub>=50 µm.
- 7. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



8. Long term performance related to thermal conductive grease and PC-TIM (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under your specific application conditions. Each temperature condition (T<sub>vj max</sub>, T<sub>vj op</sub>, T<sub>C max</sub>) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

## <IGBT Modules> CM450DY-24T HIGH POWER SWITCHING USE INSULATED TYPE

### **RECOMMENDED OPERATING CONDITIONS**

Sumbol	Itom	Conditions	Limits			Unit
Symbol	Item	Conditions	Min.	Тур.	Max.	Unit
V <sub>cc</sub>	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V
$V_{\text{GEon}}$	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R <sub>G</sub>	External gate resistance	Per switch	1.0	-	10	Ω

### CHIP LOCATION (Top view)

(108)(93)72.4 2 σ. 2 ς 0 23. 33. 50. 61. Ð + ÷ 48.1 44.2 40.7  $\overline{D_{i} 2}$  Tr2 Di 43.87 <u>D¢2E1Tr</u>2 T r 2== <del>C1</del> E2 (62) (48) E1 21.6 18.0 14.2 ·D|i|1 -Tr 1 Di 1 18.4 ·Tr1 Tr1 Di1 Φ0 0 Œ 26.8-66.2-6 0 4 0 49. 75. 37 LABEL SIDE

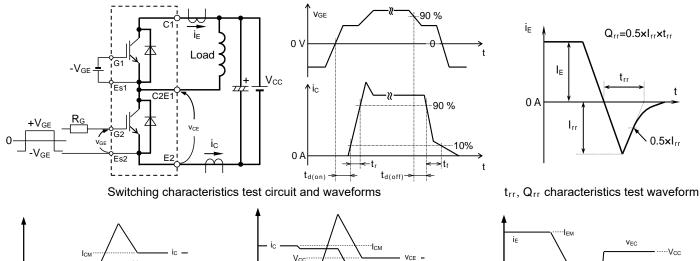
Tr1/Tr2: IGBT, Di1/Di2: FWD

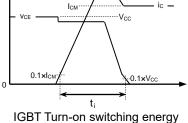
Dimension in mm, tolerance: ±1 mm

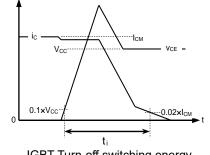
## <IGBT Modules> CM450DY-24T HIGH POWER SWITCHING USE

INSULATED TYPE

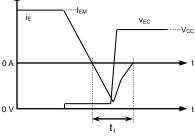








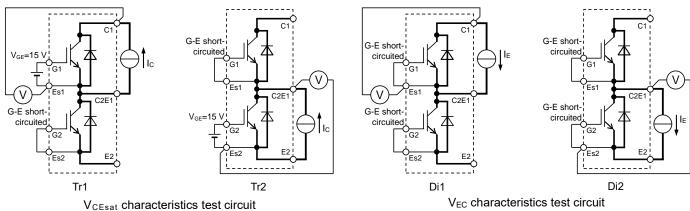
IGBT Turn-off switching energy



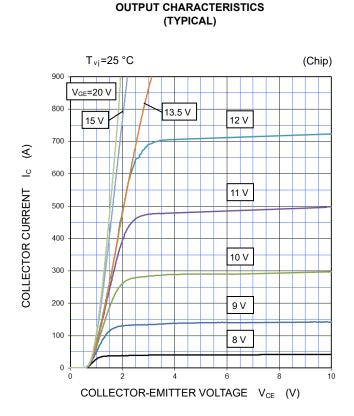
FWD Reverse recovery energy

Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

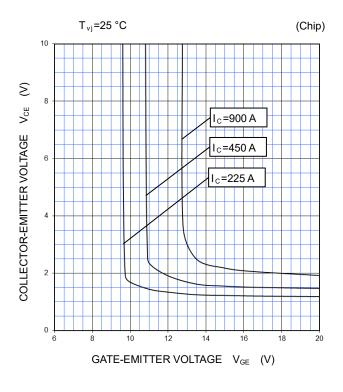
### **TEST CIRCUIT**



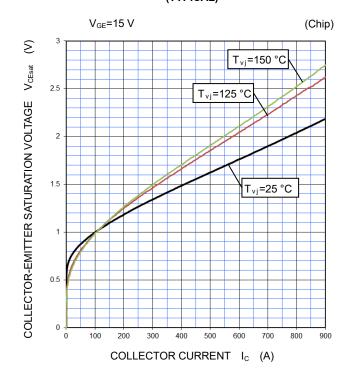
### PERFORMANCE CURVES



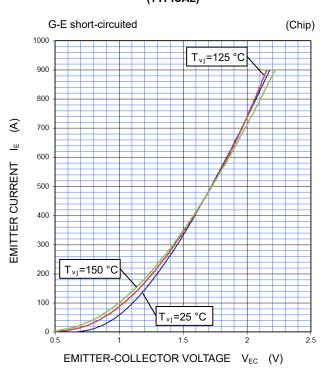
# COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)







#### FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



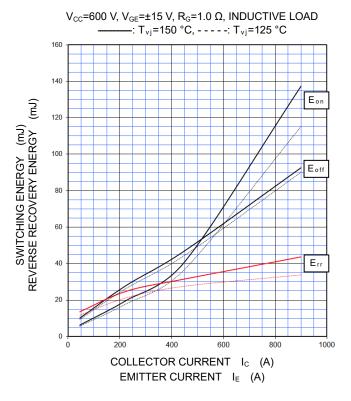
### PERFORMANCE CURVES

 $V_{CC}$ =600 V,  $V_{GE}$ =±15 V,  $R_{G}$ =1.0  $\Omega$ , INDUCTIVE LOAD -: T<sub>vj</sub>=150 °C, - - - -: T<sub>vj</sub>=125 °C 1000  $t_{d(off)}$ t<sub>d(on)</sub> (su)  $t_{r}$ SWITCHING TIME  $t_{\rm f}$ 100 10 400 0 200 600 800 1000 COLLECTOR CURRENT  $I_c$  (A)

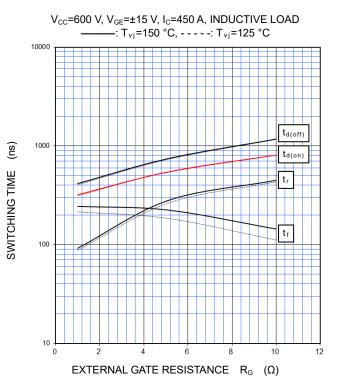
HALF-BRIDGE SWITCHING CHARACTERISTICS

(TYPICAL)

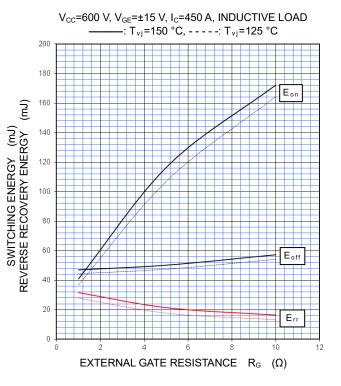
# HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



## HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

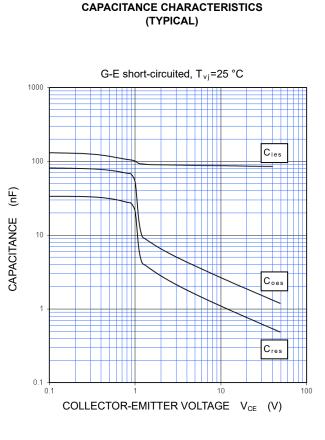


# HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

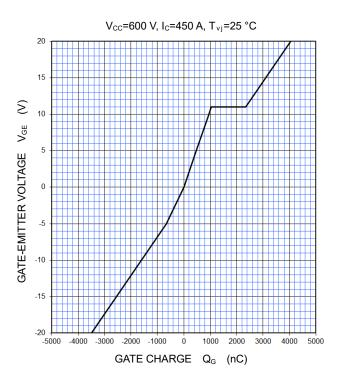


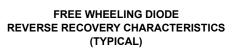
## <IGBT Modules> CM450DY-24T HIGH POWER SWITCHING USE INSULATED TYPE

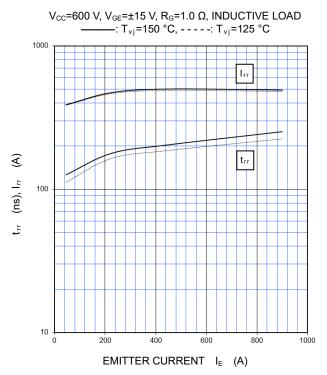
### PERFORMANCE CURVES



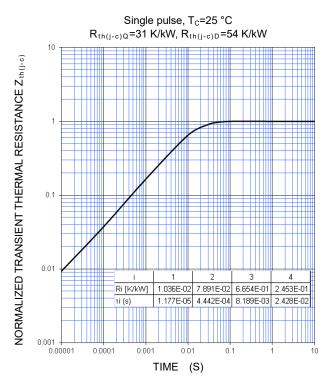
GATE CHARGE CHARACTERISTICS (TYPICAL)





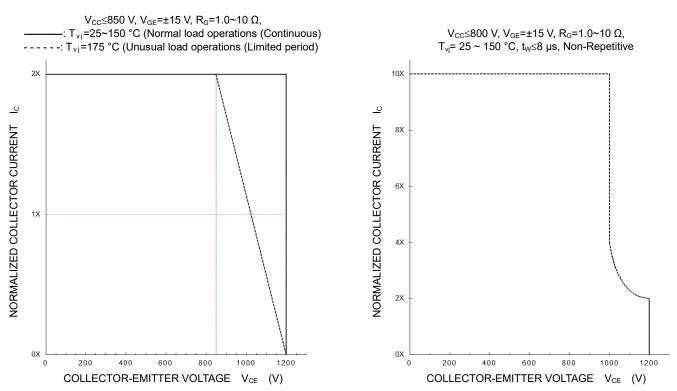


# TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



### PERFORMANCE CURVES

#### TURN-OFF SWITCHING SAFE OPERATING AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

SHORT-CIRCUIT SAFE OPERATING AREA

(MAXIMUM)

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