



# 40V 175°C P-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8 (SWP) (Type UX)

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C		
4017	25mΩ @ V <sub>GS</sub> = -10V	-40A		
-40V	45mΩ @ V <sub>GS</sub> = -4.5V	-30A		

#### **Features and Benefits**

- Rated to +175°C—Ideal for High Ambient Temperature Environments
- Low R<sub>DS(ON)</sub>—Ensures Minimal On-State Losses
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- Occupies Just 33% of the Board Area Occupied by SO-8 Enabling Smaller End Product
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

## **Description and Applications**

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

- Reverse-Polarity Protection
- Power-Management Functions
- DC-DC Converters

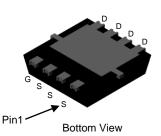
#### **Mechanical Data**

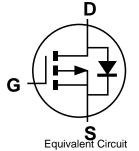
- Case: PowerDI<sup>®</sup>3333-8 (SWP) (Type UX)
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 (23)
- Weight: 0.072 grams (Approximate)

PowerDI3333-8 (SWP) (Type UX)



Top View





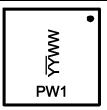
#### Ordering Information (Note 5)

Part Number	Case	Packaging
DMPH4025SFVWQ-7	PowerDI3333-8 (SWP) (Type UX)	2000/Tape & Reel
DMPH4025SFVWQ-13	PowerDI3333-8 (SWP) (Type UX)	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

#### Marking Information



PW1 = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 17 = 2017)
WW = Week Code (01 to 53)



# **Maximum Ratings** $(@T_A = +25^{\circ}C, \text{ unless otherwise specified.})$

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	$V_{DSS}$	-40	V	
Gate-Source Voltage	$V_{GSS}$	±20	V	
	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	-8.7 -7.3	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = -10V	$T_C = +25$ °C $T_C = +70$ °C	I <sub>D</sub> (Package Limit)	-40 -33	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	-80	Α	
Maximum Continuous Body Diode Forward Current	I <sub>S</sub>	-3	Α	
Pulsed Source Current (10µs Pulse, Duty Cycle = 1%)	I <sub>SM</sub>	-80	Α	
Avalanche Current, L = 0.3mH		I <sub>AS</sub>	-23	Α
Avalanche Energy, L = 0.3mH		E <sub>AS</sub>	82	mJ

## Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	ם	2.3	W
Total Power Dissipation (Note 6)	T <sub>C</sub> = +25°C	$P_{D}$	60	W
Thermal Resistance, Junction to Ambient (Note 6)  Steady State		R <sub>ÐJA</sub>	53	°C/W
Thermal Resistance, Junction to Case (Note 6)	R <sub>eJC</sub>	2.5	*C/VV	
Operating and Storage Temperature Range	$T_{J_i}T_{STG}$	-55 to +175	°C	

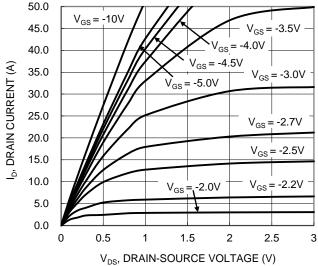
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-40	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	l		-1	μA	$V_{DS} = -40V, V_{GS} = 0V$	
Gate-Source Leakage	$I_{GSS}$	1		±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(TH)}$	-0.8	_	-1.8	V	$V_{DS} = V_{GS}$ , $I_D = -250\mu A$	
Static Drain-Source On-Resistance			18	25	mΩ	$V_{GS} = -10V, I_D = -30A$	
Static Drain-Source On-Nesistance	R <sub>DS(ON)</sub>		23	45		$V_{GS} = -4.5V$ , $I_D = -15A$	
Diode Forward Voltage	$V_{SD}$		_	-1	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>	_	1918	_	pF	\\ - 20\\\\\ - 0\\	
Output Capacitance	Coss		390	_	pF	$V_{DS} = -20V, V_{GS} = 0V,$ - f = 1MHz	
Reverse Transfer Capacitance	$C_{rss}$		151	_	pF		
Gate Resistance	$R_{g}$		5.76	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = -4.5V)	$Q_{g}$		19.6	_	nC	V <sub>DS</sub> = -20V, I <sub>D</sub> = -3A	
Total Gate Charge (V <sub>GS</sub> = -10V)	$Q_{g}$		38.6	_	nC		
Gate-Source Charge	$Q_{gs}$		3.7	_	nC		
Gate-Drain Charge	$Q_{gd}$		7.3	_	nC		
Turn-On Delay Time	t <sub>D(ON)</sub>		4.8	_	ns	V <sub>DD</sub> = -20V, V <sub>GS</sub> = -10V, I <sub>D</sub> = -3A	
Turn-On Rise Time	t <sub>R</sub>	1	14.2		ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>		72.2	_	ns		
Turn-Off Fall Time	t <sub>F</sub>		35.9	_	ns		

6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate. Notes:

<sup>7.</sup> Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.





V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V) Figure 1. Typical Output Characteristic

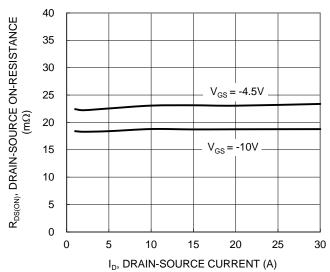


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

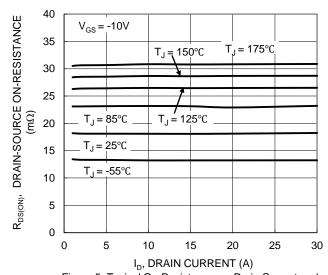


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

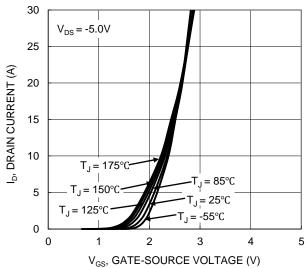


Figure 2. Typical Transfer Characteristic

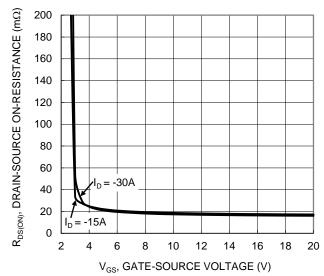


Figure 4. Typical Transfer Characteristic

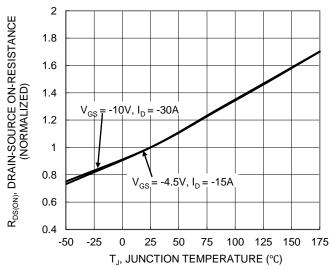
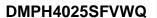


Figure 6. On-Resistance Variation with Temperature





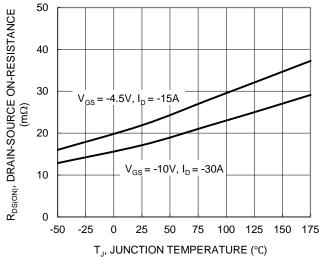


Figure 7. On-Resistance Variation with Temperature

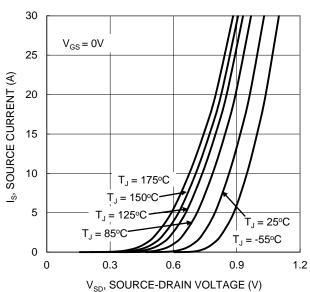
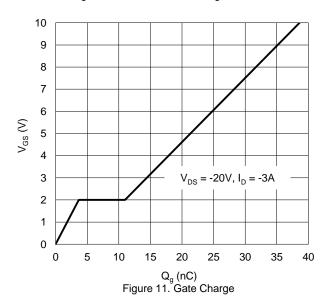
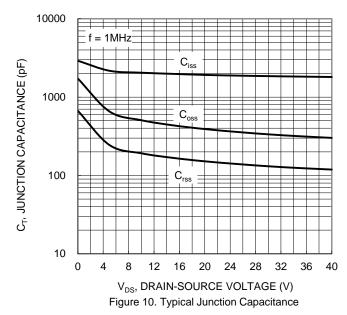


Figure 9. Diode Forward Voltage vs. Current



 $T_J$ , JUNCTION TEMPERATURE (°C) Figure 8. Gate Threshold Variation vs. Junction Temperature



V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



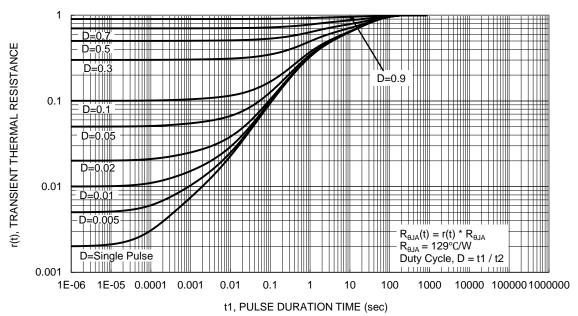


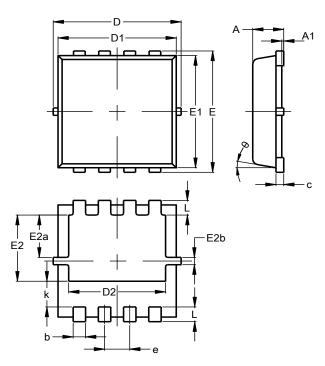
Figure 13. Transient Thermal Resistance



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI3333-8 (Type UX)

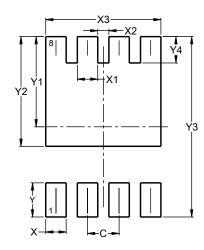


PowerDI3333-8 (Type UX)					
Dim	Min Max Typ				
Α	0.75	0.85	0.80		
A1	0.00	0.05			
b	0.25	0.40	0.32		
С	0.10	0.25	0.15		
D	3.20	3.40	3.30		
D1	2.95	3.15	3.05		
D2	2.30	2.70	2.50		
Е	3.20	3.40	3.30		
E1	2.95	3.15	3.05		
E2	1.60	2.00	1.80		
E2a	0.95	1.35	1.15		
E2b	0.10	0.30	0.20		
е	0.65 BSC				
k	0.50	0.90	0.70		
L	0.30	0.50	0.40		
θ	0°	12°	10°		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI3333-8 (Type UX)



Dimensions	Value (in mm)
С	0.650
Х	0.420
X1	0.420
X2	0.230
Х3	2.370
Y	0.700
Y1	1.850
Y2	2.250
Y3	3.700
Y4	0.540



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