

STGP4M65DF2

Trench gate field-stop IGBT, M series 650 V, 4 A low loss

Datasheet - production data

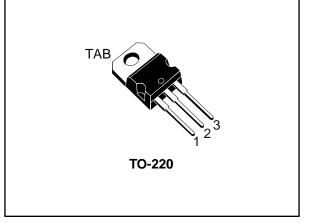
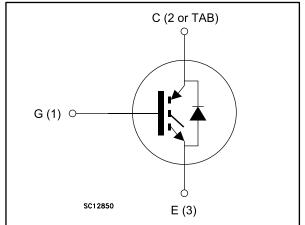


Figure 1: Internal schematic diagram



Features

- 6 µs of short-circuit withstand time
- V_{CE(sat)} = 1.6 V (typ.) @ I_C = 4 A
- Tight parameter distribution
- Safer paralleling
- Low thermal resistance
- Soft and very fast recovery antiparallel diode

Applications

- Motor control
- UPS
- PFC

Description

This device is an IGBT developed using an advanced proprietary trench gate field-stop structure. The device is part of the M series IGBTs, which represent an optimal balance between inverter system performance and efficiency where low-loss and short-circuit functionality are essential. Furthermore, the positive $V_{CE(sat)}$ temperature coefficient and tight parameter distribution result in safer paralleling operation.

Table 1: Device summary	Table	1:	Device	summary
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Order code	Marking	Package	Packing
STGP4M65DF2	G4M65DF2	TO-220	Tube

DocID028671 Rev 3

This is information on a product in full production.

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1 Electrical ratings

 Table 2: Absolute maximum ratings

Symbol	Parameter	Value	Unit
VCES	Collector-emitter voltage (V _{GE} = 0 V)	650	V
la	Continuous collector current at $T_C = 25 \text{ °C}$	8	А
lc	Continuous collector current at T _c = 100 °C	4	А
ICP ⁽¹⁾	Pulsed collector current	16	А
V_{GE}	Gate-emitter voltage	±20	V
1_	Continuous forward current at $T_c = 25 \ ^{\circ}C$	8	А
IF	Continuous forward current at T _c = 100 °C	4	А
I _{FP} ⁽¹⁾	Pulsed forward current	16	А
Ртот	Total dissipation at $T_C = 25 \ ^{\circ}C$	68	W
Tstg	Storage temperature range	- 55 to 150	°C
TJ	Operating junction temperature range	- 55 to 175	°C

Notes:

 $^{(1)}\mbox{Pulse}$ width limited by maximum junction temperature.

Table 3: Thermal data

Symbol	Parameter	Value	Unit
RthJC	Thermal resistance junction-case IGBT	2.2	°C/W
RthJC	Thermal resistance junction-case diode	5	°C/W
RthJA	Thermal resistance junction-ambient	62.5	°C/W



 $T_C = 25 \ ^{\circ}C$ unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)CES}	Collector-emitter breakdown voltage	$V_{GE} = 0 V, I_C = 250 \mu A$	650			V
		V_{GE} = 15 V, I_C = 4 A		1.6	2.1	
V _{CE(sat)}	Collector-emitter saturation voltage	V _{GE} = 15 V, I _C = 4 A, T _J = 125 °C		1.9		V
	Voltage	$V_{GE} = 15 \text{ V}, I_C = 4 \text{ A},$ T _J = 175 °C		2.1		
		IF = 4 A		1.9		
VF	Forward on-voltage	I _F = 4 A, T _J = 125 °C		1.7		V
		I _F = 4 A, T _J = 175 °C		1.6		
$V_{\text{GE(th)}}$	Gate threshold voltage	$V_{CE} = V_{GE}, I_C = 250 \ \mu A$	5	6	7	V
I _{CES}	Collector cut-off current	$V_{GE} = 0 V, V_{CE} = 650 V$			25	μA
Iges	Gate-emitter leakage current	$V_{CE} = 0 \text{ V}, V_{GE} = \pm 20 \text{ V}$			±250	μΑ

Table 4: Static characteristics

Table 5: Dynamic characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Cies	Input capacitance		-	369	-	
Coes	Output capacitance	V _{CE} = 25 V, f = 1 MHz,	-	24.8	-	рF
Cres	Reverse transfer capacitance	V _{GE} = 0 V	-	8	-	μ.
Qg	Total gate charge	Vcc = 520 V, Ic = 4 A,	-	15.2	-	
Q _{ge}	Gate-emitter charge	V _{GE} = 15 V (see <i>Figure 30:</i> " <i>Gate charge</i>	-	3	-	nC
Q _{gc}	Gate-collector charge	test circuit")	-	7	-	



	Table 6: IGBT switching characteristics (inductive load)						
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit	
t _{d(on)}	Turn-on delay time			12	-	ns	
tr	Current rise time			6.9	-	ns	
(di/dt) _{on}	Turn-on current slope	V _{CE} = 400 V, I _C = 4 A,		480	-	A/µs	
t _{d(off)}	Turn-off-delay time	$V_{GE} = 15 \text{ V}, \text{ R}_G = 47 \Omega$ (see <i>Figure 29: " Test</i>		86	-	ns	
t _f	Current fall time	circuit for inductive load		120	-	ns	
Eon ⁽¹⁾	Turn-on switching energy	switching")		0.040	-	mJ	
E _{off} ⁽²⁾	Turn-off switching energy			0.136	-	mJ	
Ets	Total switching energy			0.176	-	mJ	
t _{d(on)}	Turn-on delay time			11.6	-	ns	
tr	Current rise time			8	-	ns	
(di/dt) _{on}	Turn-on current slope	V _{CE} = 400 V, I _C = 4 A,		410	-	A/µs	
t _{d(off)}	Turn-off-delay time	V _{GE} = 15 V, R _G = 47 Ω, T _i = 175 °C		85	-	ns	
t _f	Current fall time	(see Figure 29: " Test circuit		211	-	ns	
Eon ⁽¹⁾	Turn-on switching energy	for inductive load switching")		0.067	-	mJ	
E _{off} ⁽²⁾	Turn-off switching energy			0.210	-	mJ	
E _{ts}	Total switching energy			0.277	-	mJ	
	Short-circuit withstand time	$\label{eq:VCC} \begin{array}{l} V_{CC} \leq 400 \text{ V}, V_{GE} = 15 \text{ V}, \\ T_{Jstart} = 150 ^\circ\text{C} \end{array}$	6		-	μs	
t _{sc}	Short-circuit withstand time	$\label{eq:VCC} \begin{array}{l} V_{CC} \leq 400 \text{ V}, V_{GE} = 13 \text{ V}, \\ T_{Jstart} = 150 ^\circ\text{C} \end{array}$	10		-	μs	

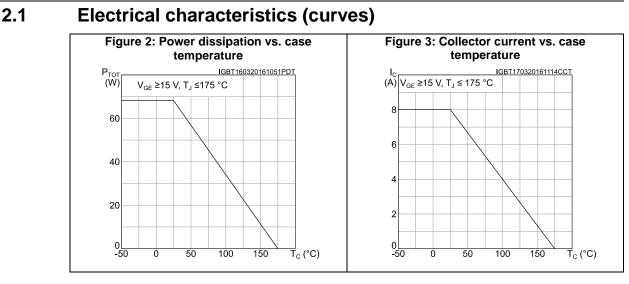
Notes:

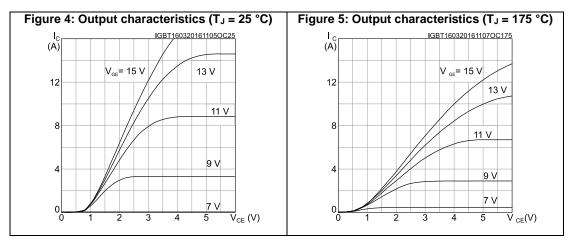
⁽¹⁾Including the reverse recovery of the diode. ⁽²⁾Including the tail of the collector current.

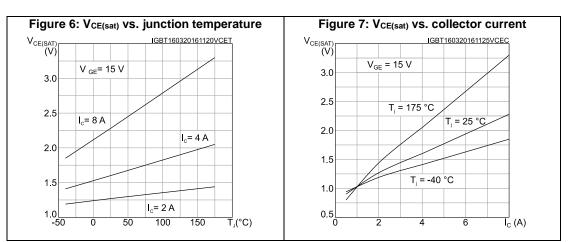
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
trr	Reverse recovery time		-	133	-	ns
Q _{rr}	Reverse recovery charge	I _F = 4 A, V _R = 400 V,	-	140	-	nC
Irrm	Reverse recovery current	V _{GE} = 15 V, di/dt = 800 A/µs	-	5	-	А
dlrr/dt	Peak rate of fall of reverse recovery current during t_b	(see Figure 29: " Test circuit for inductive load switching")	-	520	-	A/µs
Err	Reverse recovery energy		-	15	-	μJ
t _{rr}	Reverse recovery time		-	236	-	ns
Qrr	Reverse recovery charge	I _F = 4 A, V _R = 400 V,	-	370	-	nC
Irrm	Reverse recovery current	V _{GE} = 15 V, T _J = 175 °C, di/dt = 800 A/µs	-	6.6	-	А
dlrr/dt	Peak rate of fall of reverse recovery current during tb	(see Figure 29: " Test circuit for inductive load switching")	-	378	-	A/µs
Err	Reverse recovery energy		-	32	-	μJ

Table 7: Diode switching	characteristics	(inductive load)







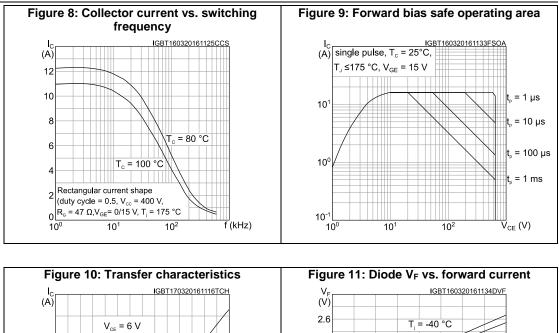


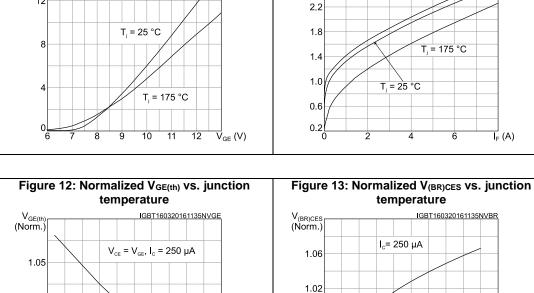


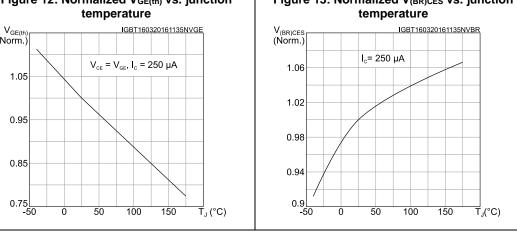
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Electrical characteristics

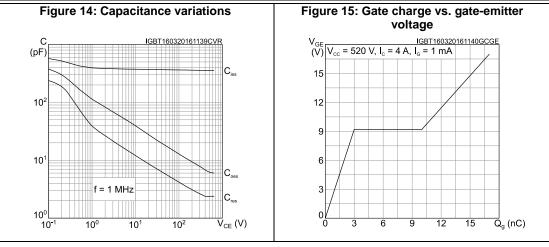


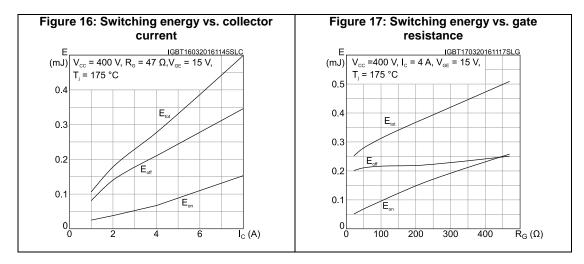


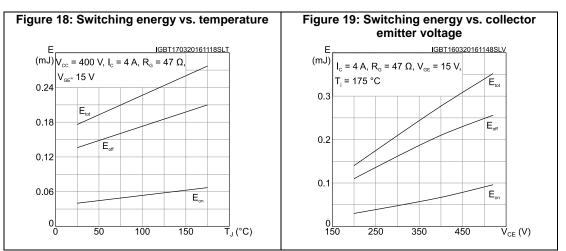




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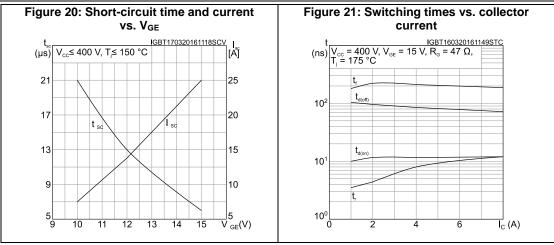


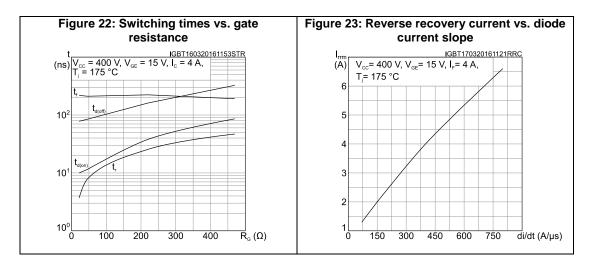


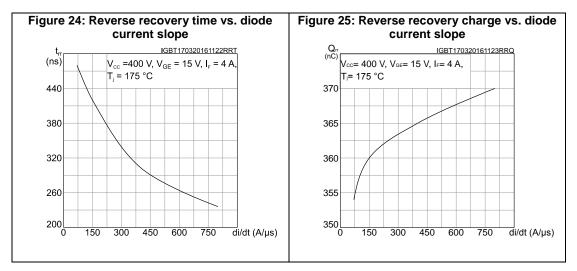
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Electrical characteristics

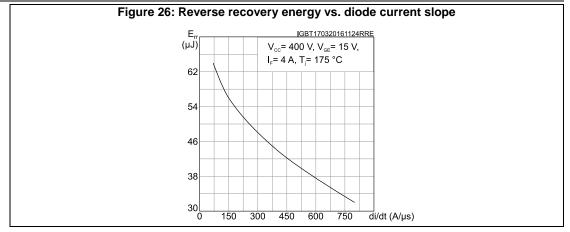


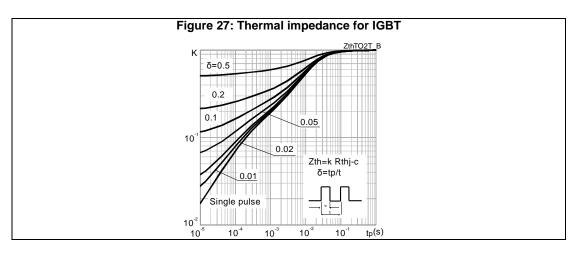


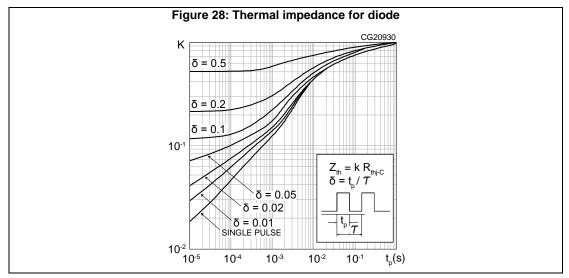


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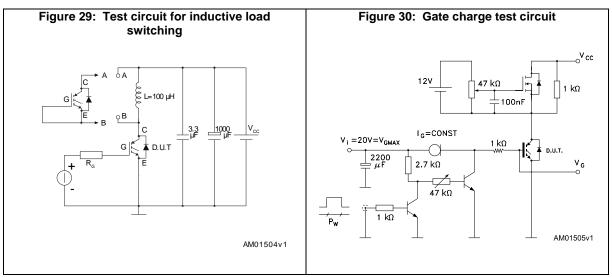


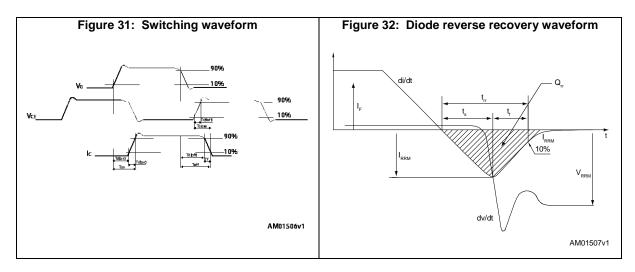


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3 Test circuits







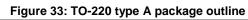
4 Package information

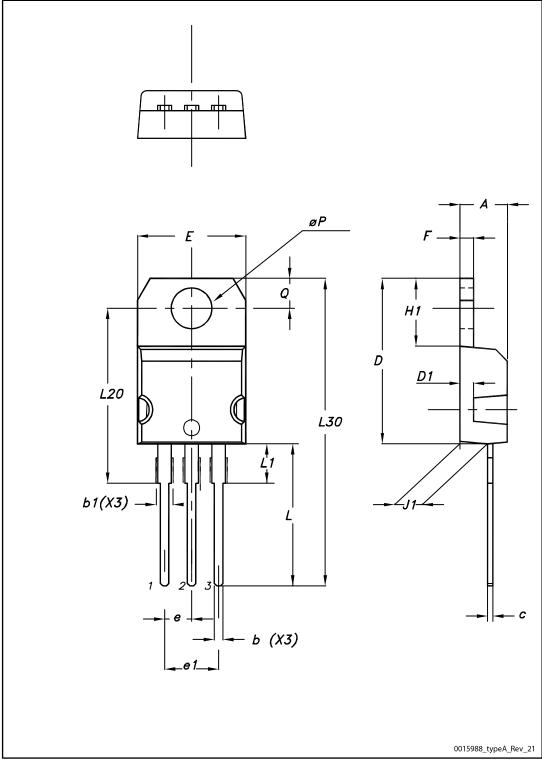
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4.1 TO-220 type A package information





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Package information

STGP4M65DF2

formation STGP4M65DF2					
	Table 8: TO-220 typ	be A mechanical data			
Dim		mm			
Dim.	Min.	Тур.	Max.		
A	4.40		4.60		
b	0.61		0.88		
b1	1.14		1.55		
С	0.48		0.70		
D	15.25		15.75		
D1		1.27			
E	10.00		10.40		
е	2.40		2.70		
e1	4.95		5.15		
F	1.23		1.32		
H1	6.20		6.60		
J1	2.40		2.72		
L	13.00		14.00		
L1	3.50		3.93		
L20		16.40			
L30		28.90			
øP	3.75		3.85		
Q	2.65		2.95		



5 Revision history

Table 9: Document revision history

Date	Revision	Changes	
25-Nov-2015	1	First release.	
30-Mar-2016	2	Modified: features in cover page Datasheet promoted from preliminary data to production data Modified: Table 2: "Absolute maximum ratings", Table 4: "Static characteristics", Table 5: "Dynamic characteristics", Table 6: "IGBT switching characteristics (inductive load)" and Table 7: "Diode switch characteristics (inductive load)" Added: Section 2.1: "Electrical characteristics (curves)" Minor text changes	
21-Nov-2016	3	Updated Table 2: "Absolute maximum ratings" Updated Table 6: "IGBT switching characteristics (inductive load)" Updated Figure 25: "Reverse recovery charge vs. diode current slope" Minor text changes	



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