

# RGT40TM65D

650V 20A Field Stop Trench IGBT

V <sub>CES</sub>	650V
I <sub>C(100°C)</sub>	10A
V <sub>CE(sat) (Typ.)</sub>	1.65V@I <sub>c</sub> =20A
P <sub>D</sub>	39W

# Features

- 1) Low Collector Emitter Saturation Voltage
- 2) Low Switching Loss
- 3) Short Circuit Withstand Time 5µs
- 4) Built in Very Fast & Soft Recovery FRD (RFN - Series)
- 5) Pb free Lead Plating ; RoHS Compliant

# Applications

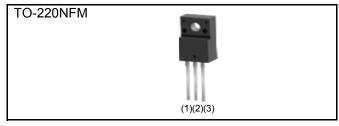
General Inverter

UPS

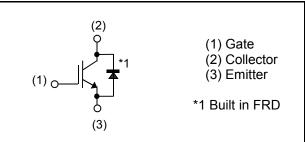
**Power Conditioner** 

Welder

# Outline



#### Inner Circuit



# Packaging Specifications

Туре	Packaging	Tube
	Reel Size (mm)	-
	Tape Width (mm)	-
	Basic Ordering Unit (pcs)	1,000
	Packing Code	C9
	Marking	RGT40TM65D

# •Absolute Maximum Ratings (at T<sub>C</sub> = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V <sub>CES</sub>	650	V
Gate - Emitter Voltage		V <sub>GES</sub>	±30	V
Callester Current	T <sub>C</sub> = 25°C	Ι <sub>C</sub>	17	А
Collector Current	T <sub>C</sub> = 100°C	Ι <sub>C</sub>	10	А
Pulsed Collector Current		I <sub>CP</sub> *1	60	А
Diode Forward Current	T <sub>C</sub> = 25°C	١ <sub>F</sub>	22	А
	T <sub>C</sub> = 100°C	١ <sub>F</sub>	13	А
Diode Pulsed Forward Current		I <sub>FP</sub> <sup>*1</sup>	60	А
Power Discinction	$T_{\rm C}$ = 25°C	P <sub>D</sub>	39	W
Power Dissipation	T <sub>C</sub> = 100°C	P <sub>D</sub>	19	W
Operating Junction Temperature		Tj	-40 to +175	°C
Storage Temperature		T <sub>stg</sub>	-55 to +175	°C
*1 Dulco width limitod by T				

\*1 Pulse width limited by T<sub>jmax.</sub>

## Thermal Resistance

Parameter	Symbol	Values			Unit
Faranielei	Symbol	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	3.82	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	4.47	°C/W

# ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit	
Farameter	Parameter Symbol Conditions		Min.	Тур.	Max.		
Collector - Emitter Breakdown Voltage	BV <sub>CES</sub>	I <sub>C</sub> = 10μΑ, V <sub>GE</sub> = 0V	650	-	-	V	
Collector Cut - off Current	I <sub>CES</sub>	V <sub>CE</sub> = 650V, V <sub>GE</sub> = 0V	-	-	10	μA	
Gate - Emitter Leakage Current	I <sub>GES</sub>	V <sub>GE</sub> = ±30V, V <sub>CE</sub> = 0V	-	-	±200	nA	
Gate - Emitter Threshold Voltage	V <sub>GE(th)</sub>	V <sub>CE</sub> = 5V, I <sub>C</sub> = 13.3mA	5.0	6.0	7.0	V	
Collector - Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = 20A, V <sub>GE</sub> = 15V T <sub>j</sub> = 25°C T <sub>j</sub> = 175°C	-	1.65 2.15	2.1 -	V	

# •IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Deremeter	Queebal	Conditions	Values			11
Parameter	Symbol		Min.	Тур.	Max.	Unit
Input Capacitance	C <sub>ies</sub>	V <sub>CE</sub> = 30V	-	1070	-	
Output Capacitance	C <sub>oes</sub>	V <sub>GE</sub> = 0V	-	45	-	pF
Reverse Transfer Capacitance	C <sub>res</sub>	f = 1MHz	-	18	-	
Total Gate Charge	$Q_g$	V <sub>CE</sub> = 300V	-	40	-	
Gate - Emitter Charge	$Q_{ge}$	I <sub>C</sub> = 20A	-	9	-	nC
Gate - Collector Charge	$Q_{gc}$	V <sub>GE</sub> = 15V	-	15	-	
Turn - on Delay Time	t <sub>d(on)</sub>	I <sub>C</sub> = 20A, V <sub>CC</sub> = 400V	-	22	-	
Rise Time	t <sub>r</sub>	V <sub>GE</sub> = 15V, R <sub>G</sub> = 10Ω	-	27	-	ns
Turn - off Delay Time	$t_{d(off)}$	T <sub>j</sub> = 25°C	-	75	-	
Fall Time	t <sub>f</sub>	Inductive Load	-	60	-	
Turn - on Delay Time	t <sub>d(on)</sub>	I <sub>C</sub> = 20A, V <sub>CC</sub> = 400V	-	22	-	
Rise Time	t <sub>r</sub>	V <sub>GE</sub> = 15V, R <sub>G</sub> = 10Ω	-	29	-	
Turn - off Delay Time	$t_{d(off)}$	T <sub>j</sub> = 175°C	-	84	-	ns
Fall Time	t <sub>f</sub>	Inductive Load	-	120	-	
		I <sub>C</sub> = 60A, V <sub>CC</sub> = 520V			-	
Reverse Bias Safe Operating Area	RBSOA	V <sub>P</sub> = 650V, V <sub>GE</sub> = 15V	FU	LL SQUA	ARE	-
		R <sub>G</sub> = 50Ω, T <sub>j</sub> = 175°C				
		$V_{CC} \leq 360V$				
Short Circuit Withstand Time	t <sub>sc</sub>	V <sub>GE</sub> = 15V	5	-	-	μs
		T <sub>j</sub> = 25°C				

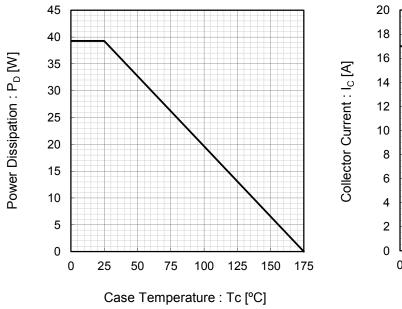
3/11

# •FRD Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Тур.	Max.	Unit
Diode Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 20A T <sub>j</sub> = 25°C T <sub>j</sub> = 175°C	-	1.45 1.25	1.9 -	V
Diode Reverse Recovery Time	t <sub>rr</sub>	$I_F = 20A$ $V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 25^{\circ}C$	-	58	-	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>		-	6.3	-	А
Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	0.20	-	μC
Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20A V <sub>CC</sub> = 400V di <sub>F</sub> /dt = 200A/µs T <sub>j</sub> = 175°C	-	256	-	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>		-	10.4	-	А
Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	1.35	-	μC

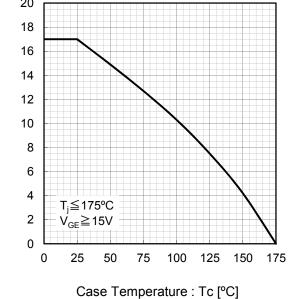
4/11





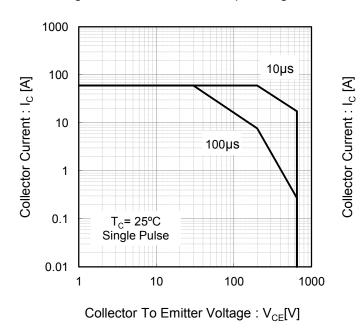
# Fig.1 Power Dissipation vs. Case Temperature

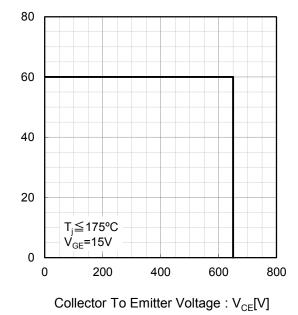
# Fig.2 Collector Current vs. Case Temperature

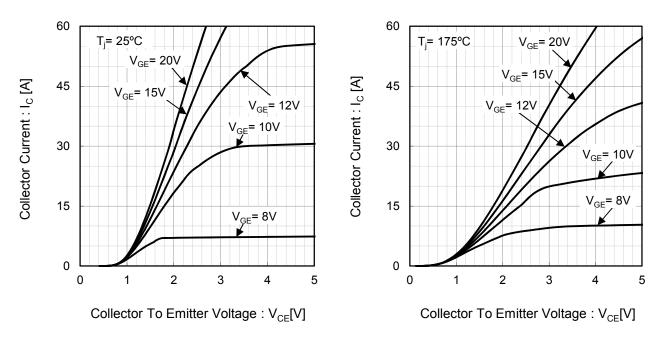


# Fig.3 Forward Bias Safe Operating Area

# Fig.4 Reverse Bias Safe Operating Area

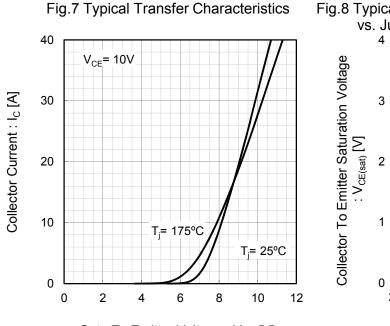






#### Fig.5 Typical Output Characteristics

#### Fig.6 Typical Output Characteristics



Gate To Emitter Voltage : V<sub>GE</sub> [V]

Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature

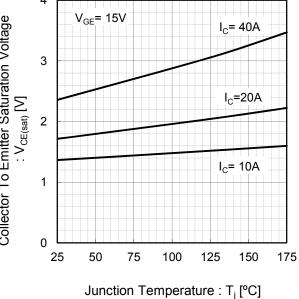


Fig.9 Typical Collector To Emitter Saturation Voltage

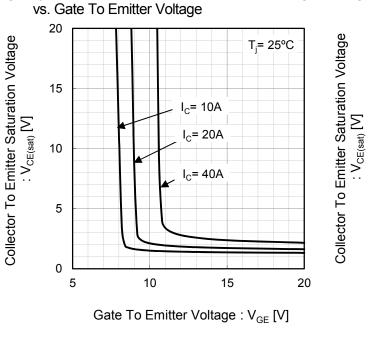


Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

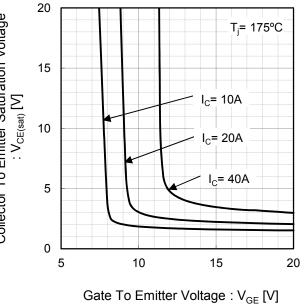
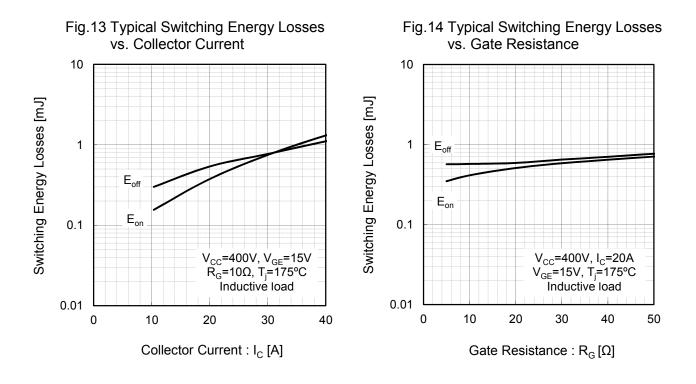


Fig.11 Typical Switching Time vs. Collector Current 1000 V<sub>CC</sub>=400V, V<sub>GE</sub>=15V R<sub>G</sub>=10Ω, T<sub>j</sub>=175°C Inductive load Switching Time [ns] tf 100 t<sub>d(off)</sub> t<sub>d(on)</sub> t, 10 0 10 20 30 40 Collector Current : I<sub>C</sub> [A]

Fig.12 Typical Switching Time vs. Gate Resistance 1000 V<sub>CC</sub>=400V, I<sub>C</sub>=20A V<sub>GE</sub>=15V, T<sub>j</sub>=175°C Inductive load Switching Time [ns] 100 t<sub>d(off)</sub> t<sub>d(on)</sub> 10 10 20 30 0 40 50 Gate Resistance :  $R_G[\Omega]$ 



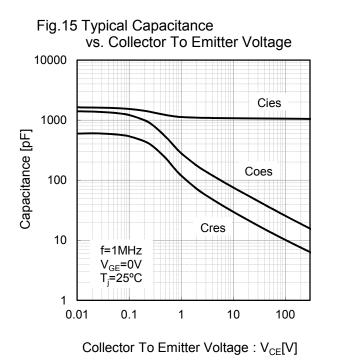
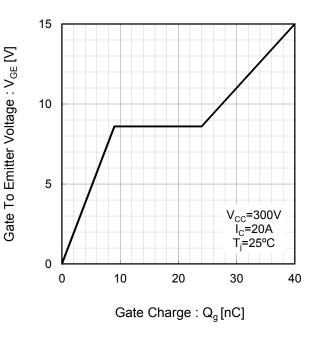
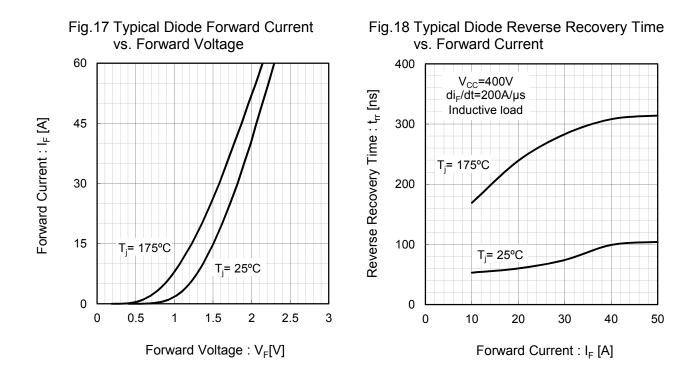


Fig.16 Typical Gate Charge





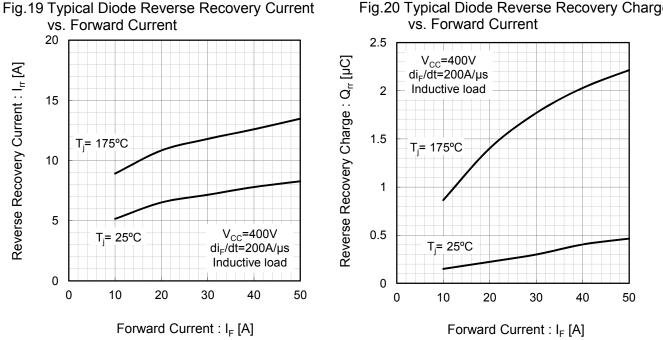


Fig.20 Typical Diode Reverse Recovery Charge

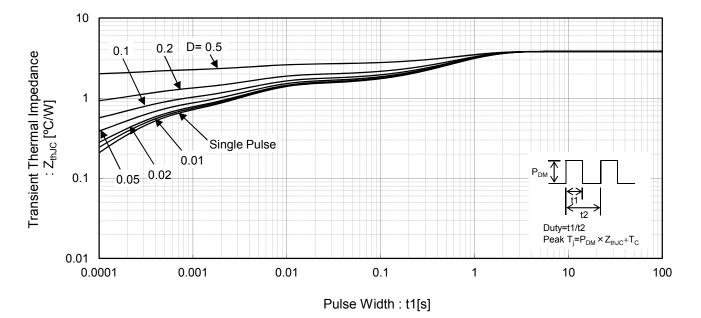
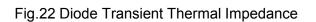
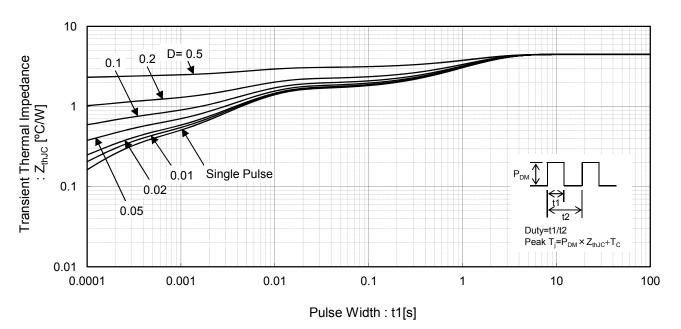


Fig.21 IGBT Transient Thermal Impedance





# ●Inductive Load Switching Circuit and Waveform

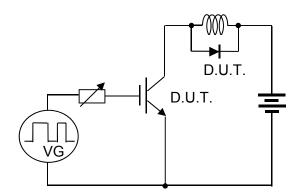


Fig.23 Inductive Load Circuit

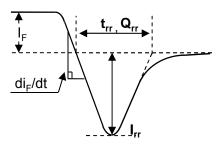


Fig.25 Diode Reverce Recovery Waveform

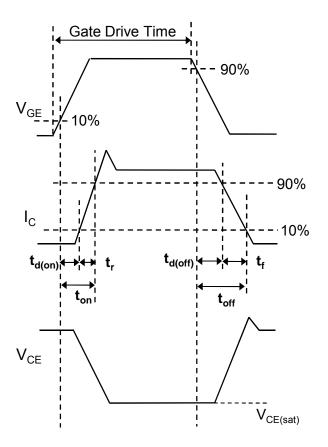


Fig.24 Inductive Load Waveform

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