KEEN SIDE
electronics

LCD MODULE

MODULE NO. :

KSEGG12864-59 SERIES

Customer:
Approved by:

Approved by	Checked by	Prepared by

RECORDS OF REVISION

Part Number	Revision	Revision Content	Revised on
KSEGG12864-59	00	First issue	June. 24th, 2016

CONTENTS

1. FUNCTIONS & FEATURES

● KSEGG12864-59 Series LCD type：
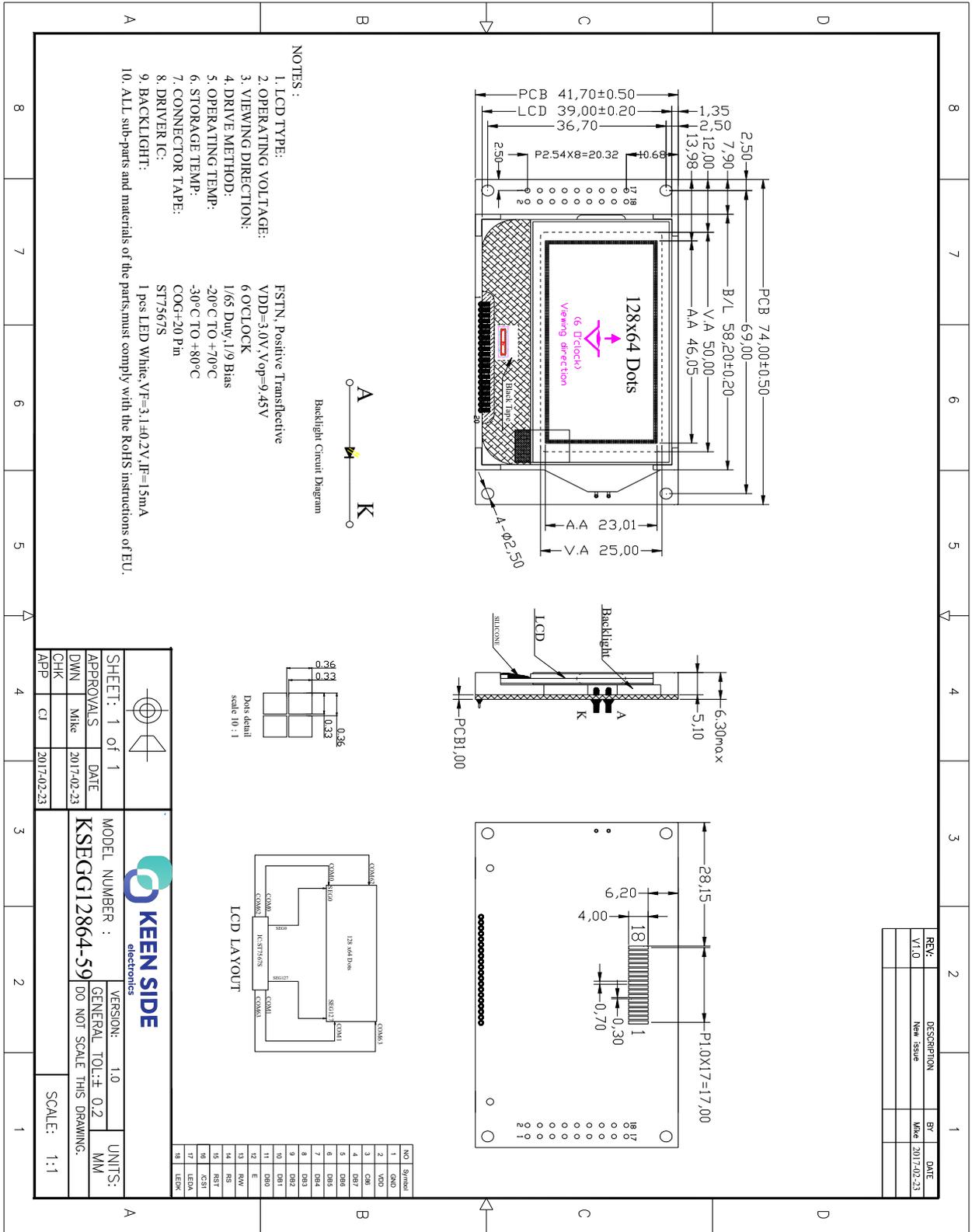
Module	LCD Type	Remark
KSEGG12864-59FWZ	FSTN Transflective Positive Mode	6 O' clock

● Display Contents   :128 x 64 Dots

● Driving Scheme   : 1/65Duty; 1/9Bias

● Viewing Direction   :6 O' clock

● Power Supply Voltage   : 3.0V.

● Driver IC   :ST7567S

● Interface   :Parallel

● Backlight   :white

● Operating Temperature   :-20℃ ﹣﹢70℃

● Storage Temperature   :-30℃ ﹣﹢80℃

● RoHS Compliant

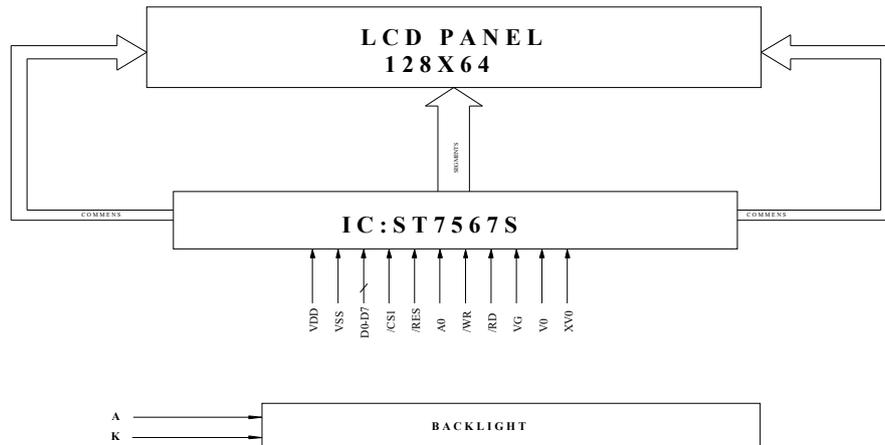● KSE   KEEN SIDE electronics

2. MECHANICAL SPECIFICATIONS

● Outline Dimensions   : 74.00(W) x 41.70(L) x 6.30(H)(mm)

● Viewing Area   : 50.00 (W) x 25.00(L)(mm)

● Active Area   : 46.05 (W) x 23.01 (L)(mm)

● Dot Pitch   : 0.36 (W) x 0.36 (L)(mm)

● Dot Size   : 0.33 (W) x 0.33 (L)(mm)

● Weight   : TBD

3. EXTERNAL DIMENSIONS

NOTES :
1. LCD TYPE: FSTN, Positive Transflective
2. OPERATING VOLTAGE: VDD=3.0V,Vop=9.45V
3. VIEWING DIRECTION: 6 O'CLOCK
4. DRIVE METHOD: 1/65 Duty,1/9 Bias
5. OPERATING TEMP: -20°C TO +70°C
6. STORAGE TEMP: -30°C TO +80°C
7. CONNECTOR TAPE: COG+20 Pin
8. DRIVER IC: ST7567S
9. BACKLIGHT: 1 pcs LED White,VF=3.1±0.2V,IF=15mA
10. ALL sub-parts and materials of the parts,must comply with the RoHS instructions of EU.

Backlight Circuit Diagram

A   K

128x64 Dots

(6 O'clock)
Viewing direction

PCB 41,70±0.50
LCD 39,00±0.20
36,70
P2.54X8=20.32
1,35
2,50
13,98
12,00
7,90
2,50

PCB 74,00±0.50
69,00
B/L 58,20±0.20
V.A 50,00
A.A 46,05

A.A 23,01
V.A 25,00
4-Φ2.50

SILICONE
LCD
Backlight
K   A
PCB1,00
6.30max
5,10

Dots detail
scale 10 : 1
0.36
0.33
0.33
0.36

LCD LAYOUT
128 x64 Dots

28,15
6,20
4,00
18
1
0,70
0,30
P1.0X17=17,00

NO	Symbol
1	GND
2	VDD
3	C86
4	DB7
5	DB6
6	DB5
7	DB4
8	DB3
9	DB2
10	DB1
11	DB0
12	E
13	RW
14	RS
15	RST
16	/CSt
17	LEDA
18	LEDK

APPROVALS
SHEET: 1 of 1
APP
CHK
DWN   Mike   2017-02-23
CHK
APP   C1   2017-02-23
DATE

MODEL NUMBER :
KSEGG12864-59

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VERSION: 1.0
GENERAL TOL:± 0.2
DO NOT SCALE THIS DRAWING.
UNITS: MM
SCALE: 1:1

REV:	DESCRIPTION	BY	DATE
V1.0	New issue	Mike	2017-02-23

Version: 00   5

4. BLOC`K DIAGRAM

5. PIN ASSIGNMENT

Pin No.	Symbol	Function
1	GND	Ground terminal of module.
2	VDD	Power terminal of module
3	C86	This is the MPU interface selection pin. C86 = "H": 6800 Series MPU interface. C86 = "L": 8080 Series MPU interface.
4-11	D0-D7	This is an 8-bit bi-directional data bus that connects to an 8-bit or 16-bit standard MPU data bus. When the serial interface (SPI-4) is selected (P/S = "L") : D7: serial data input (SI); D6: the serial clock input (SCL). D0 to D5 should be connected to VDD or floating. When the chip select is not active, D0 to D7 are set to high impedance.
12	/RD(E)	• When connected to 8080 series MPU, this pin is treated as the "/RD" signal of the 8080 MPU and is LOW-active. The data bus is in an output status when this signal is "L". • When connected to 6800 series MPU, this pin is treated as the "E" signal of the 6800 MPU and is HIGH-active. This is the enable clock input terminal of the 6800 Series MPU.
13	/WR(R/W)	• When connected to 8080 series MPU, this pin is treated as the "/WR" signal of the 8080 MPU and is LOW-active. The signals on the data bus are latched at the rising edge of the /WR signal. • When connected to 6800 series MPU, this pin is treated as the "R/W" signal of the 6800 MPU and decides the access type : When R/W = "H": Read. When R/W = "L": Write.
14	RS	This is connecting to the least significant bit of the normal MPU address bus, and it determines whether the data bits are data or command. RS = "H": Indicates that D0 to D7 are display data. RS = "L": Indicates that D0 to D7 are control data.
15	/RES	When /RES is set to "L", the register settings are initialized (cleared). The reset operation is performed by the /RES signal level.
16	/CS1	This is the chip select signal.
17	LEDA	Anode of Backlight
18	LEDK	Cathode of Backlight

6. BACKLIGHT ELECTRICAL/OPTICAL SPECIFICATIONS

Electrical/Optical Specifications

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
Forward Voltage	Vf	2.9	3.1	3.3	V	If= 15 x1 mA
Reverse Current	Ir			120	mA	Vr=5.0 V
Power Dissipation	Pd			275	mW	If= 15 x1 mA
Dominant wave length	X		0.299	0.33		If= 15 x 1 mA
	Y		0.313	0.33	-	
Luminous	LV	85			cd/m2	If= 15 x 1 mA
Luminous Uniformity	△Lv	70			%	If= 15 x 1 mA
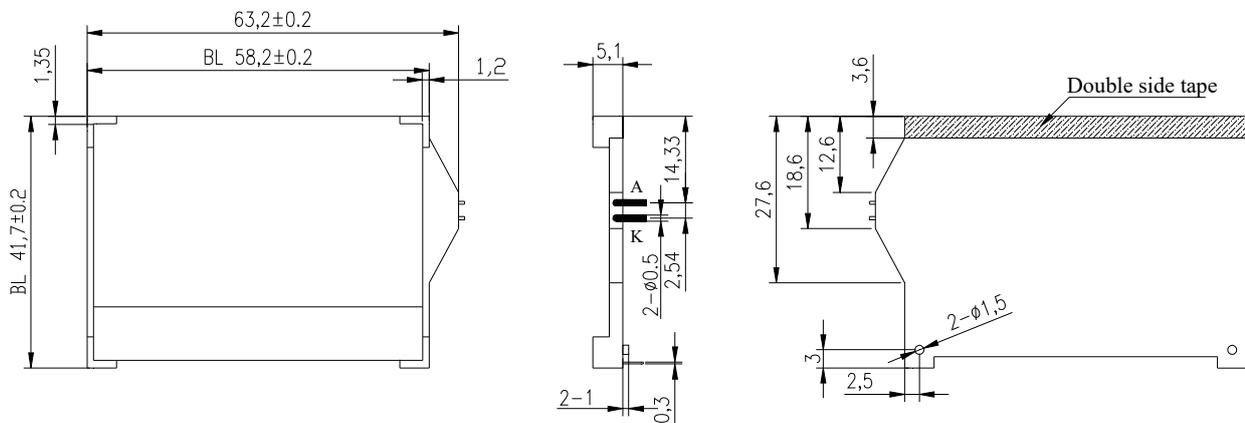
7. MAXIMUM ABSOLUTE POWER RATINGS

Item	Symbol	Standard value	Unit
Power supply voltage(1)	VDD	-0.3~+3.6	V
Power supply voltage(2)	VOUT,, V0,	-0.3 ~ 13.5	V
Power supply voltage(3)	VIN	-0.3 ~ V0	V
Operating temperature	Topr	-20~+70	℃
Storage temperature	Tstg	-30~+80	℃

Version: 00

8. ELECTRICAL CHARACTERISTICS

8-1   DC Characteristics

Item	Symbol	Standard Value			Test Condition	Unit
		Min	Typ	Max		
Operating Voltage	V_{DD} -Vss	2.8	3.0	3.2	TA=25℃	V
Supply Current	I_{DD}	----	TBD	5.0	--	mA
LCD Driving Voltage	V_0-V_{ss}	9.25	9.45	9.65	TA=25℃	V

8-2 AC Characteristics

8.2.1 Read/Write mode for the 8080 Series MPU
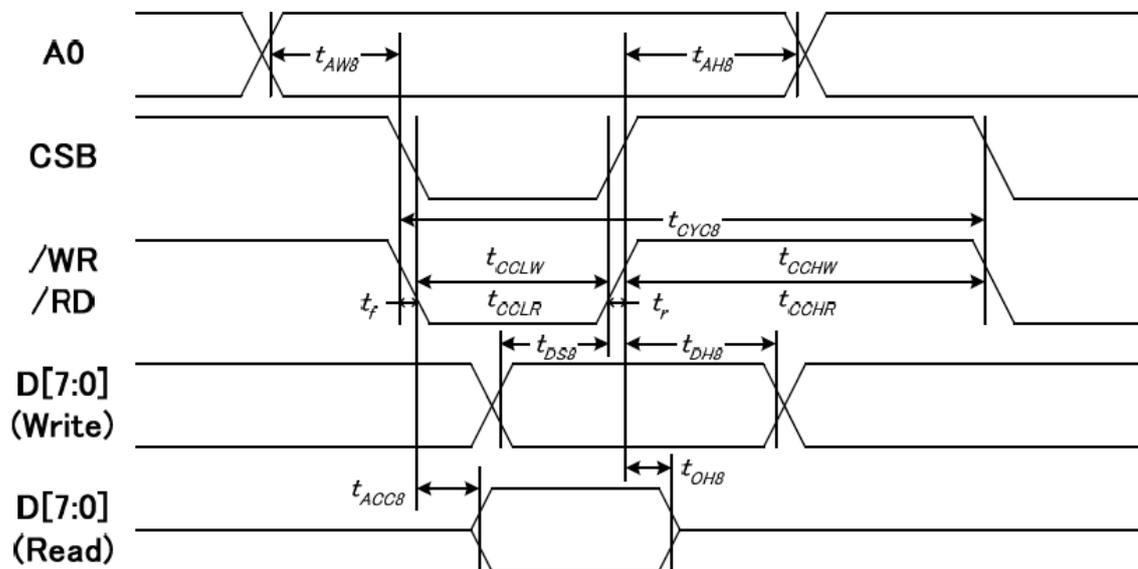
(VDD1 = 3.3V , Ta =25˚C)

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Address setup time	A0	tAW8		0	—	
Address hold time		tAH8		10	—	
System cycle time	/WR	tCYC8		240	—	
/WR L pulse width (WRITE)		tCCLW		80	—	
/WR H pulse width (WRITE)		tCCHW		80	—	
/RD L pulse width (READ)	RD	tCCLR		140	—	ns
/RD H pulse width (READ)		tCCHR		80		
WRITE Data setup time	D[7:0]	tDS8		40	—	
WRITE Data hold time		tDH8		20	—	
READ access time		tACC8	CL = 16 pF	—	70	
READ Output disable time		tOH8	CL = 16 pF	5	50	

8.2.2 Read/Write mode for the 6800 Series MPU
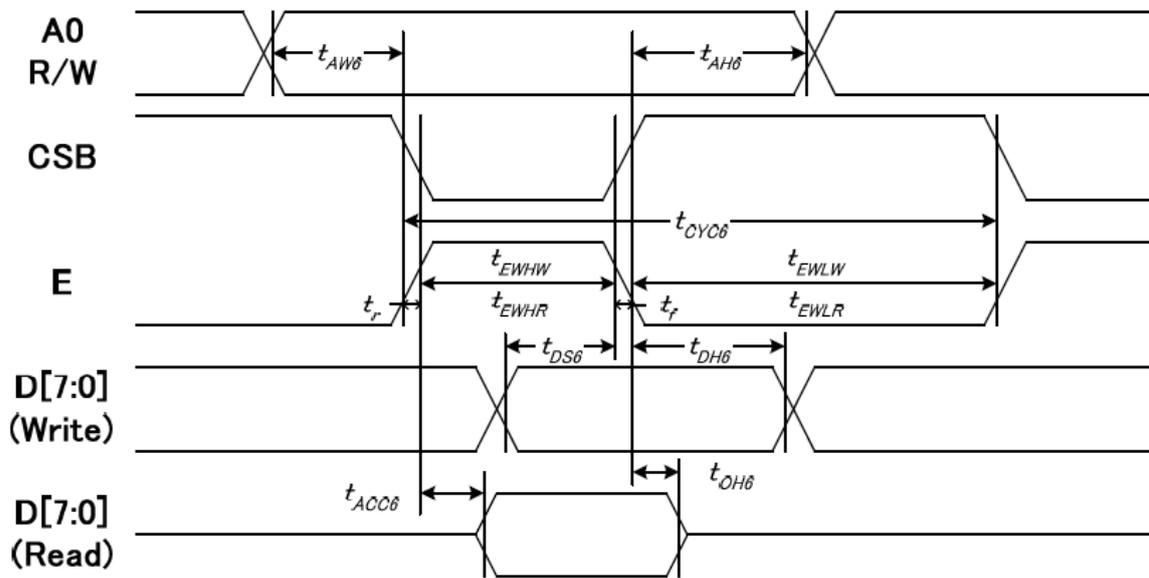
(VDD1 = 3.3V , Ta =25°C)

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Address setup time	A0	tAW6		0	—	
Address hold time		tAH6		10	—	
System cycle time	E	tCYC6		240	—	
Enable L pulse width (WRITE)		tEWLW		80	—	
Enable H pulse width (WRITE)		tEWHW		80	—	
Enable L pulse width (READ)		tEWLR		80	—	ns
Enable H pulse width (READ)		tEWHR		140		
Write data setup time	D[7:0]	tDS6		40	—	
Write data hold time		tDH6		10	—	
Read data access time		tACC6	CL = 16 pF	—	70	
Read data output disable time		tOH6	CL = 16 pF	5	50	

9. INSTRUCTION TABLE

INSTRUCTION	A0	R/W (RWR)	COMMAND BYTE								DESCRIPTION
			D7	D6	D5	D4	D3	D2	D1	D0	
(1) Display ON/OFF	0	0	1	0	1	0	1	1	1	D	D=1, display ON D=0, display OFF
(2) Set Start Line	0	0	0	1	S5	S4	S3	S2	S1	S0	Set display start line
(3) Set Page Address	0	0	1	0	1	1	Y3	Y2	Y1	Y0	Set page address
(4) Set Column Address	0	0	0	0	0	1	X7	X6	X5	X4	Set column address (MSB)
	0	0	0	0	0	0	X3	X2	X1	X0	Set column address (LSB)
(5) Read Status	0	1	0	MX	D	RST	0	0	0	0	Read IC Status
(6) Write Data	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write display data to RAM
(7) Read Data	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read display data from RAM
(8) SEG Direction	0	0	1	0	1	0	0	0	0	MX	Set scan direction of SEG MX=1, reverse direction MX=0, normal direction
(9) Inverse Display	0	0	1	0	1	0	0	1	1	INV	INV =1, inverse display INV =0, normal display
(10) All Pixel ON	0	0	1	0	1	0	0	1	0	AP	AP=1, set all pixel ON AP=0, normal display
(11) Bias Select	0	0	1	0	1	0	0	0	1	BS	Select bias setting 0=1/9; 1=1/7 (at 1/65 duty)
(12) Read-modify-Write	0	0	1	1	1	0	0	0	0	0	Column address increment: Read:+0 , Write:+1
(13) END	0	0	1	1	1	0	1	1	1	0	Exit Read-modify-Write mode
(14) RESET	0	0	1	1	1	0	0	0	1	0	Software reset
(15) COM Direction	0	0	1	1	0	0	MY	-	-	-	Set output direction of COM MY=1, reverse direction MY=0, normal direction
(16) Power Control	0	0	0	0	1	0	1	VB	VR	VF	Control built-in power circuit ON/OFF
(17) Regulation Ratio	0	0	0	0	1	0	0	RR2	RR1	RR0	Select regulation resistor ratio
(18) Set EV	0	0	1	0	0	0	0	0	0	1	Double command!! Set electronic volume (EV) level
	0	0	0	0	EV5	EV4	EV3	EV2	EV1	EV0	
(19) Set Booster	0	0	1	1	1	1	1	0	0	0	Double command!! Set booster level: BL=0: 4X BL=1: 5X
	0	0	0	0	0	0	0	0	0	BL	
(20) Power Save	0	0	Compound Command								Display OFF + All Pixel ON
(21) NOP	0	0	1	1	1	0	0	0	1	1	No operation
(22) Test	0	0	1	1	1	1	1	1	1	-	Do NOT use. Reserved for testing.

Note: Symbol "-" means this bit can be "H" or "L".

10. Optical Characteristics

10.1 Definition of Viewing Angle
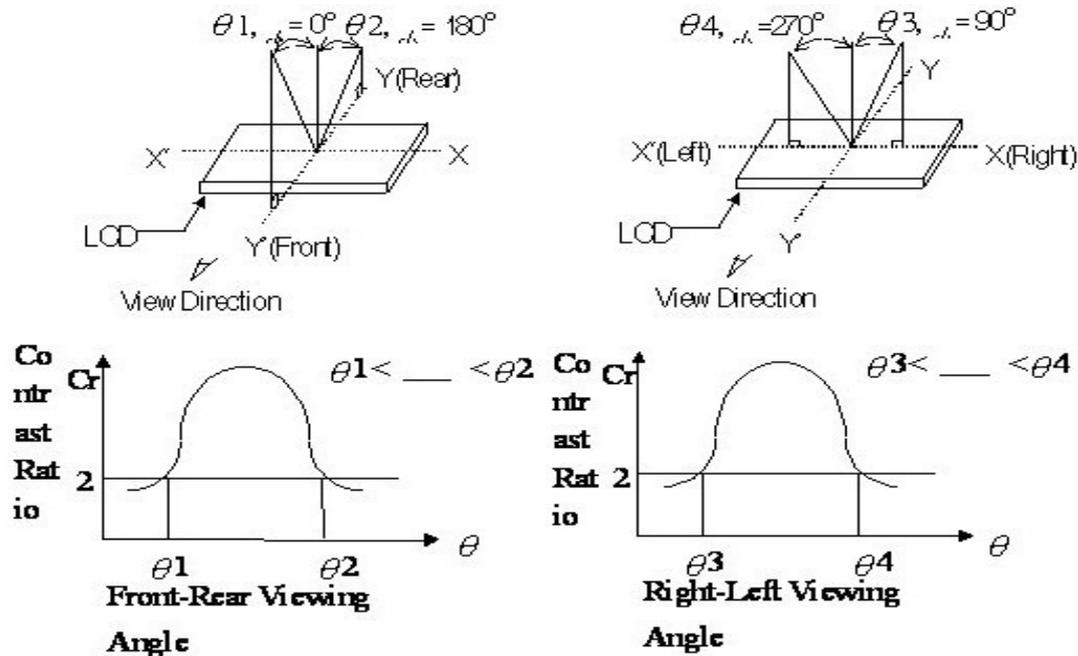
10.2 Definition of Contrast

$$C.R = \frac{\text{RATIO}}{\text{Brightness of nonselected segment (B2)}}{\text{Brightness of selected segment}}$$
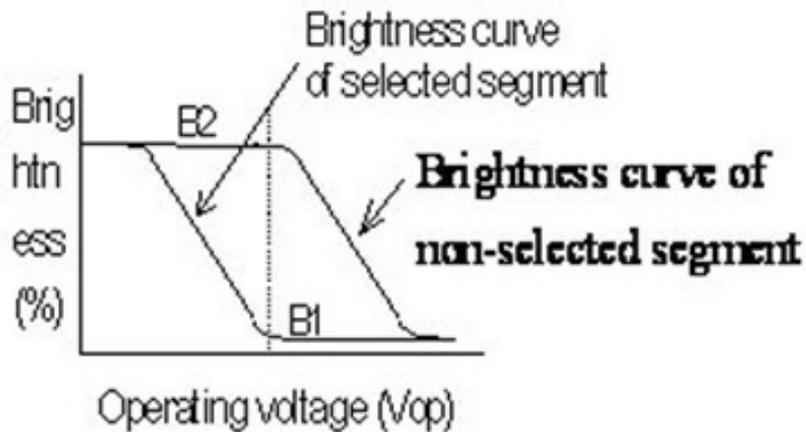
10.3 Definition of Response
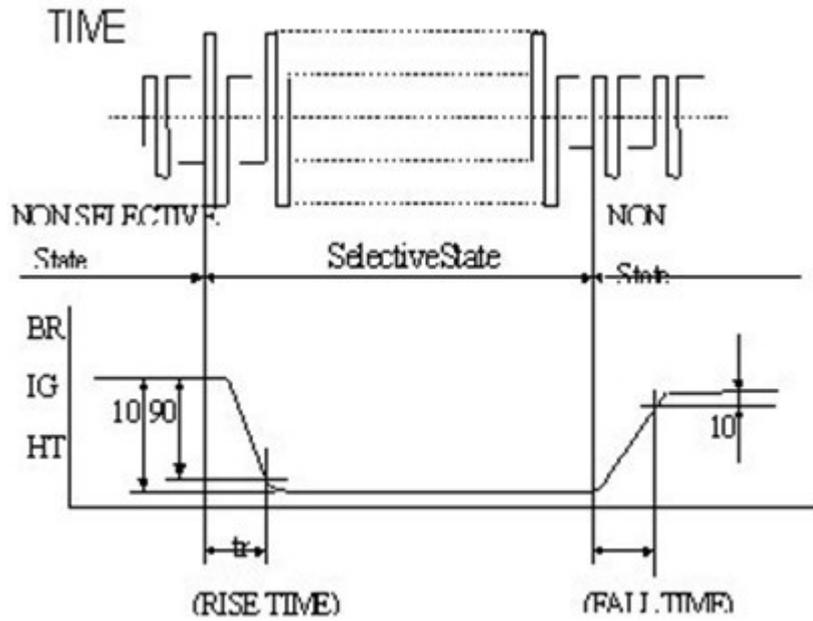
10.4 Measuring Instruments For Electro-optical Characteristics
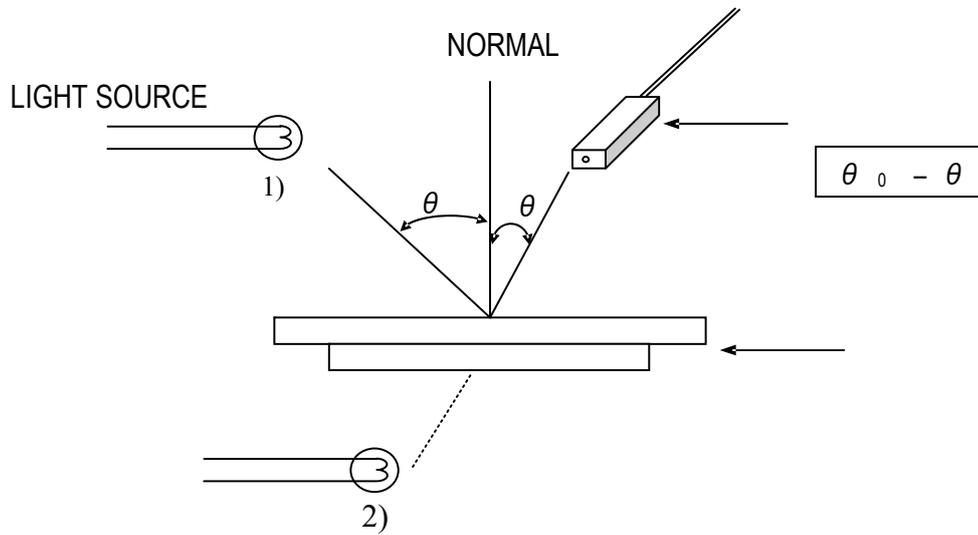
* Note:
1) Light source position for measuring the reflective type of LCD panel;
2) Light source position for measuring the transflective / transmissive types of LCD panel.

11. MODULE ACCEPT QUALITY LEVEL (AQL)

11.1 AQL Standard Value: Critical Defect =0.1, Major Defect=0.65; Minor Defect =2.5
11.2 Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level II

12. RELIABILITY TEST

Operating life time: Longer than 75,000 hours

          (at room temperature without direct irradiation of sunlight)

   Reliability characteristics shall meet following requirements.

No.	Test Item	Content of Test	Test Condition
1	High Temperature Storage	Endurance test applying the high storage temperature for a long time	+80°C 96H
2	Low Temperature Storage	Endurance test applying the low storage temperature for a long time	−30°C 96H
3	High Temperature Operation	Endurance test applying the electric stress (voltage & current) and the thermal stress to the element for a long time	+70°C 96H
4	Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time	−20°C 96H
5	High Temperature/ Humidity Storage	Endurance test applying the high temperature and humidity storage for a long time	60°C 90%RH 96H
6	Temperature Cycle	Endurance test applying the low and high temperature cycle −20°C ⟷ 25°C ⟷ 70°C ⟷ 25°C 30min 5min 30min 5min ⟵————————————————⟶ 1 cycle	−20°C/70°C 5 cycles
7	Vibration Test (Package State)	Endurance test applying the vibration during transportation	10Hz ₋ 55Hz, 50m/s, 15min
8	Shock Test (Package State)	Endurance test applying the shock during transportation	Half-sinewave, 100m/s, 11ms
9	Atmospheric Pressure Test	Endurance test applying the atmospheric pressure during transportation by air	40 kPa 16 H

13. Inspection specification

13.1 Visual Inspection

1) Inspect under 2x20W or 40W fluorescent lamp (approximately 3000 lux) leaving 25 to 30 cm between the module and the lamp and 30 cm between the module and the eye (measuring position).

2) Appearance is inspected at the best contrast voltage (best contrast is adjusted considering clearness and crosstalk on screen).

3) Inspect the module at 45° right and left, top and bottom.

4) Use the optimum viewing angle during the contrast inspection.
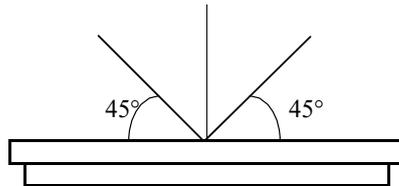
13.2 Standard of Appearance Inspection

No.	Item	Criteria			
1	Black spot White spot Dust	Round type: as per following drawing Line type: as per following drawing Total acceptable quantity: 3			
2	Polariser	Scratch on protective film is permitted			

Round type: as per following drawing

Acceptable quantity		
Size	Zone A	Zone B
$\Phi < 0.1$	Any number	
$0.1 < \Phi < 0.2$	2	Any number
$0.2 < \Phi < 0.25$	1	
$0.25 < \Phi$	0	

Line type: as per following drawing

Acceptable quantity			
Length	Width	Zone A	Zone B
—	$W \leq 0.02$	Any number	Any number
$L \leq 3.0$	$0.02 < W \leq 0.03$	2	
$L \leq 2.5$	$0.03 < W \leq 0.05$		
—	$0.05 < W$	As round type	

| scratch | Scratch on polariser: same as No. 1 |

| 3 | Polariser bubble | Total acceptable quantity: 3 | | | |
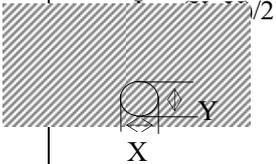
			Acceptable quantity		
			Size	Zone A	Zone B
			$\Phi <0.2$	Any number	Any number
			$0.2< \Phi <0.5$	2	
			$0.5< \Phi <1.0$	1	
			$1.0< \Phi$	0	

4.1 Pin hole on segmented display

W: segment width

$\Phi = (A+B)/2$
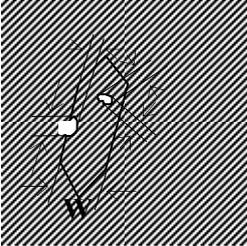
Acceptable quantity	
Width	Quantity
W≤0.4	$\Phi \leq 0.2$ and $\Phi \leq 1/2W$
W>0.4	$\Phi \leq 0.25$ and $\Phi \leq 1/3W$

Total acceptable quantity: 1 defect per segment

Pin holes with Φ under 0.10 mm are acceptable

4.2 Pin hole on dot matrix display

| 4 | Segment deformation |

≤0.05

Total acceptable quantity: 7

4.3 Segments / dots with different width

Acceptable quantity	
Size	
a, b<0.1	Any number
(a+b)/2≤0.1	Any number
$0.5< \Phi <1.0$	3
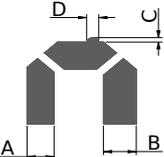
Acceptable	
a≥b	a/b≤4/3
a<b	a/b>4/3

4.4 Alignment layer defect

Φ = (A+B)/2

Total acceptable quantity: 7

Acceptable quantity	
Size	
Φ ≤0.4	Any number
0.4< Φ ≤1.0	5
1.0< Φ ≤1.5	3
1.5< Φ ≤2.0	2

5	Colour uniformity	Level of sample for approval set as limit sample
6	Backlight	The backlight colour should correspond to the product specification Flashing and or unlit backlight is not allowed Dust larger than 0.25 mm is not allowed
7	COB	Exposed wire bond pad is not allowed Insufficient covering with resin is not allowed (wire bond line exposed) Dust or bubble on the resin are not allowed
8	PCB	No unmelted solder paste should be present on PCB Cold solder joints, missing solder connections, or oxidation are not allowed No residue or solder balls on PCB are allowed Short circuits on components are not allowed
9	Tray particles	

Acceptable quantity		
	Size	Quantity
On tray	Φ <0.2	Any number
	Φ >0.25	4
On display	Φ ≥0.25	2
	L = 3	1

14. LCD MODULES HANDLING PRECAUTIONS

- Please remove the protection foil of polarizer before using.
- The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

- If the display panel is damaged and the liquid crystal substance inside it leaks out, do not get any in your mouth. If the substance come into contact with your skin or clothes promptly wash it off using soap and water.

- Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

- The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarize carefully.

- To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  -Be sure to ground the body when handling the LCD module.
  -Tools required for assembly, such as soldering irons, must be properly grounded.
  -To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
  -The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

- Storage precautions
  When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags designed to prevent static electricity charging under low temperature / normal humidity conditions (avoid high temperature / high humidity and low temperatures below 0

  ℃).Whenever possible, the LCD modules should be stored in the same conditions in which they

  were shipped from our company.

15. OTHERS

- Liquid crystals solidify at low temperature (below the storage temperature range) leading to defective orientation of liquid crystal or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to a strong shock at a low temperature.

- If the LCD modules have been operating for a long time showing the same display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. Abnormal operating status can be resumed to be normal condition by suspending use for some time. It should be noted that this phenomena does not adversely affect performance reliability.

- To minimize the performance degradation of the LCD modules resulting from caused by static electricity, etc. exercise care to avoid holding the following sections when handling the modules:
  - Exposed area of the printed circuit board
  - Terminal electrode sections