



LCD MODULE

MODULE NO.:

KSEGB12864XXX-08 SERIES

Customer.									
Approved by:									
Approved by	Checked by	Prepared by							

RECORDS OF REVISION

Part Number	Revision	Revision Content	Revised on
KSEGB12864XXX-08	1.0	First issue	Oct. 15th, 2022

CONTENTS

1. MODULE CLASSIFICATION INFORMATION	3
2. FUNCTIONS & FEATURES	4
3. MODULE ARTWORK	4
4. EXTERNAL DIMENSIONS	5
5. BLOCK DIAGRAM	6
6. PIN ASSIGNMENT	6
7. BACKLIGHT ELECTRICAL/OPTICAL SPECIFICATIONS	7
8. ABSOLUTE MAXIMUM RATINGS(Vss=0V, Ta=25C)	7
9. ELECTRICAL CHARACTERISTICS	7
10. COMMAND DEFINITIONS	9
11. OPTICAL CHARACTERISTICS	11
12. MODULE ACCEPT QUALITY LEVEL (AQL)	
13. RELIABILITY TEST	13
14. INSPECTION SPECIFICATION	14
15. LCD MODULES HANDLING PRECAUTIONS	17
16 OTHERS	17

1. MODULE CLASSIFICATION INFORMATION

KSE G B 12864 - X X X - 08 - V

(1) KSE: KEEN SIDE electronics

(3)

② C: Character Type, G: Graphic Type

(4)

③ B: COB, G: COG

2

(1)

4 Display Font 128 * 64

 \bigcirc LCD Mode: B \rightarrow STN-Blue Negative F \rightarrow FSTN Positive

 $G \rightarrow STN$ Gray Positive $Y \rightarrow STN$ Yellow Green Positive

⑥ Backlight Type· N→ Without backlight A→ Amber LED backlight

 $B \rightarrow Blue LED$ backlight $G \rightarrow Green LED$ backlight

(5) (6) (7)

R→ Red LED backlight W→ Withe LED backlight

Y→ Yellow-Green LED backlight

7 LCD Polarizer Type/Temperature range/View direction :

D→ Transflective, W.T, 12:00 E→ Transmissive, W.T, 6:00

P→ Reflective, W. T, 6:00 Q→ Transmissive, W.T, 12:00

Z→ Transflective, W.T, 6:00

(8) Model serials no. :

Special Code: V: Built in negative voltage& Temperature Compensation

2. FUNCTIONS & FEATURES

• Driving Scheme : 1/64Duty, 1/9 Bias

• Power Supply for logic : 5.0V

• Display Content : 128*64 Dots

 \bullet V_{LCD} : 9.0V

• Driver IC : AIP31070&AIP31080

• Operation temperature : -20°C to $+70^{\circ}\text{C}$ • Storage temperature : -30°C to $+80^{\circ}\text{C}$

3. MODULE ARTWORK

• Module Size : 75.00(L)*52.60 (W)*9.00(H)mm

• Viewing Area : 60.00(L)mm*32.50 (W)mm

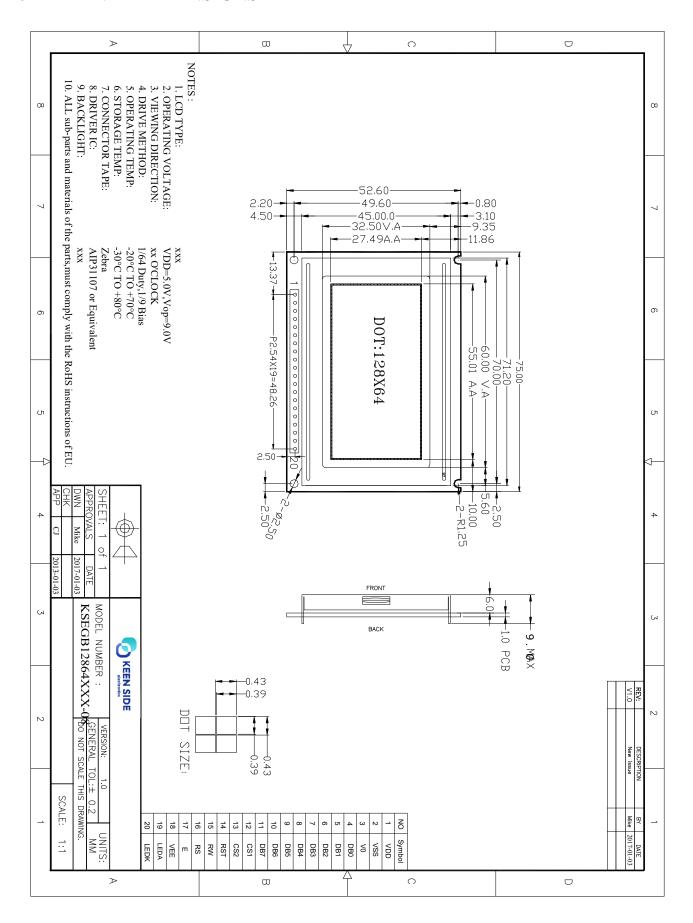
• Active Area : 55.01(L)mm*27.49(W)mm

• Dot Pitch : 0.43 (W)mm*0.43 (H)mm

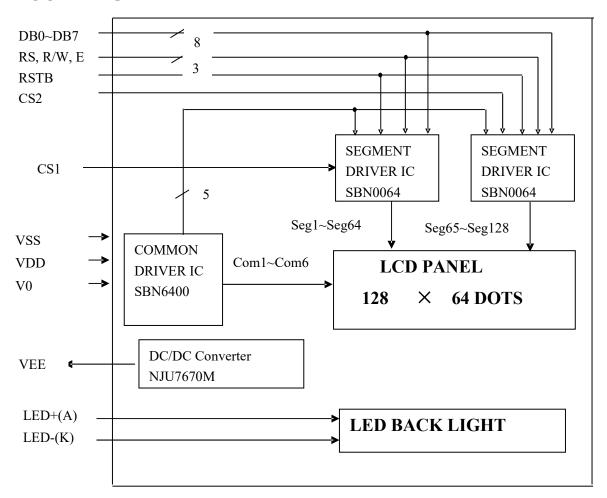
• Dot Size : 0.39(W)mm*0.39 (H)mm

• Dot Gap : 0.04 mm

4. EXTERNAL DIMENSIONS



5. BLOCK DIAGRAM



6. PIN ASSIGNMENT

Pin No.	Symbol	Function					
1	VDD	Power supply voltage for logic.					
2	VSS	Ground					
3	V0	Input voltage for LCD					
4	DB0	Data bit 0					
5	DB1	Data bit 1					
6	DB2	Data bit 2					
7	DB3	Data bit 3					
8	DB4	Data bit 4					
9	DB5	Data bit 5					
10	DB6	Data bit 6					
<u>11</u>	DB7	Data bit 7					
12	CS1	Chip select signal for SBN0064(1)					
13	CS2	Chip select signal for SBN0064(2)					
14	RST	Reset signal					
15	R/W	Read /Write $R/W = 1 \cdots Read$ $R/W = 0 \cdots Write$					
16	RS	Register select					
10	KS	RS = 0···Instruction register $RS = 1$ ···Data register					
17	Е	Chip enable signal					
18	VEE	Output voltage for LCD,-9.0V					
19	LEDA	Anode of LED backlight					
20	LEDK	Cathode of LED backlight					

7. BACKLIGHT ELECTRICAL/OPTICAL SPECIFICATIONS

Item	Symbol	MIN.	TYP.	MAX.	Unit	Conditions
Forward Voltage	Vf	4.8	5.0	5. 2	V	If=15*3mA
Forward Current	If		45		mA	
Power Dissipation	Pd		0.30		W	If=15*3mA
Reverse Voltage	VR			5.0	V	
Reverse Current	IR			0.4	mA	
Luminous Intensity	Lv	100			cd/m ²	If=15*3mA
Luminous Uniformity	ΔLv	70			%	If = 15*3mA
Color Coordinate	X	0.27		0.32		If=15*3mA
Color Coordinate	у	0.27	-	0.32		White

8. ABSOLUTE MAXIMUM RATINGS(Vss=0V, Ta=25C)

Item	Symbol	Standard value	Unit
Supply voltage for logic	$V_{ m DD}$	-0.3~+7.0	V
Supply voltage	V_0	V _{DD} -19.0~V _{DD} +0.3	V
Driver supply voltage	$ m V_{DD}$ - $ m V_0$	V ₀ -0.3~V _{DD} +0.3	V
Operating temperature	Topr	-20~+70	$^{\circ}$
Storage temperature	Tstg	-30~+80	$^{\circ}$

9. ELECTRICAL CHARACTERISTICS

1). DC Characteristics

Item	Symbol	Stan	dard Va	alue	Test	Unit	
Item	Symbol	MIN	TYP	MAX	Condition		
Supply current for logic	V_{DD}	4.8	5.0	5.2		V	
Supply current for logic	I_{DD}		2.7	5		mA	
		10.8	11.0	11.2	-20℃		
operating Voltage for LCD	$ m V_{DD} ext{-}V_0$	8.8	9.0	9.2	25℃	V	
		8.0	8.2	8.4	70℃]	
Input voltage "H" level	VIH	$0.7V_{DD}$		V_{DD}		V	
Input voltage "L" level	VIL	0		$0.3V_{DD}$		V	

2). AC Characteristics

Characteristic	Symbol	Min	Тур	Max	Unit
E Cycle	t_{C}	1000	-	-	ns
E Rise	t_{R}	-	-	25	ns
E Fall	$t_{ m F}$	-	-	25	ns
E High Level Width	t _{WH}	450	-	_	ns
E-Low Level Width	t _{WL}	450	-	-	ns
Address Set –Up Time	t _{ASU}	140	-	-	ns
Address Hold Time	t _{AH}	10	-	-	ns
Data Delay Time	t_{D}	-	-	320	ns
Data Set –Up Time	$t_{ m DSU}$	200	-	-	ns
Data Hold Time (Write)	$t_{ m DHW}$	10	-	-	ns
Data Hold Time (Read)	$t_{ m DHR}$	20	-	_	ns

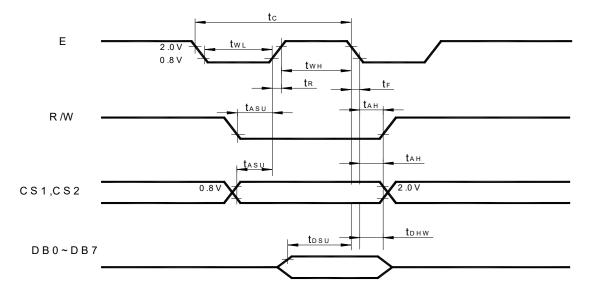
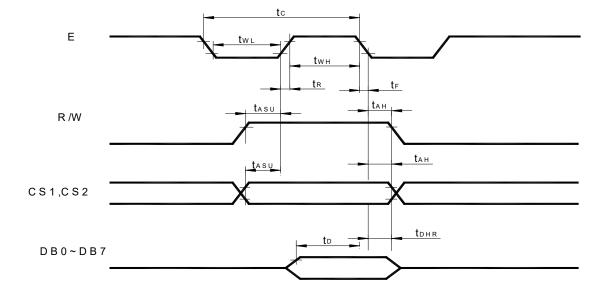


Figure 5.0 MPU write timing



10. COMMAND DEFINITIONS

The display control instructions the internal state of the SBN0064. Instruction is received from MPU to SBN0064 for the display control. The following table shows various instructions.

Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function
Display	L	L	L	L	Н	Н		Н	Н	L/H	Controls the
ON/OFF											display on or off. Internal
											status and display RAM
											data is not affected.
											L: OFF, H: ON
Set Address	L	L	L	Н	Y add	ress (0~	-63)				Sets the Y address
(Y address)											in the Y address counter.
Set Page	L	L	H	L	H	H	Н	Page (0~7)		Sets the X address
(X address)											At the X address counter
Display Start	L	L	H	Н	Displa	ay start i	line (0~0	53)			Indicates the
line											display data RAM
(Z address)											displayed at the top of the
											screen.
Status Read	L	H	В	L	О	R	L	L	L	L	Read status
			U		N	E					BUSY L: Ready
			S		/	S					H: In operation
			Y		0	E					On/Off L: display ON
					F	T					H: Display OFF
					F						RESET L: Normal
											H: Reset
Write Display	Н	L	Write	Date							Writes data (DB0:7) into
Data											display data RAM . After
											writing instruction, Y
											address is increased by 1
											automatically.
Read Display	Н	H	Read I	Date							Reads data (DB0:7)
data											From display data RAM to
											the data bus.

1-1. Display On/Off

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	
0	0	0	0	1	1	1	1	1	D0	

The display data appears when D is 1 and disappears when D is 0.

Though the data is not on the screen with D=0, it remains display data RAM.

Therefore, you can make it appear by changing D=0 into D=1.

10-2. Set Address (Y Address)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0

Y address (AC0~AC5) of the display data RAM is set in the Y address counter.

An address is set by instruction and increased by 1 automatically by read or write operations of display data.

10-3. Set Page (X Address)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	0	1	1	1	AC2	AC1	AC0

X address (AC0~AC2) of the display data RAM is set in the X address register.

Writing or reading to or from MPU is executed in this specified page until the next page is set.

10-4. Display Start Line (Z Address)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	1	AC5	AC4	AC3	AC2	AC1	AC0

Z address (AC0 \sim AC5) of the display data RAM is set in the display start line register and display at the top of the screen. When the display duty cycle is 1/64 or others ($1/32\sim1/64$), the data of total line number of LCD screen, from the line

specified by display start line instruction, is displayed.

10-5. Status Read

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	BUSY	0	ON/OFF	RESET	0	0	0	0

BUSY

When BUSY is 1, the Chip is executing internal operation and no instructions are accepted.

When BUSY is 0, the Chip is ready to accept any instructions.

■ ON/OFF

When ON/OFF is 1, the display is on.

When ON/OFF is 0, the display is off.

■ RESET

When RESET is 1, the system is being initialized.

In this condition is 0, no instructions except status read can be accepted.

When RESET is 0, initialized has finished and the system is in the usual operation condition

10-6. Write Display data

]	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
	1	D7	D6	D5	D4	D3	D2	D1	D0

Writes data (D0~D7) into the display data RAM.

After writing instruction, Y addressed is increased 1 automatically.

10-7. Read Display data

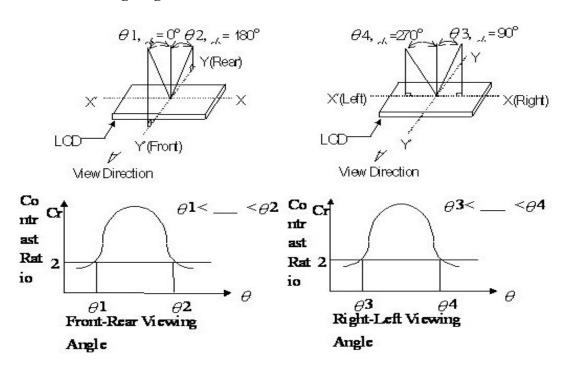
R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	D7	D6	D5	D4	D3	D2	D1	D0

Reads data (D0~D7) from the display data RAM.

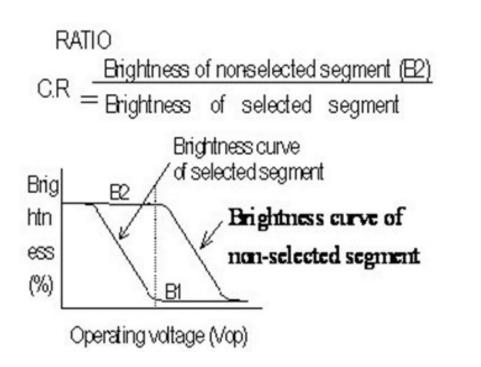
After reading instruction, Y addressed is increased 1 automatically.

11. OPTICAL CHARACTERISTICS

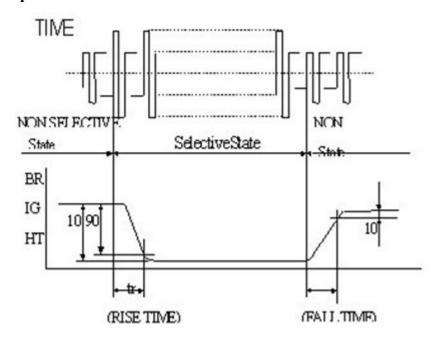
11.1 Definition of Viewing Angle



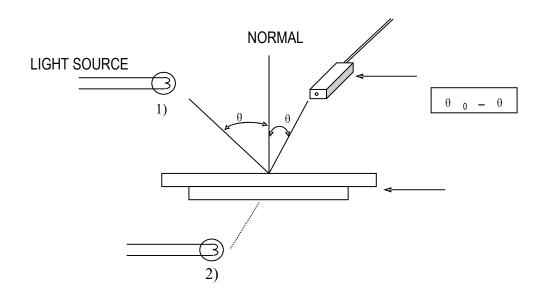
11.2 Definition of Contrast



11.3 Definition of Response



11.4 Measuring Instruments For Elector-optical Characteristics



* Note:

- 1) Light source position for measuring the reflective type of LCD panel;
- 2) Light source position for measuring the transflective / transmissive types of LCD panel.

12. MODULE ACCEPT QUALITY LEVEL (AQL)

12.1 AQL Standard Value: Critical Defect =0.1, Major Defect=0.65; Minor Defect =2.5.

12.2 Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level II

13. RELIABILITY TEST

Operating life time: Longer than 75,000 hours

(at room temperature without direct irradiation of sunlight)

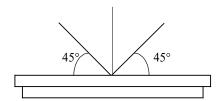
Reliability characteristics shall meet following requirements.

No.	Test Item	Content of Test	Test Condition
1	High Temperature Storage	Endurance test applying the high storage temperature for a long time	+80°C 96H
2	Low Temperature Storage	Endurance test applying the low storage temperature for a long time	−30°C 96H
3	High Temperature Operation	Endurance test applying the electric stress (voltage & current) and the thermal stress to the element for a long time	+70°C 96H
4	Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time	−20°C 96H
5	High Temperature/ Humidity Storage	Endurance test applying the high temperature and humidity storage for a long time	40°C 90%RH 96H
6	Temperature Cycle	Endurance test applying the low and high temperature cycle $-20^{\circ}\text{C} \longleftrightarrow 25^{\circ}\text{C} \longleftrightarrow 70^{\circ}\text{C} \longleftrightarrow 25^{\circ}\text{C}$ $30\text{min} 5\text{min} 30\text{min} 5\text{min}$ 1 cycle	-20°C/70°C 5 cycles
7	Vibration Test (Package State)	Endurance test applying the vibration during transportation	10Hz - 55Hz, 50m/s,15min
8	Shock Test (Package State)	Endurance test applying the shock during transportation	Half-sinewave, 100m/s, 11ms
9	Atmospheric Pressure Test	Endurance test applying the atmospheric pressure during transportation by air	40 kPa 16 H

14. Inspection specification

14.1 Visual Inspection

- 1) Inspect under 2x20W or 40W fluorescent lamp (approximately 3000 lux) leaving 25 to 30 cm between the module and the lamp and 30 cm between the module and the eye (measuring position).
- 2) Appearance is inspected at the best contrast voltage (best contrast is adjusted considering clearness and crosstalk on screen).
- 3) Inspect the module at 45° right and left, top and bottom.
- 4) Use the optimum viewing angle during the contrast inspection.



14.2 Standard of Appearance Inspection

No.	Item	Criteria								
		Round type: as per following drawing								
		$\Phi = (X+Y)$	7)/2	Acceptable quantit	у					
			Size	Zone A	Zone B					
			Ф<0.1	Any number						
		X	0.1<Ф<0.2		Any number					
			0.2< Ф <0.2		_ rany number					
			0.25<Ф	0						
	able quantity									
	Black spot	Length	Width	Zone A	Zone B					
1	White spot		W≤0.02	Any number						
	Dust	L≤3.0 L≤2.5	0.02 <w≤0.03 0.03<w≤0.05< td=""><td>2</td><td>Any number</td></w≤0.05<></w≤0.03 	2	Any number					
	Dust		0.05 <w< td=""><td>As round type</td><td></td></w<>	As round type						
		L								
			otable quantity: 3							
2	Polariser scratch		protective film is perm polariser: same as No.							
3	Polariser bubble	$\Phi = (X+Y)$	7)/2							

		Y		Acceptable quanti	
		├ ──	Size	Zone A	Zone B
		X	Ф<0.2	Any number	
			0.2<Ф	2	
			<0.5		Any number
		Total	0.5< Ф <1.0	1	-
		acce	1.0< Φ	0	
		ptabl		0	
		e quantit	y: 3		
		4.1 Pin hole o	n segmented displa	ay	
		W: segment w			
		$\Phi = (A+B)/2$		Acceptable qua	antity
		B	Wic		Quantity
			NR -	Φ.	
			W≤ı	0.4	≤1/2W
		A	A W>O	$\Phi \leq 0.23$	
				$\Phi \leq 1/3$	
		/ W /		acceptable quantity: 1 egment	defect per
			Din h	oles with Ф unde	er 0.10 mm are
		4.2 Pin hole o	n a	cceptable	
		dot mat display	IIX -		
		r Y 1	≤0.05.,		ble quantity
		\supset_{1}		Size a, b<0.1	Any number
		(]	<u>) </u>	(a+b)/2≤0.1	Any number
		1		0.5<Ф	<u> </u>
				<1.0	3
		Total acceptal	ble quantity: 7		
4	Segment	4.2.5	/ 1. 4		
4	deformation	_	/ dots with differen	nt width	
		<u>D</u>			
			'	Accepta	
				a≥b	a/b≤4/3
		A	В	a <b< th=""><th>a/b>4/3</th></b<>	a/b>4/3
		4.4 Alignmen $\Phi = (A+B)/2$			
		Φ – (A+b)/2	•		
		BY BY	7/-4		
			4	Accepta	ble quantity
				Size	
			\ \\\	Ф ≤0.4	Any number
		Θ P	· · · · · · · · · · · · · · · · · · ·	0.4<Ф	5
			Ø	≤1.0	
		≱_ _		1.0< Ф ≤1.5	3
				1.5< Ф	
		-= -	<u>-</u>	≤2.0	2
		Total acceptal	ble quantity: 7		
		1,			
5	Colour	Level of samp	ole for approval set	as limit sample	

		uniformity						
6	5	Backlight	The backlight colour should correspond to the product specification Flashing and or unlit backlight is not allowed Dust larger than 0.25 mm is not allowed					
7	'	COB	Exposed wire bond pad is not allowed Insufficient covering with resin is not allowed (wire bond line exposed) Dust or bubble on the resin are not allowed					
8	3	PCB	No unmelted solder paste should be present on PCB Cold solder joints, missing solder connections, or oxidation are not allowed No residue or solder balls on PCB are allowed Short circuits on components are not allowed					
				•	Acceptable quan	tity		
					Size	Quantity		
				O 4	Ф<0.2	Any number		
9	'	Tray particles	On tray		Ф>0.25	4		
				On display	Φ ≥0.25	2		
			On display		L = 3	1		

15. LCD MODULES HANDLING PRECAUTIONS

- Please remove the protection foil of polarizer before using.
- The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- If the display panel is damaged and the liquid crystal substance inside it leaks out, do not get any in your mouth. If the substance come into contact with your skin or clothes promptly wash it off using soap and water.
- Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarize carefully.
- To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - -Be sure to ground the body when handling the LCD module.
 - -Tools required for assembly, such as soldering irons, must be properly grounded.
 - -To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - -The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
- Storage precautions

When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags designed to prevent static electricity charging under low temperature / normal humidity conditions (avoid high temperature / high humidity and low temperatures below 0°C). Whenever possible, the LCD modules should be stored in the same conditions in which they were shipped from our company.

16. OTHERS

- Liquid crystals solidify at low temperature (below the storage temperature range) leading to defective orientation of liquid crystal or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to a strong shock at a low temperature.
- If the LCD modules have been operating for a long time showing the same display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. Abnormal operating status can be resumed to be normal condition by suspending use for some time. It should be noted that this phenomena does not adversely affect performance reliability.
- To minimize the performance degradation of the LCD modules resulting from caused by static electricity, etc. exercise care to avoid holding the following sections when handling the modules:
 - Exposed area of the printed circuit board
 - Terminal electrode sections