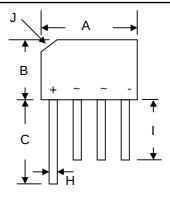


2.0A BRIDGE RECTIFIER

Features

- Diffused Junction
- Low Forward Voltage Drop
- High Current Capability
- High Reliability
- High Surge Current Capability
- Ideal for Printed Circuit Boards



RS					
Dim	Min	Max			
Α	14.22	15.24			
В	10.60	11.68			
С	15.2	_			
D	3.40	4.20			
E	3.60	4.10			
G	1.27	_			
Н	0.70	0.90			
ı	12.7	_			
J	4.2 x 45° Typical				
All Dimensions in mm					

Mechanical Data • Case: Molded Plastic

• Terminals: Plated Leads Solderable

MIL-STD-202, Method 208

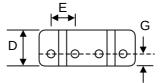
· Polarity: As Marked on Body

• Weight: 1.7 grams (approx.)

· Mounting Position: Any

• Marking: Type Number

Lead Free: For RoHS / Lead Free Version



Type number	Marking code		
RS205	RS205, KBP206		
RS206	RS206, KBP208		
RS207	RS207, KBP210		

Maximum Ratings and Electrical Characteristics @TA=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	RS 205	RS 206	RS 207	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM VRWM VR	600	800	1000	V
RMS Reverse Voltage	VR(RMS)	420	560	700	٧
Average Rectified Output Current (Note 1) $@T_A = 50^{\circ}C$	lo	2.0			Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	 FSM	60			А
Forward Voltage (per element) @I _F = 2.0A	VFM	1.1			٧
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	lкм	10 500		μΑ	
Typical Thermal Resistance (Note 3)	R JA	30			K/W
Operating and Storage Temperature Range	Тj, Tsтg	-55 to +150			°C

Note: 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case.

- 2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.
- 3. Thermal resistance junction to ambient mounted on PC board with 12mmcopper pad.



2.0A BRIDGE RECTIFIER

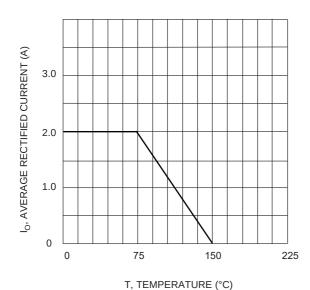


Fig. 1 Forward Current Derating Curve

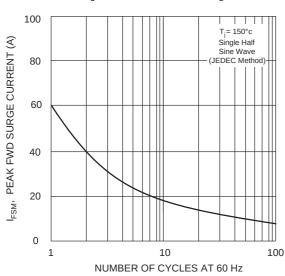
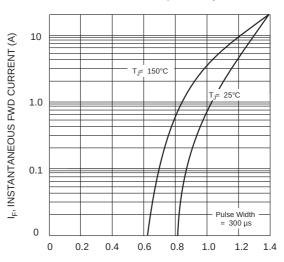


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current



 $\rm V_{\rm p}$ INSTANTANEOUS FWD VOLTAGE (V)

Fig. 2 Typical Fwd Characteristics

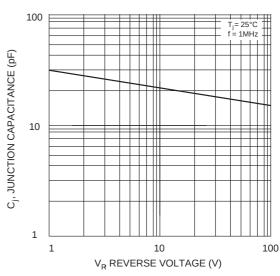
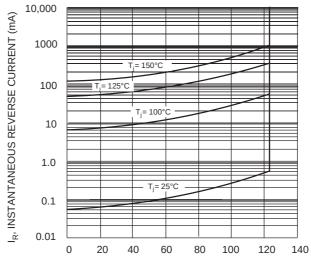


Fig. 4 Typical Junction Capacitance



PERCENT OF RATED PEAK REVERSE VOLTAGE (%)
Fig. 5 Typical Reverse Characteristics