Unit: mm

TOSHIBA Field Effect Transistor Silicon P Channel MOS Type

# 2SJ168

High Speed Switching Applications Analog Switch Applications Interface Applications

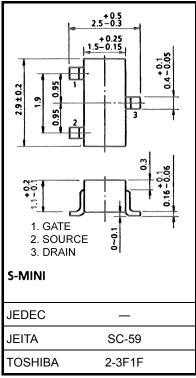
- Excellent switching time: ton = 14 ns (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 100 \text{ mS (min)}$

 $@I_D = -50 \text{ mA}$ 

- Low on resistance: RDS (ON) = 1.3  $\Omega$  (typ.) @ ID = -50 mA
- Enhancement-mode
- Complementary to 2SK1062

### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	-60	V	
Gate-source voltage		$V_{GSS}$	±20	V	
Drain current	DC	ID	-200	mA	
	Pulse	I <sub>DP</sub>	-800		
Drain power dissipation (Ta = 25°C)		P <sub>D</sub>	200	mW	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



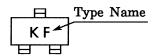
Weight: 0.012 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note: This transistor is the electrostatic sensitive device. Please handle with caution.

### Marking

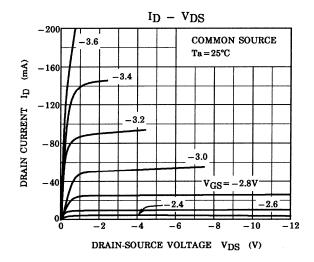


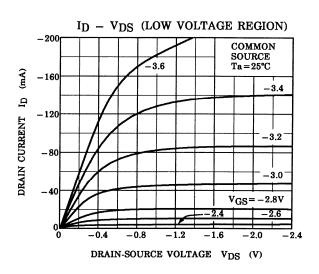


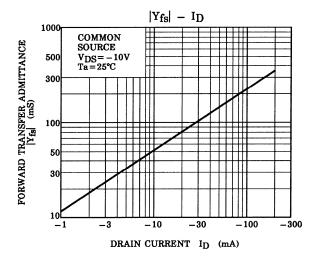
### **Electrical Characteristics (Ta = 25°C)**

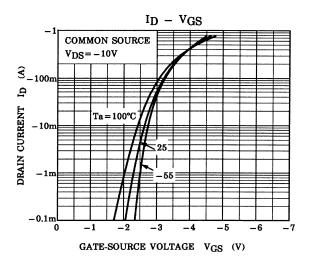
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 10 \text{ V}, V_{DS} = 0$	_	_	±100	nA
Drain cut-off curre	ent	I <sub>DSS</sub>	$V_{DS} = -60 \text{ V}, V_{GS} = 0$	_	_	-10	μА
Drain-source brea	akdown voltage	V (BR) DSS	$I_D = -1$ mA, $V_{GS} = 0$	-60	_	_	V
Gate threshold vo	oltage	V <sub>th</sub>	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-2	_	-3.5	V
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = -10 \text{ V}, I_D = -50 \text{ mA}$	100	_	_	mS
Drain-source ON	resistance	R <sub>DS</sub> (ON)	$I_D = -50 \text{ mA}, V_{GS} = -10 \text{ V}$	_	1.3	2.0	Ω
Drain-source ON	voltage	V <sub>DS</sub> (ON)	$I_D = -50 \text{ mA}, V_{GS} = -10 \text{ V}$	_	-65	-100	mV
Input capacitance		C <sub>iss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	73	85	pF
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	15	22	pF
Output capacitance		Coss	$V_{DS} = -10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	48	60	pF
Switching time	Rise time	t <sub>r</sub>	1 <sub>D</sub> =-100mA -10V VIN VOUT 10μs VOUT VDD≒ -30V	_	8	_	ns
	Turn-on time	t <sub>on</sub>		_	14	_	
	Fall time	t <sub>f</sub>		_	35	_	
	Turn-off Time	t <sub>off</sub>	$V_{IN}$ : $t_r$ , $t_f < 5$ ns D.U. $\leq$ 1% ( $Z_{out} = 50 \Omega$ )	_	100		

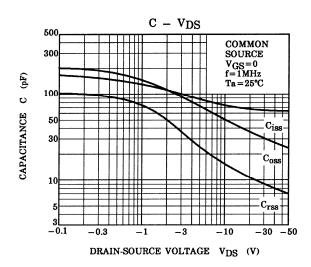
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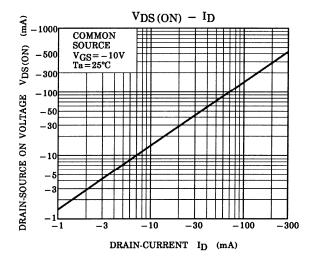


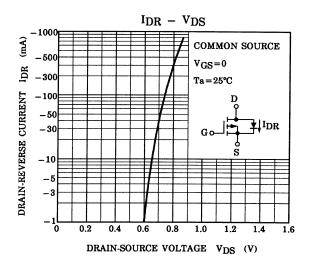


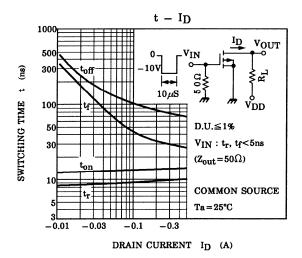


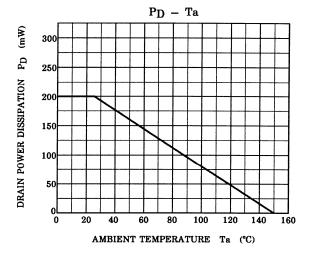


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