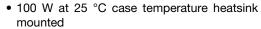


## **Power Resistor Thick Film Technology**



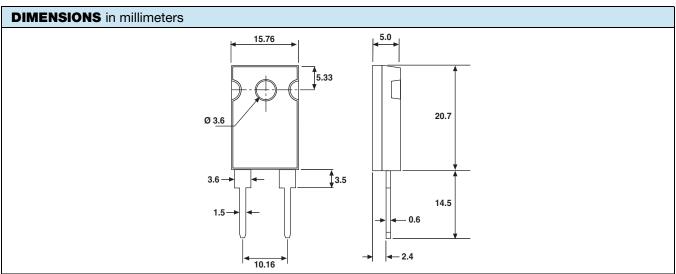
LTO series are the extension of RTO types. We used the direct ceramic mounting design (no metal tab) of our RCH power resistors applied to semiconductor packages.

#### **FEATURES**





- Direct mounting ceramic on heatsink
- Broad resistance range: 0.015  $\Omega$  to 1 M $\Omega$
- Non inductive
- TO-247 package: Compact and easy to mount
- AEC-Q200 qualified
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>



#### Note

• Tolerances unless stated: ± 0.3 mm

| STANDARD ELECTRICAL SPECIFICATIONS |        |                          |                                   |   |                  |  |                       |
|------------------------------------|--------|--------------------------|-----------------------------------|---|------------------|--|-----------------------|
| MODEL                              | SIZE   | RESISTANCE<br>RANGE<br>Ω | RATED POWER  P <sub>25 °C</sub> W | LIMITING ELEMENT<br>VOLTAGE U <sub>L</sub><br>V | TOLERANCE<br>± % | TEMPERATURE<br>COEFFICIENT<br>± ppm/°C | CRITICAL RESISTANCE Ω |
| LTO 100                            | TO-247 | 0.015 to 1 M             | 100                               | 500   | 1, 2, 5, 10      | 150, 350, 900                          | 2.5 K                 |

| MECHANICAL SPECIFICATIONS |               |  |  |
|---------------------------|---------------|--|--|
| Mechanical Protection     | Molded        |  |  |
| Resistive Element         | Thick film    |  |  |
| Substrate                 | Alumina       |  |  |
| Connections               | Tinned copper |  |  |
| Weight                    | 3.5 g max.    |  |  |
| Mounting Torqure          | 1 Nm          |  |  |

| ENVIRONMENTAL SPECIFICATIONS |  |  |  |
|------------------------------|--|--|--|
| Temperature Range            | -55 °C to +175 °C  |  |  |
| Climatic Category            | 55/175/56  |  |  |
| Flammability                 | IEC 60695-11-5<br>2 applications 30 s<br>separated by 60 s |  |  |

| TECHNICAL SPECIFICATIONS                                   |  |  |  |  |  |
|--|--|--|--|--|--|
| Dissipation and Associated                                 | Onto a heatsink  |  |  |  |  |
| Power Rating and<br>Thermal Resistance<br>of the Component | 100 W at +25 °C (case temp.)  R <sub>TH (j - c)</sub> : 1.5 °C/W  Free air:  3.5 W at +25 °C |  |  |  |  |
| Temperature Coefficient                                    | See Performance table  |  |  |  |  |
| Standard   | ± 150 ppm/°C   |  |  |  |  |
| Dielectric Strength<br>MIL STD 202                         | 2500 V <sub>RMS</sub> - 1 min<br>10 mA max.  |  |  |  |  |
| Insulation Resistance                                      | $\geq 10^4 \ M\Omega$  |  |  |  |  |
| Inductance   | ≤ 0.1 µH   |  |  |  |  |



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| PERFORMANCE               |  |                                |  |  |  |
|---------------------------|--|--------------------------------|--|--|--|
| TESTS                     | CONDITIONS   | REQUIREMENTS                   |  |  |  |
| Momentary Overload        | EN 60115-1<br>1.5 Pr/5 s<br>U <sub>S</sub> < 1.5 U <sub>L</sub>  | ± (0.5 % + 0.005 Ω)            |  |  |  |
| Load Life                 | EN 60115-1<br>1000 h Pr at +25 °C  | $\pm$ (1 % + 0.005 $\Omega$ )  |  |  |  |
| High Temperature Exposure | AEC-Q200 REV D conditions:<br>MIL-STD-202 method 108<br>1000 h, + 175 °C, unpowered                        | $\pm (0.25 \% + 0.005 \Omega)$ |  |  |  |
| Temperature Cycling       | AEC-Q200 REV D conditions:<br>JESD22 method JA-104<br>1000 cycles, -55 °C to +125 °C<br>dwell time -15 min | ± (1 % + 0.005 Ω)              |  |  |  |
| Biased Humidity           | AEC-Q200 REV D conditions:  MIL-STD-202 method 103 ± (1 % +0.005 Ω)  1000 h, 85 °C, 85 % RH                |                                |  |  |  |
| Operational Life          | AEC-Q200 REV D conditions:<br>MIL-STD-202 method 108<br>1000 h, 90/30, powered, +125 °C                    | ± (1 % +0.005 Ω)               |  |  |  |
| ESD Human Body Model      | AEC-Q200 REV D conditions:<br>AEC-Q200-002<br>25 kV <sub>AD</sub>  | ± (0.5 % +0.005 Ω)             |  |  |  |
| Vibration                 | AEC-Q200 REV D conditions: MIL-STD-202 method 204 5 g's for 20 min, 12 cycles test from 10 Hz to 2000 Hz   | ± (0.5 % +0.005 Ω)             |  |  |  |
| Mechanical Shock          | AEC-Q200 REV D conditions: MIL-STD-202 method 213 100 g's, 6 ms, 3.75 m/s 3 shocks/direction               | ± (0.5 % +0.005 Ω)             |  |  |  |
| Terminal Strength         | AEC-Q200 REV D conditions:<br>AEC-Q200-006<br>2 kgf, 60 sec  | ± (0.25 % +0.01 Ω)             |  |  |  |

| SPECIAL FEATURES                                     |                 |              |              |  |  |
|--|-----------------|--------------|--------------|--|--|
| Resistance Values                                    | ≥ 0.015         | ≥ 0.1        | > 20         |  |  |
| Tolerances   | ± 1 % at ± 10 % |              |              |  |  |
| Typical Temperature Coefficient (- 55 ° to + 175 °C) | ± 900 ppm/°C    | ± 350 ppm/°C | ± 150 ppm/°C |  |  |

#### CHOICE OF THE HEATSINK

The user must choose according to the working conditions of the component (power, room temperature). Maximum working temperature must not exceed 175 °C. The dissipated power is simply calculated by the following ratio:

$$P \; = \; \frac{\Delta T}{[R_{TH \; (j \; - \; c)}] + [R_{TH \; (c \; - \; h)}] + [R_{TH \; (h \; - \; a)}]}^{\;\; (1)}$$

P: Expressed in W

ΔΤ: Difference between maximum working temperature and room temperature

Thermal resistance value measured between resistive layer and outer side of the resistor. It is the thermal resistance of the component.

R<sub>TH (c - h)</sub>: Thermal resistance value measured between outer side of the resistor and upper side of the heatsink. This is the thermal resistance of the interface (grease, thermal pad), and the quality of the fastening device.

R<sub>TH (h - a)</sub>: Thermal resistance of the heatsink.

 $R_{TH~(c~-h)} + R_{TH~(h~-a)}$  for LTO 100 power rating 10 W at ambient temperature +25 °C Thermal resistance R<sub>TH (j - c)</sub>: 1.5 °C/W Considering equation <sup>(1)</sup> we have:

$$\Delta T = 175 \, ^{\circ}C - 25 \, ^{\circ}C = 150 \, ^{\circ}C$$
 
$$R_{TH \, (j - c)} + R_{TH \, (c - h)} + R_{TH \, (h - a)} = \frac{\Delta T}{P} = \frac{150}{10} = 15 \, ^{\circ}C/W$$
 
$$R_{TH \, (c - h)} + R_{TH \, (h - a)} = 15 \, ^{\circ}C/W - 1.5 \, ^{\circ}C/W = 13.5 \, ^{\circ}C/W$$

with a thermal grease  $R_{TH (c-h)} = 1$  °C/W, we need a heatsink with  $R_{TH (h-a)} = 12.5$  °C/W.

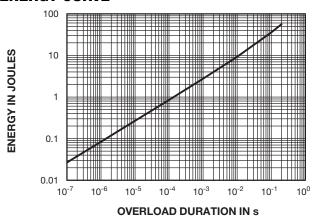
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#### **OVERLOADS**

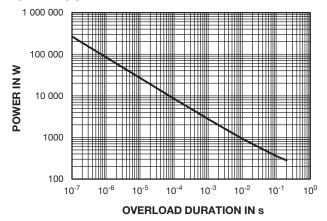
In any case the applied voltage must be lower than the maximum overload voltage of 750 V.

The values indicated on the graph below are applicable to resistors in air or mounted onto a heatsink.

#### **ENERGY CURVE**



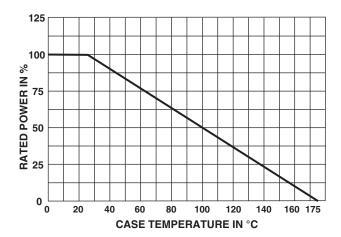
#### **POWER CURVE**



### **POWER RATING**

The temperature of the case should be maintained within the limits specified.

To improve the thermal conductivity, surfaces in contact should be coated with a silicone grease and the torque applied on the screw for tightening should be around 1 Nm.



#### **PACKAGING**

Tube of 30 units

#### **MARKING**

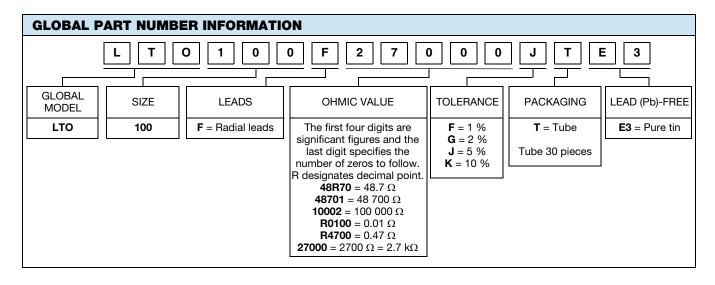
Model, style, resistance value (in  $\Omega$ ), tolerance (in %), manufacturing date, Vishay Sfernice trademark.



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| ORDERING INFORMATION |       |             |                  |                                   |   |           |                |  |
|----------------------|-------|-------------|------------------|-----------------------------------|---|-----------|----------------|--|
| LTO                  | 100   | F           | 2.7 kΩ           | ± 1 %                             | xxx   | TU30      | e3             |  |
| MODEL                | STYLE | CONNECTIONS | RESISTANCE VALUE | TOLERANCE                         | CUSTOM DESIGN   | PACKAGING | LEAD (Pb)-FREE |  |
|                      |       |             |                  | ± 1 %<br>± 2 %<br>± 5 %<br>± 10 % | Optional<br>on request:<br>Special TCR,<br>shape etc. |           |                |  |





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