



1 CHANNEL LOW CAPACITANCE TVS

Features

- IEC 61000-4-2 (ESD): Air ±15kV, Contact ±8kV
- 1 Channel of ESD Protection
- Low Channel Input Capacitance
- Low Profile Package (0.65mm max) with a Small PCB Footprint (only 1.7*0.9mm) Suitable for Portable Electronics Applications
- Typically Used at High Speed Ports such as USB 2.0, IEEE1394, Serial ATA, DVI, HDMI, PCI
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

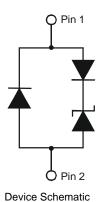
Mechanical Data

- Case: SOD523
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.001 grams (Approximate)









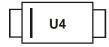
Ordering Information (Note 4)

Part Number	Case	Packaging
D1213A-01T-7	SOD523	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com.
- 5. Dispensed every other cavity of the carrier tape.

Marking Information



U4 = Product Type Marking Code Line Denotes Pin 1



Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current	Ipp	5	Α	8/20µs, Per Fig. 2
ESD Protection – Contact Discharge	V _{ESD_Contact}	±8	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	V _{ESD_Air}	±15	kV	Standard IEC 61000-4-2

Thermal Characteristics

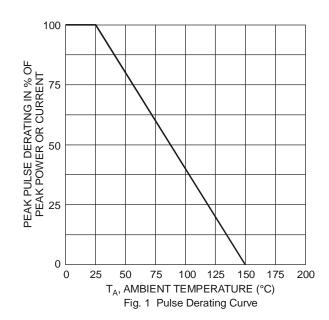
Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 6)	P _D	250	mW
Thermal Resistance, Junction to Ambient (Note 6)	$R_{ heta JA}$	500	°C/W
Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	T _{STG}	-65 to +150	°C

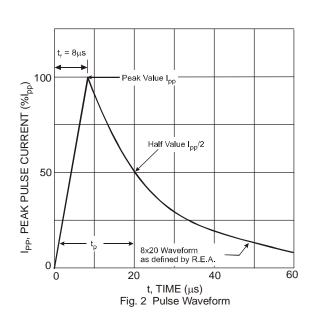
Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse working voltage	VRWM	1	-	3.3	V	-
Reverse current (Note 7)	I _R	-	0.1	1.0	μΑ	$V_R = V_{RWM} = 3.3V$
Reverse breakdown voltage	V_{BR}	6.0	7.5	9.0	V	$I_R = 1 \text{mA}$
Forward voltage	V_{F}	0.6	0.8	0.95	V	$I_F = 8mA$
Reverse clamping voltage, Positive Transients	V _{CL1}	-	10.0	-	V	$I_{PP} = 1A, t_p = 8/20 \mu s$
Reverse clamping voltage, Negative Transients	V _{CL2}	-	-1.7	-	V	$I_{PP} = -1A$, $t_p = 8/20 \mu s$
Dynamic resistance	R _{DYN}	-	0.9	-	Ω	$I_R = 1A$, $t_p = 8/20 \mu s$
Capacitance	C _T	1	0.85	1.2	pF	V _R = 1.65V, f = 1MHz

Notes:

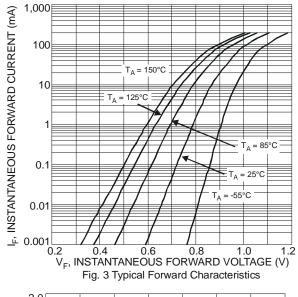
^{7.} Short duration pulse test used to minimize self-heating effect.





^{6.} Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.





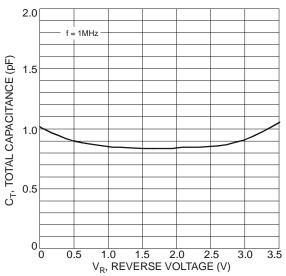
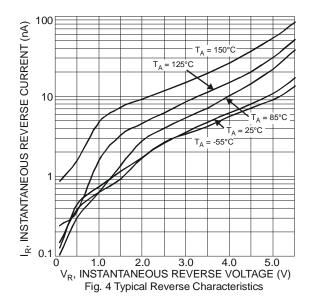
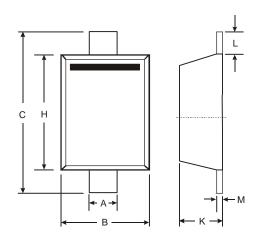


Fig. 5 Typical Total Capacitance vs. Reverse Voltage



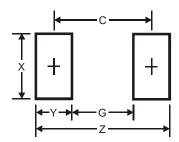
Package Outline Dimensions



SOD523					
Dim	Min	Max			
Α	0.25	0.35			
В	0.70	0.90			
С	1.50	1.70			
Н	1.10	1.30			
K	0.55	0.65			
L	0.10	0.30			
М	0.10	0.12			
All Dimensions in mm					



Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.3
G	1.1
Х	0.8
Υ	0.6
С	1.7

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