ROHS COMPLIANT

HALOGEN

FREE

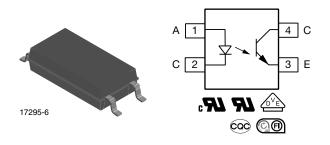
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(5-2008)



Vishay Semiconductors

Optocoupler, Phototransistor Output, Low Input Current, 4 Pin LSOP, Long Creepage Mini-Flat Package



LINKS TO ADDITIONAL RESOURCES



DESCRIPTION

The VOL618A has a GaAs infrared emitting diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a 4 pin LSOP wide body package.

It features a high current transfer ratio, low coupling capacitance, and high isolation voltage.

The coupling device is designed for signal transmission between two electrically separated circuits.

FEATURES

- Low profile package
- High collector emitter voltage, V_{CEO} = 80 V
- Isolation test voltage, 5000 V_{RMS}
- Isolation voltage V_{IORM} = 1050 V_{peak}
- Low coupling capacitance
- · High common mode transient immunity
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

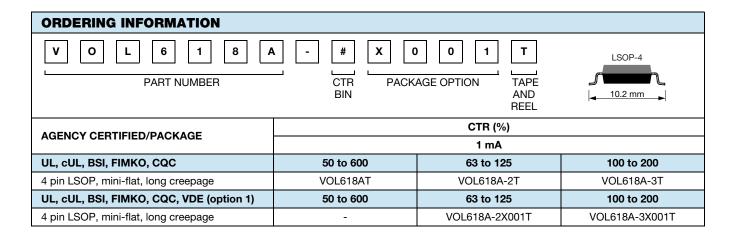
APPLICATIONS

- Telecom
- Industrial controls
- Battery powered equipment
- Office machines
- Programmable controllers

AGENCY APPROVALS

(All parts are certified under base model VOL618A)

- <u>UL</u>
- <u>cUL</u>
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- <u>BSI</u>
- <u>FIMKO</u>
- <u>CQC</u>



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Document Number: 82405

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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT	·	÷	•	•		
Reverse voltage		V _R	6	V		
Power dissipation		P _{diss}	100	mW		
Forward current		١ _F	60	mA		
Forward surge current	t _p < 10 μs	I _{FSM}	1.5	A		
Junction temperature		Тj	125	°C		
OUTPUT						
Collector emitter voltage		V _{CEO}	80	V		
Emitter collector voltage		V _{ECO}	7	V		
Collector current		Ι _C	50	mA		
	$t_p/T = 0.5, t_p < 10 ms$	Ι _C	100	mA		
Power dissipation		P _{diss}	150	mW		
Junction temperature		Tj	125	°C		
COUPLER						
Total power dissipation		P _{tot}	250	mW		
Storage temperature range		T _{stg}	-55 to +125	°C		
Ambient temperature range		T _{amb}	-55 to +110	°C		
Soldering temperature ⁽¹⁾	≤ 10 s	T _{sld}	260	°C		

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

⁽¹⁾ Refer to reflow profile for soldering conditions for surface mounted devices.

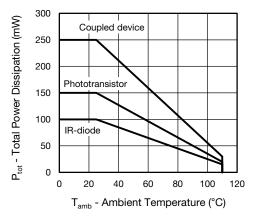


Fig. 1 - Total Power Dissipation vs. Ambient Temperature

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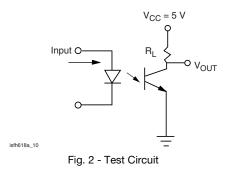
ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	I _F = 5 mA		V _F	-	1.16	1.5	V
Capacitance	$V_R = 0 V$, f = 1 MHz		Co	-	45	-	pF
Reverse current	V _R = 6 V		I _R	-	-	100	μA
OUTPUT							
Collector emitter leakage current	$V_{CE} = 10 \text{ V}, I_F = 0 \text{ A}$		I _{CEO}	-	10	200	nA
Collector emitter capacitance	$V_{CE} = 5 V, f = 1 MHz$		C _{CE}	-	7	-	pF
COUPLER							
Collector emitter saturation	I _C = 0.32 mA, I _F = 1 mA	VOL618A-2	V _{CEsat}	-	0.25	0.4	V
voltage	I _C = 0.5 mA, I _F = 1 mA	VOL618A-3	V _{CEsat}	-	0.25	0.4	V
Coupling capacitance	f = 1 MHz		C _C	-	0.25		pF

Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I _C /I _F	I _F = 1 mA, V _{CE} = 5 V	VOL618A	CTR	50	-	600	%
		VOL618A-2	CTR	63	-	125	%
		VOL618A-3	CTR	100	-	200	%

SWITCHING CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn on time	V_{CC} = 5 V, I_C = 2 mA, R_L = 100 Ω	t _{on}	-	6	-	μs
Rise time	V_{CC} = 5 V, I_C = 2 mA, R_L = 100 Ω	t _r	-	3.5	-	μs
Turn off time	V_{CC} = 5 V, I_C = 2 mA, R_L = 100 Ω	t _{off}	-	5.5	-	μs
Fall time	V_{CC} = 5 V, I_{C} = 2 mA, R_{L} = 100 Ω	t _f	-	5	-	μs



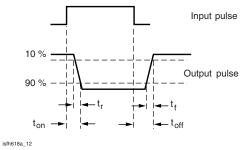


Fig. 3 - Test Circuit and Waveforms

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VOL618A



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SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Climatic classification	According to IEC 68 part 1		55 / 110 / 21				
Pollution degree	According to DIN VDE 0109		2				
Comparative tracking index	Insulation group IIIa	CTI	175				
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V _{ISO}	5000	V _{RMS}			
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V _{IOTM}	8000	V _{peak}			
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	VIORM	1050	V _{peak}			
	$T_{amb} = 25 \ ^{\circ}C, \ V_{IO} = 500 \ V$	R _{IO}	≥ 10 ¹²	Ω			
Isolation resistance	$T_{amb} = 100 \ ^{\circ}C, V_{IO} = 500 \ V$	R _{IO}	≥ 10 ¹¹	Ω			
	$T_{amb} = TS, V_{IO} = 500 V$	R _{IO}	≥ 10 ⁹	Ω			
Output safety power		P _{SO}	265	mW			
Input safety current		I _{SI}	130	mA			
Input safety temperature		Τ _S	150	°C			
Creepage distance			≥ 8	mm			
Clearance distance			≥ 8	mm			
Insulation thickness		DTI	≥ 0.4	mm			
Input to output test voltage, method B	$V_{IORM} x 1.875 = V_{PR}$, 100 % production test with $t_M = 1$ s, partial discharge < 5 pC	V _{PR}	2000	V _{peak}			
Input to output test voltage, method A	$V_{IORM} x 1.6 = V_{PR}$, 100 % sample test with $t_M = 10$ s, partial discharge < 5 pC	V _{PR}	1680	V _{peak}			

Note

 According to DIN EN 60747-5-5 (VDE 0884), § 7.4.3.8.2, (see Fig. 4). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

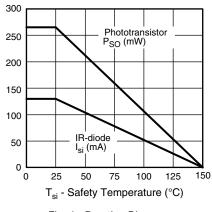


Fig. 4 - Derating Diagram

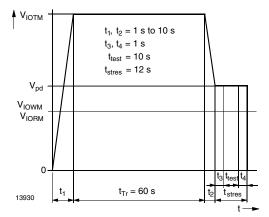


Fig. 5 - Test Pulse Diagram for Sample Test according to DIN EN 60747-5-5

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TYPICAL CHARACTERISTICS ($T_{amb} = 25$ °C, unless otherwise specified)

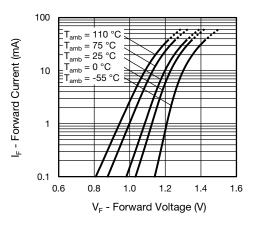


Fig. 6 - Forward Current vs. Forward Voltage

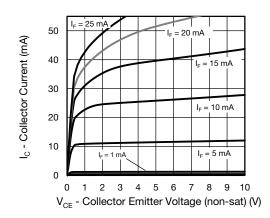


Fig. 7 - Collector Current vs. Collector Emitter Voltage (non-saturated)

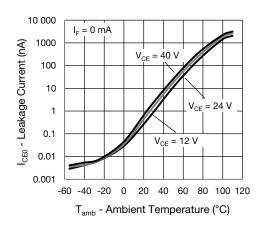


Fig. 8 - Collector Emitter Current vs. Ambient Temperature

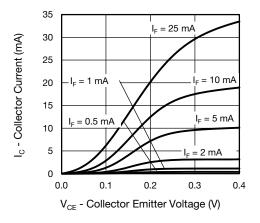


Fig. 9 - Collector Current vs. Collector Emitter Voltage (saturated)

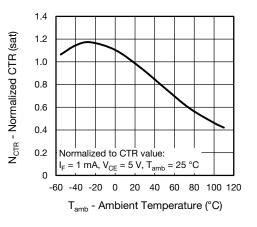
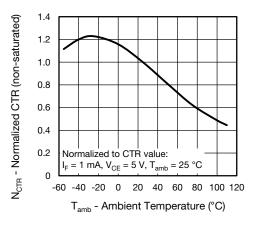
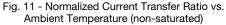


Fig. 10 - Normalized Current Transfer Ratio vs. Ambient Temperature (saturated)

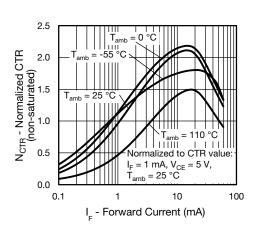




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Fig. 12 - Normalized Current Transfer Ratio (non-saturated) vs. Forward Current

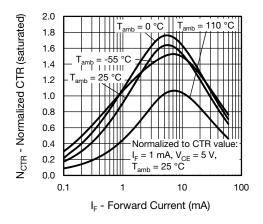


Fig. 13 - Normalized Current Transfer Ratio (saturated) vs. Forward Current

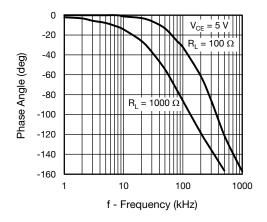


Fig. 14 - Phase Angle vs. Frequency

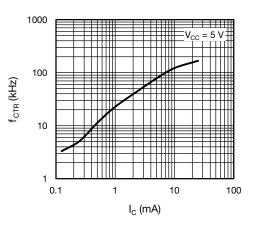


Fig. 15 - f_{CTR} vs. Collector Current

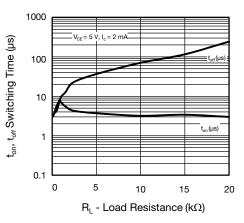


Fig. 16 - Switching Time vs. Load Resistance

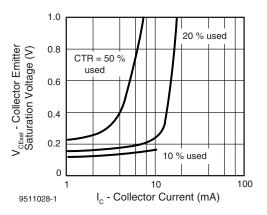


Fig. 17 - Collector Emitter Saturation Voltage vs. Collector Current

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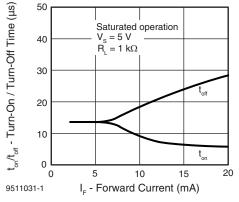


Fig. 18 - Turn-On/Turn-Off Time vs. Forward Current

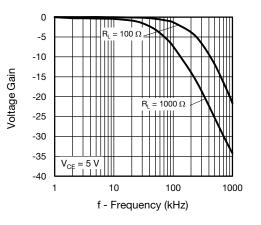
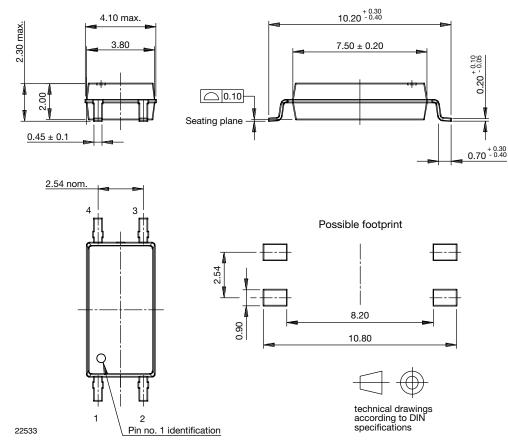


Fig. 19 - Voltage Gain vs. Cut-off Frequency



PACKAGE DIMENSIONS (in millimeters)

Rev. 1.9, 13-Feb-2023



PACKAGE MARKING (example of VOL618A-3X001T)



Notes

- Only option 1 is reflected in the package marking with the characters "X1"
- Tape and reel suffix (T) is not part of the package marking
- XXXX = LMC (lot marking code)

TAPE AND REEL DIMENSIONS (in millimeters)

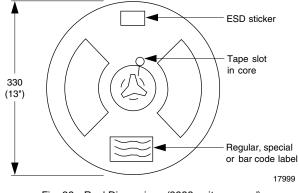


Fig. 20 - Reel Dimensions (3000 units per reel)

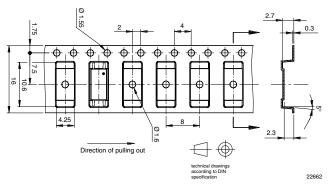


Fig. 21 - Tape Dimensions

SOLDER PROFILE

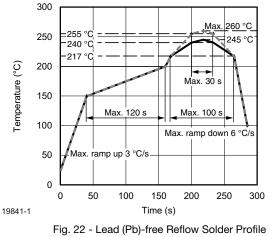


Fig. 22 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited Conditions: $T_{amb} < 30$ °C, RH < 85 % Moisture sensitivity level 1, according to J-STD-020



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