



DMP2130L

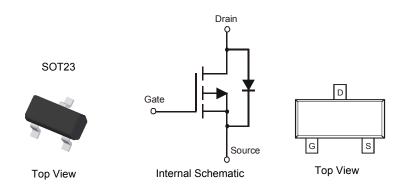
P-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low R_{DS(ON)}:
 - 75 m Ω @V_{GS} = -4.5V
 - 110 m Ω @V_{GS} = -2.7V
 - 125 mΩ @V_{GS} = -2.5V
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT23
- Case Material Molded Plastic, "Green" Molding Compound.
 UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram Below
- Weight: 0.008 grams (approximate)



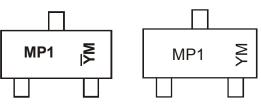
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2130L-7	SOT23	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 - See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



Chengdu A/T Site Shanghai A/T Site

MP1 = Product Type Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test site) $\overline{\gamma}_M$ = Date Code Marking for CAT (Chengdu Assembly/ Test site)

Y or ¬ = Year (ex: A = 2013)

M = Month (ex: 9 = September)

Date Code Key

Year	2007	2008	2009	2010	201	1 20	12	2013	2014	2015	2016	2017
Code	כ	V	W	X	Υ	- 2	Z	Α	В	С	D	Е
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	-20	V
Gate-Source Voltage		V _{GSS}	±12	V
Drain Current (Note 5) Continuous	T _A = +25°C T _A = +70°C	I _D	-3.0 -2.4	А
Pulsed Drain Current (Note 6)		I _{DM}	-15	Α
Body-Diode Continuous Current (Note 5)		I _S	2.0	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_{D}	1.4	W
Thermal Resistance, Junction to Ambient (Note 5); Steady-State	$R_{ hetaJA}$	90	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

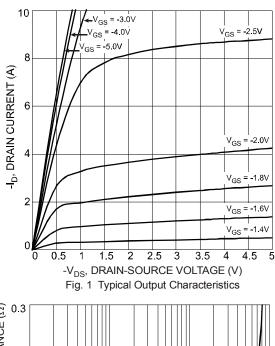
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
STATIC PARAMETERS						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$I_D = -250 \mu A$, $V_{GS} = 0 V$
Zero Gate Voltage Drain Current T _J = 25°C	I _{DSS}	_	_	-1	μA	V _{DS} = -20V, V _{GS} = 0V
Gate-Body Leakage Current	I _{GSS}	_	_	±100	nA	$V_{DS} = 0V, V_{GS} = \pm 12V$
Gate Threshold Voltage	V _{GS(th)}	-0.6	_	-1.25	V	$V_{DS} = V_{GS}, I_{D} = -250\mu A$
On State Drain Current (Note 7)	I _{D (ON)}	-15		_	Α	V _{GS} = -4.5V, V _{DS} = -5V
Static Drain-Source On-Resistance (Note 7)		_	51 87 99	75 110 125	mΩ	V_{GS} = -4.5V, I_D = -3.5A V_{GS} = -2.7V, I_D = -3.0A V_{GS} = -2.5V, I_D = -2.6A
Forward Transconductance (Note 7)	9 FS	_	7.3	_	S	$V_{DS} = -10V$, $I_D = -3.0A$
Diode Forward Voltage (Note 7)	V_{SD}	_	0.79	-1.26	V	I _S = -1.7A, V _{GS} = 0V
Maximum Body-Diode Continuous Current (Note 5)		_	_	1.7	Α	_
DYNAMIC PARAMETERS (Note 8)						
Total Gate Charge		_	7.3	_	nC	$V_{GS} = -4.5V$, $V_{DS} = -10V$, $I_D = -3.0A$
Gate-Source Charge		_	2.0	—	nC	$V_{GS} = -4.5V$, $V_{DS} = -10V$, $I_D = -3.0A$
Gate-Drain Charge	Q_{gd}	_	1.9	_	nC	$V_{GS} = -4.5V$, $V_{DS} = -10V$, $I_D = -3.0A$
Turn-On Delay Time		_	12	_	ns	
Turn-On Rise Time		_	20	_	ns	$V_{DS} = -10V, V_{GS} = -4.5V,$
Turn-Off Delay Time		_	38		ns	$R_L = 10\Omega$, $R_G = 6\Omega$
Turn-Off Fall Time	t _f	_	41	_	ns	
Input Capacitance		_	443	_	pF	101/1/
Output Capacitance		_	128	_	pF	V _{DS} = -16V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	101	_	pF	1.0

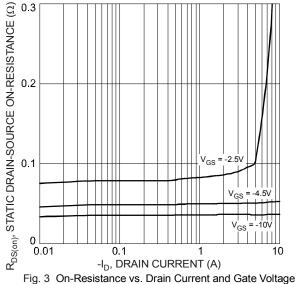
Notes:

- 5. Device mounted on 1"x1", FR-4 PC board with 2 oz. Copper and test pulse width t \leq 10s. 6. Repetitive Rating, pulse width limited by junction temperature. 7. Test pulse width t = 300 μ s.

- 8. Guaranteed by design. Not subject to production testing.







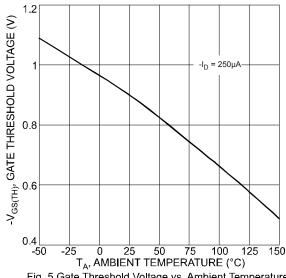
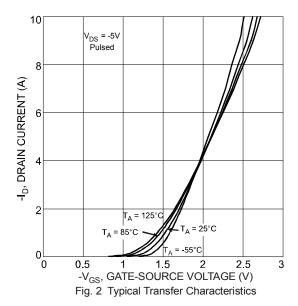
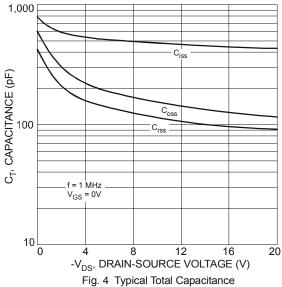


Fig. 5 Gate Threshold Voltage vs. Ambient Temperature





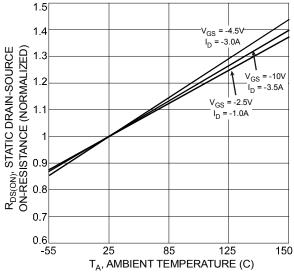
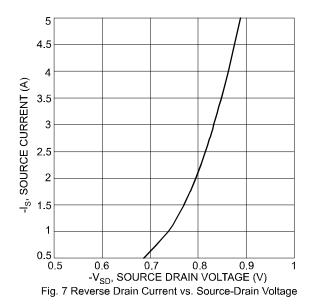
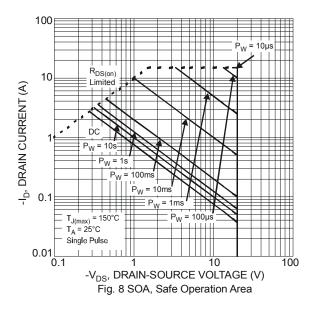


Fig. 6 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature

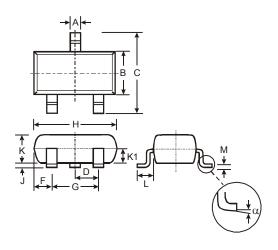






Package Outline Dimensions

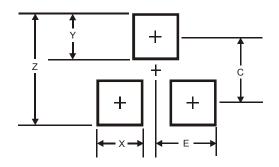
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
7	0.013	0.10	0.05			
K	0.903	1.10	1.00			
K1	-	-	0.400			
L	0.45	0.61	0.55			
М	0.085	0.18	0.11			
α	0°	8°	-			
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35



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