

# PJA3460-AU

#### 60V N-Channel Enhancement Mode MOSFET

Current

2.5 A

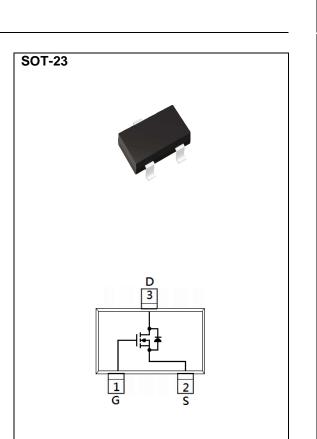
#### Features

Voltage

- R<sub>DS(ON)</sub>, V<sub>GS</sub>@10V, I<sub>D</sub>@2A<75mΩ
- R<sub>DS(ON)</sub>, V<sub>GS</sub>@4.5V, I<sub>D</sub>@1A<90mΩ
- Advanced Trench Process Technology

60 V

- Specially Designed for Switch Load, PWM Application, etc
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard



#### **Mechanical Data**

- Case : SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.009 grams

#### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V <sub>DS</sub>	60	V	
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 20		
Continuous Drain Current (Note 4)		ID	2.5	A	
Pulsed Drain Current (Note 1)		Ідм	10		
Power Dissipation	T <sub>a</sub> =25°C	-	1.25	W	
	Derate above 25°C	PD	10	mW/°C	
Operating Junction and Storage Temperature Range		TJ,Tsтg	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient (Note 3,4)		R <sub>θJA</sub>	100	°C/W	



#### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

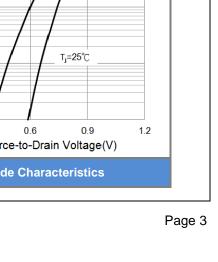
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub> V <sub>GS</sub> =0V, I <sub>D</sub> =250uA           V <sub>GS(th)</sub> V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	60	-	-	V	
Gate Threshold Voltage		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1	1.75	2.5	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =2A	-	55	75	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =1A	-	63	90	
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V	-	-	1	uA
Gate-Source Leakage Current	lgss	V <sub>GS</sub> = <u>+</u> 20V, V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 5)		-			_	_
Total Gate Charge	Qg	V <sub>DS</sub> =48V, I <sub>D</sub> =2A, V <sub>GS</sub> =10V <sup>(Note 1,2)</sup>	-	9.3	-	nC
Gate-Source Charge	Qgs		-	2.2	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	1.9	-	
Input Capacitance	Ciss	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHZ	-	509	-	pF
Output Capacitance	Coss		-	47	-	
Reverse Transfer Capacitance	Crss		-	23	-	
Turn-On Delay Time	td <sub>(on)</sub>	V <sub>DD</sub> =30V, I <sub>D</sub> =2A, V <sub>GS</sub> =10V, R <sub>G</sub> =3.3Ω <sup>(Note 1,2)</sup>	-	3.2	-	
Turn-On Rise Time	tr		-	9.7	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	18.5	-	
Turn-Off Fall Time	tf		-	6.4	-	
Drain-Source Diode						
Maximum Continuous Drain-Source				_	2.5	А
Diode Forward Current	ls				2.5	~
Diode Forward Voltage	V <sub>SD</sub>	Is=1A, V <sub>GS</sub> =0V	-	0.77	1.2	V

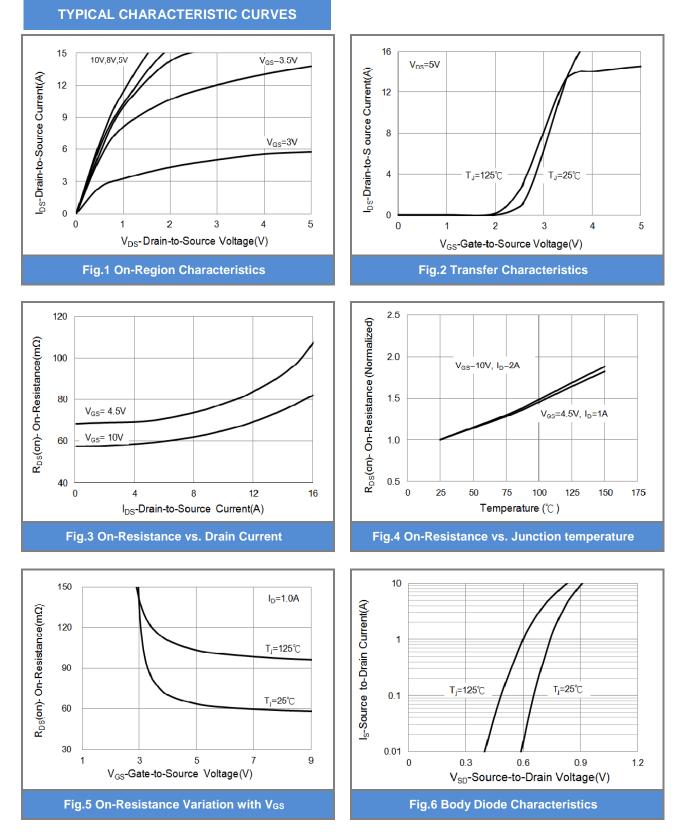
NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. ReJA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.

October 23,2019

#### PJA3460-AU-REV.00



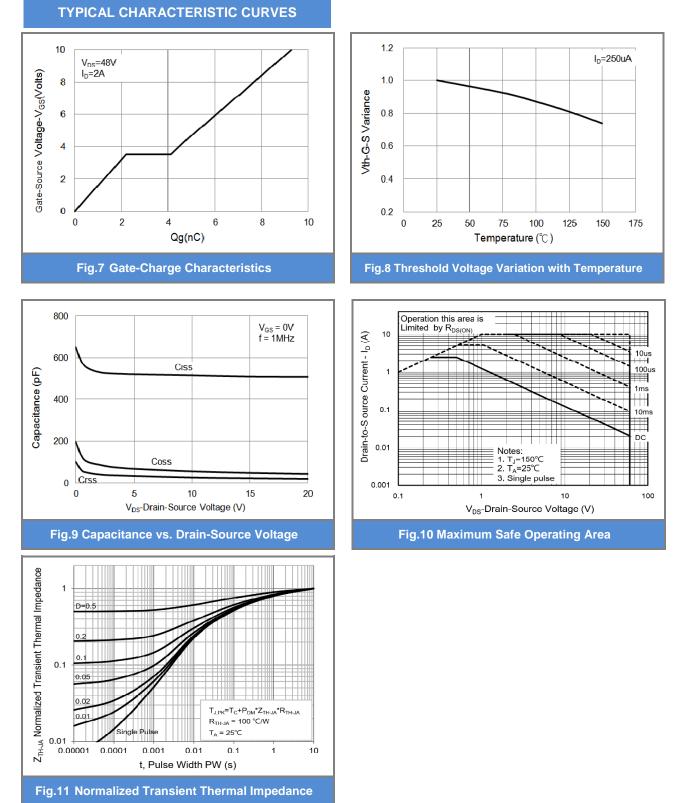




**PJA3460-AU** 



Page 4



### PANJ SEMI CONDUCTOR

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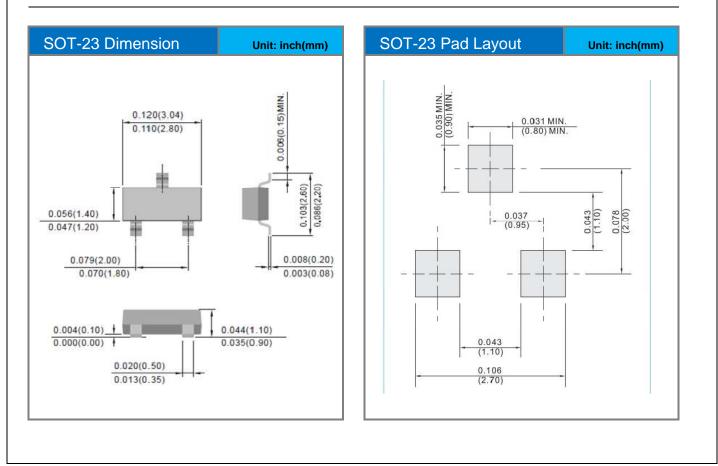
## **PJA3460-AU**

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#### Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJA3460-AU_R1_000A1	SOT-23	3K pcs / 7" reel	A60	Halogen free

#### **Packaging Information & Mounting Pad Layout**





# **PJA3460-AU**

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