## BAS16VV; BAS16VY

# Triple high-speed switching diodes Rev. 03 — 20 April 2007

**Product data sheet** 

## **Product profile**

#### 1.1 General description

Triple high-speed switching diodes, encapsulated in very small Surface-Mounted Device (SMD) plastic packages.

Table 1. **Product overview** 

Type number	Package		Configuration
	NXP	JEITA	
BAS16VV	SOT666	-	triple isolated
BAS16VY	SOT363	SC-88	

#### 1.2 Features

- High switching speed:  $t_{rr} \le 4$  ns
- Low leakage current

- Reverse voltage: V<sub>R</sub> ≤ 100 V
- Very small SMD plastic packages

#### 1.3 Applications

- High-speed switching
- General-purpose switching
- Voltage clamping
- Reverse polarity protection

#### 1.4 Quick reference data

Table 2. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per diode						
I <sub>F</sub>	forward current		-	-	200	mA
$V_R$	reverse voltage		-	-	100	V
t <sub>rr</sub>	reverse recovery time		<u>[1]</u> _	-	4	ns

[1] When switched from  $I_F$  = 10 mA to  $I_R$  = 10 mA;  $R_L$  = 100  $\Omega$ ; measured at  $I_R$  = 1 mA.



## 2. Pinning information

Table 3. Pinning

Pin	Description	Simplified outline	Symbol
1	anode (diode 1)		
2	anode (diode 2)	[6] [5] [4]	6 5 4
3	anode (diode 3)		
4	cathode (diode 3)		
5	cathode (diode 2)		1 2 3 sym043
6	cathode (diode 1)	001aab555	, , ,

## 3. Ordering information

Table 4. Ordering information

Type number	Package					
	Name	Description	Version			
BAS16VV	-	plastic surface-mounted package; 6 leads	SOT666			
BAS16VY	SC-88	plastic surface-mounted package; 6 leads	SOT363			

## 4. Marking

Table 5. Marking codes

Type number	Marking code <sup>[1]</sup>
BAS16VV	53
BAS16VY	16*

[1] \* = -: made in Hong Kong

\* = p: made in Hong Kong

\* = t: made in Malaysia

\* = W: made in China

## 5. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
Per diode					
$V_{RRM}$	repetitive peak reverse voltage		-	100	V
$V_R$	reverse voltage		-	100	V
l <sub>F</sub>	forward current		-	200	mA
I <sub>FRM</sub>	repetitive peak forward current		-	450	mA
I <sub>FSM</sub>	non-repetitive peak forward	square wave	<u>[1]</u>		
	current	t <sub>p</sub> = 1 μs	-	4.5	Α
		$t_p = 1 \text{ ms}$	-	1	Α
		t <sub>p</sub> = 1 s	-	0.5	Α
P <sub>tot</sub>	total power dissipation				
	BAS16VV	$T_{amb} \le 25  ^{\circ}C$	[2] _	180	mW
	BAS16VY	$T_{sp} = 85  ^{\circ}C$	[3] _	250	mW
Per device					
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-65	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

<sup>[1]</sup>  $T_i = 25$  °C prior to surge.

## 6. Thermal characteristics

Table 7. Thermal characteristics

Table 11	Thorna onaraotoriotico					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per diode						
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	<u>[1]</u>			
	BAS16VV		[2] _	-	700	K/W
			[3]	-	410	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point					
	BAS16VY		<u>[4]</u> _	-	260	K/W

<sup>[1]</sup> Reflow soldering is the only recommended soldering method.

[4] Soldering points at pins 4, 5 and 6.

<sup>[2]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

<sup>[3]</sup> Soldering points at pins 4, 5 and 6.

<sup>[2]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

<sup>[3]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

## 7. Characteristics

Table 8. Characteristics

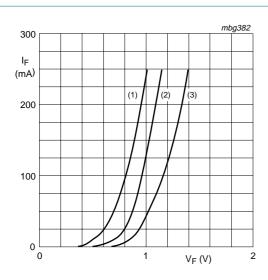
 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per diod	е					
V <sub>F</sub> forward voltage			[1]			
	I <sub>F</sub> = 1 mA	-	-	715	mV	
		$I_F = 10 \text{ mA}$	-	-	855	mV
		$I_F = 50 \text{ mA}$	-	-	1	V
		I <sub>F</sub> = 150 mA	-	-	1.25	V
I <sub>R</sub> reverse current	reverse current	V <sub>R</sub> = 25 V	-	-	30	nA
		V <sub>R</sub> = 75 V	-	-	1	μΑ
		$V_R = 25 \text{ V}; T_j = 150 ^{\circ}\text{C}$	-	-	30	μΑ
		$V_R = 75 \text{ V}; T_j = 150 ^{\circ}\text{C}$	-	-	50	μΑ
C <sub>d</sub>	diode capacitance	$V_R = 0 V$ ; $f = 1 MHz$	-	-	1.5	pF
t <sub>rr</sub>	reverse recovery time		[2] _	-	4	ns
$V_{FR}$	forward recovery voltage		[3]	-	1.75	V

<sup>[1]</sup> Pulse test:  $t_p \le 300 \ \mu s; \ \delta \le 0.02.$ 

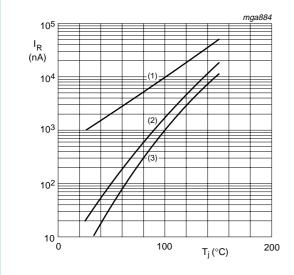
<sup>[2]</sup> When switched from  $I_F$  = 10 mA to  $I_R$  = 10 mA;  $R_L$  = 100  $\Omega;$  measured at  $I_R$  = 1 mA.

<sup>[3]</sup> When switched from  $I_F = 10$  mA;  $t_r = 20$  ns.



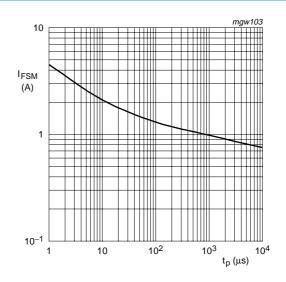
- (1)  $T_{amb} = 150 \,^{\circ}C$ ; typical values
- (2)  $T_{amb} = 25 \,^{\circ}C$ ; typical values
- (3)  $T_{amb} = 25 \,^{\circ}C$ ; maximum values

Fig 1. Forward current as a function of forward voltage



- (1)  $V_R = 75 \text{ V}$ ; maximum values
- (2)  $V_R = 75 V$ ; typical values
- (3)  $V_R = 25 V$ ; typical values

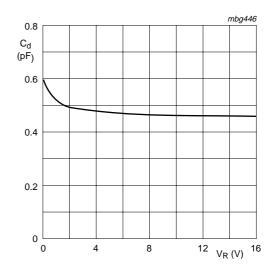
Fig 3. Reverse current as a function of junction temperature



Based on square wave currents.

T<sub>i</sub> = 25 °C; prior to surge

Fig 2. Non-repetitive peak forward current as a function of pulse duration; maximum values

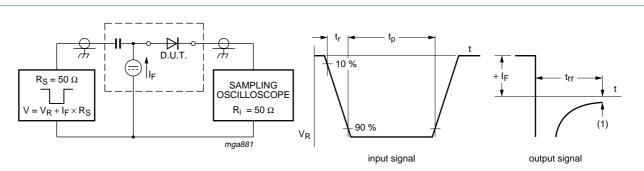


 $f = 1 \text{ MHz}; T_{amb} = 25 \,^{\circ}\text{C}$ 

Fig 4. Diode capacitance as a function of reverse voltage; typical values

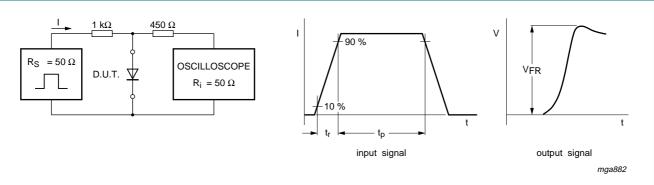
5 of 12

## 8. Test information



(1)  $I_R$  = 1 mA Input signal: reverse pulse rise time  $t_r$  = 0.6 ns; reverse voltage pulse duration  $t_p$  = 100 ns; duty cycle  $\delta$  = 0.05 Oscilloscope: rise time  $t_r$  = 0.35 ns

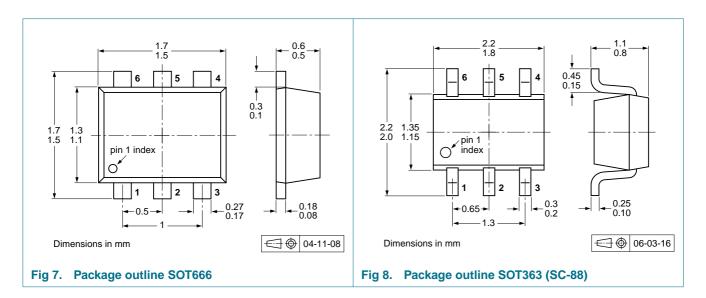
Fig 5. Reverse recovery time test circuit and waveforms



Input signal: forward pulse rise time  $t_r = 20$  ns; forward current pulse duration  $t_p \ge 100$  ns; duty cycle  $\delta \le 0.005$ 

Fig 6. Forward recovery voltage test circuit and waveforms

## 9. Package outline



## 10. Packing information

Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

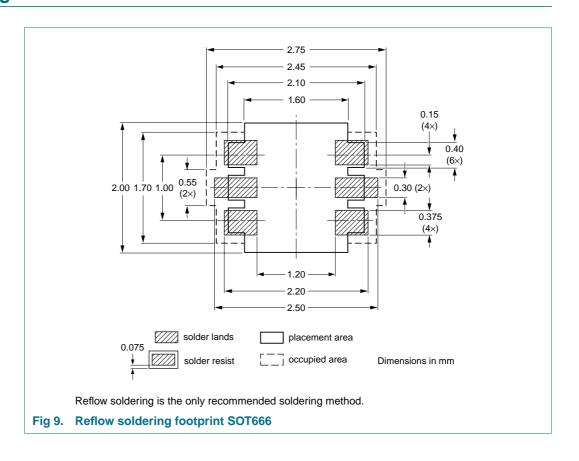
Type number	Package	Description	Packi	ng qua	ntity	
			3000	4000	8000	10000
BAS16VV	SOT666	2 mm pitch, 8 mm tape and reel	-	-	-315	-
		4 mm pitch, 8 mm tape and reel	-	-115	-	-
BAS16VY	SOT363	4 mm pitch, 8 mm tape and reel; T1	-115	-	-	-135
		4 mm pitch, 8 mm tape and reel; T2	-125	-	-	-165

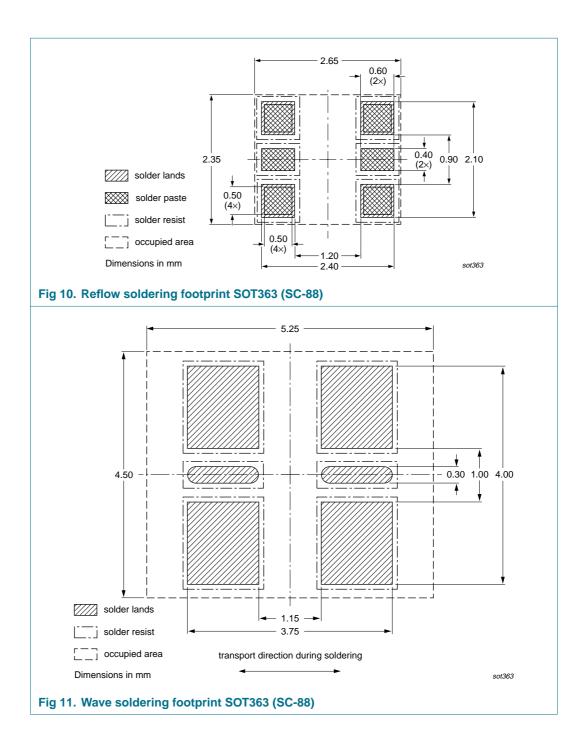
[1] For further information and the availability of packing methods, see <u>Section 14</u>.

[2] T1: normal taping

[3] T2: reverse taping

## 11. Soldering





## 12. Revision history

#### Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
BAS16VV_BAS16VY_3	20070420	Product data sheet	-	BAS16VV_BAS16VY_2		
Modifications:		this data sheet has been NXP Semiconductors.	n redesigned to comply w	rith the new identity		
	<ul> <li>Legal texts have</li> </ul>	ave been adapted to the	new company name whe	re appropriate.		
	<ul> <li><u>Table 2 "Quick reference data"</u>: indication per diode added</li> </ul>					
	<ul> <li><u>Table 2 "Quick reference data"</u>: <u>Table note 1</u> for t<sub>rr</sub> added</li> </ul>					
	<ul> <li><u>Table 5 "Marking codes"</u>: enhanced table note section</li> </ul>					
	• <u>Table 6 "Limiting values"</u> : <u>Table note 3</u> amended					
	• Table 7 "Ther	mal characteristics": indi	cation per diode added			
				n junction to soldering point		
	redefined to R <sub>th(j-sp)</sub> thermal resistance from junction to solder point					
	• <u>Table 7 "Thermal characteristics"</u> : <u>Table note 2</u> , <u>3</u> and <u>4</u> amended					
		racteristics": Table note 1	for V <sub>F</sub> added			
		re title amended				
			fined to T <sub>amb</sub> ambient ten	nperature		
		re title and figure note an	nended			
		re note amended				
	<ul> <li>Figure 7 and 8: superseded by minimized package outline drawings</li> </ul>					
			ethod for SOT666 added			
	<ul> <li><u>Table 9 "Packing methods"</u>: enhanced table note section</li> </ul>					
	Section 11 "Soldering": added					
		egal information": update	ed			
BAS16VV_BAS16VY_2	20040910	Product data sheet	-	BAS16VY_1		
BAS16VY_1	20030408	Product specification	-	-		

## 13. Legal information

#### 13.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <a href="http://www.nxp.com">http://www.nxp.com</a>.

#### 13.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

#### 13.3 Disclaimers

**General** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use** — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or

malfunction of a NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <a href="http://www.nxp.com/profile/terms">http://www.nxp.com/profile/terms</a>, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

#### 13.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

#### 14. Contact information

For additional information, please visit: http://www.nxp.com

For sales office addresses, send an email to: <a href="mailto:salesaddresses@nxp.com">salesaddresses@nxp.com</a>

## 15. Contents

1	Product profile
1.1	General description
1.2	Features
1.3	Applications
1.4	Quick reference data
2	Pinning information 2
3	Ordering information 2
4	Marking 2
5	Limiting values
6	Thermal characteristics 3
7	Characteristics 4
8	Test information 6
9	Package outline
10	Packing information 7
11	Soldering 8
12	Revision history
13	Legal information11
13.1	Data sheet status
13.2	Definitions
13.3	Disclaimers
13.4	Trademarks 11
14	Contact information 11
15	Contents 12

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

