TOSHIBA Photocoupler GaAs Ired & Photo-Triac

TLP360J

Triac Drivers

Programmable Controllers

AC-Output Modules

Solid State Relays

TOSHIBA TLP360J consists of a photo-triac optically coupled to a gallium arsenide infrared-emitting diode in a four-lead plastic DIP package.

• Peak off-state voltage: 600 V (min)

• Trigger LED current: 10 mA (max)

On-state current: 100 mA (max)

• Isolation voltage: 5000 Vrms (min)

•UL approved: UL1577, File No.E67349

•cUL approved : CSA Component Acceptance Service

No. 5A, File No.E67349

•CQC approved: GB4943.1, GB8898 Japan Factory

·Option (D4) VDE approved :

EN60747-5-5, EN60065, EN60950-1 (Note 1)

EN62368-1(Pending) (Note 1)

Note 1: When a EN60747-5-5 approved type is needed, please designate "Option(D4)"

Maximum operating insulation voltage : 890 Vpk
Maximum permissible overvoltage : 8000 Vpk

· Construction mechanical rating

Creepage distance 7.0 mm (min)
Clearance 7.0 mm (min)
Insulation thickness 0.4 mm (min)

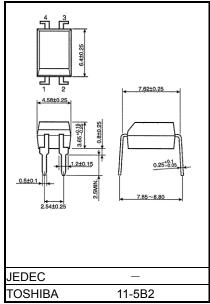
Trigger LED Current

Classification (Note 1)	Trigger LED			
	$V_{T} = 6 V$,	Marking of classification		
	Min	Max	oldosillodilori	
(IFT7)	_	7	T7	
Standard	_	10	T7, blank	

Note 1: Example: "(IFT7)"; "TLP360J(IFT7)"

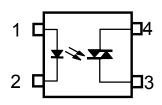
Note: When specifying the application type name for certification testing, be sure to use the standard product type name, e.g. TLP360J(IFT7): TLP360J.

Unit: mm



Weight: 0.26 g (typ.)

Pin Configuration (top view)



- 1: Anode
- 2: Cathode
- 3: Triac Terminal
- 4: Triac Terminal

Start of commercial production 2003-06

Absolute Maximum Ratings (Ta = 25°C)

	Characteristic		Symbol	Rating	Unit	
Forward current			lF	50	mA	
	Forward current derating (Ta ≥ 53°C)		ΔIF /°C	-0.7	mA /°C	
	Peak forward current (100 µs pulse, 100 pps)	eak forward current (100 µs pulse, 100 pps)		1	Α	
LED	Reverse voltage		VR	5	V	
	Diode power dissipation		P _D	100	mW	
	Diode power dissipation derating (Ta ≥ 53°C)		ΔP _D /°C	-1.4	mW/°C	
	Junction temperature		Tj	125	°C	
	Off-state output terminal voltage	V _{DRM}	600	V		
	On-state RMS current	Ta = 25°C	I=	100	mA	
	On-state RWS current	Ta = 70°C	I _{T(RMS)}	50	IIIA	
or .	On-state current derating (Ta ≥ 25°C)	ΔI _T /°C	-1.1	mA /°C		
Detector	Peak on-state current (100 µs pulse, 120 pps)		ITP	2	Α	
D	Peak non-repetitive surge current (Pw = 10 ms)		ITSM	1.2	Α	
	Output power dissipation		Po	300	mW	
	Output power dissipation derating (Ta ≥ 25°C)		ΔP _O /°C	-2.0	mW / °C	
	Junction temperature	Tj	115	°C		
Sto	Storage temperature range			-55 to 125	°C	
Оре	Operating temperature range		Topr	-40 to 100	°C	
Lea	d soldering temperature (10 s)		T _{sol}	260	°C	
Isola	ation voltage (AC, 60 s, R.H. ≤ 60%)	(Note 1)	BVS	5000	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pins 1 and 2 are shorted together and pins 3 and 4 are shorted together.

Recommended Operating Conditions

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Characteristic	Symbol	Min	Тур.	Max	Unit		
Supply voltage	V _A C	_	_	240	V _{ac}		
Forward current	lF	15	20	25	mA		
Peak on-state current	ITP	_	_	1	Α		
Operating temperature	T _{opr}	-25	-	85	°C		

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

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Electrical Characteristics (Ta = 25°C)

	Characteristic Symbol Test Condition		Min	Тур.	Max	Unit	
	Forward voltage	VF	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5 V	_	_	10	μA
	Capacitance	Ст	VF = 0 V, f = 1 MHz	_	30	_	pF
	Peak off-state current	I _{DRM}	V _{DRM} = 600 V	_	10	1000	nA
	Peak on-state voltage	V _{TM}	I _{TM} = 100 mA	_	1.7	3.0	V
Detector	Holding current	lΗ	_	_	1.0	_	mA
Det	Critical rate of rise of off-state voltage	dv/dt	Vin = 240 Vrms, Ta = 85°C (Fig. 1)	200	500	_	V/µs
	Critical rate of rise of commutating voltage	dv/dt(c)	Vin = 60 Vrms, I _T = 15 mA (Fig. 1)		0.2	_	V/µs

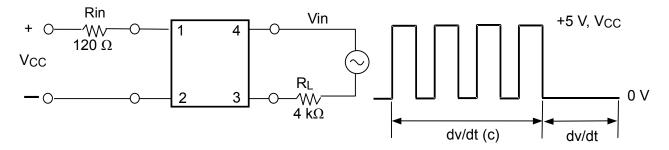
Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	lfT	VT = 3 V	_	5	10	mA
Turn-on time	ton	V_D = 3 \rightarrow 1.5 V , R_L = 20 Ω IF = Rated IFTX1.5	_	30	100	μs

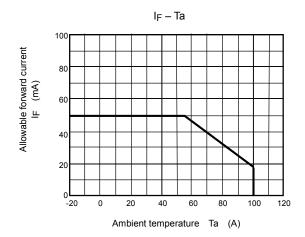
Isolation Characteristics (Ta = 25°C)

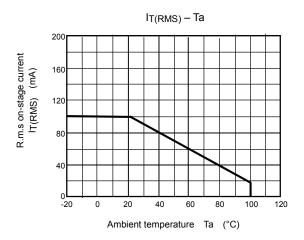
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance (input to output)	CS	V _S = 0 V , f = 1 MHz	_	0.8	_	pF
Isolation resistance	Rs	V _S = 500 V, R.H. ≤ 60%	1×10 ¹²	10 ¹⁴	_	Ω
Isolation voltage	BVS	AC, 60 s	5000	_	_	Vrms
		AC, 1 s, in oil	_	10000	_	VIIIIS
		DC, 60 s, in oil	_	10000	_	Vdc

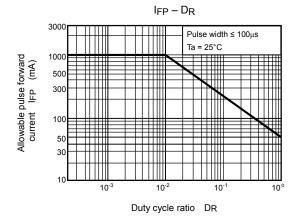
Fig. 1: dv/dt test circuit

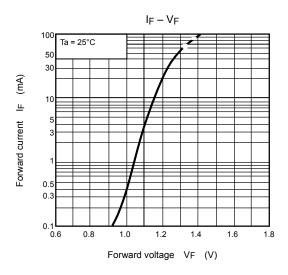


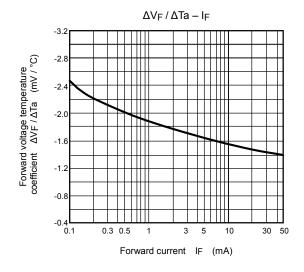
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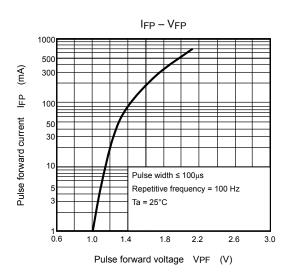




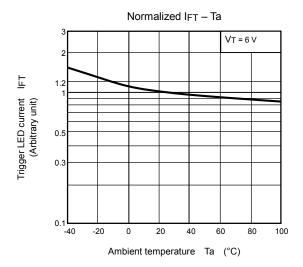


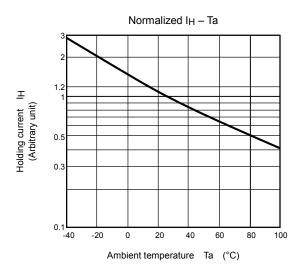


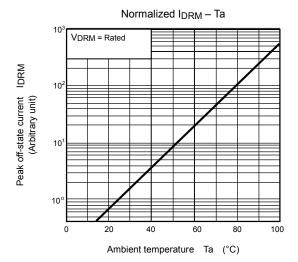


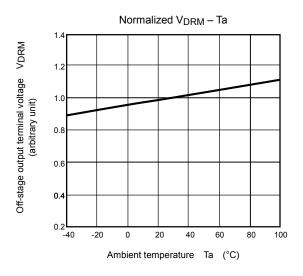


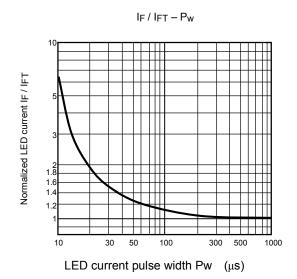
^{*:} The above graphs show typical characteristics.











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