Vishay General Semiconductor

# **Surface-Mount Schottky Barrier Rectifiers**



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### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	1.0 A			
V <sub>RRM</sub>	40 V			
I <sub>FSM</sub>	40 A			
$V_F$ at $I_F$ = 1.0 A ( $T_A$ = 125 °C)	0.37 V			
T <sub>J</sub> max. (AC mode)	150 °C			
T <sub>J</sub> max. (DC forward current)	175 °C			
Package	SMF (DO-219AB)			
Circuit configuration	Single			

### FEATURES

- · Low profile package
- · Ideal for automated placement
- · Low forward voltage drop, low power losses
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Wave and reflow solderable
- AEC-Q101 qualified available - Automotive ordering code: base P/NHM3
- Compatible to SOD-123W package case outline
- Material categorization: for definitions of compliance please see <a href="http://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

### **TYPICAL APPLICATIONS**

For use in high frequency inverters, freewheeling, DC/DC converters, and polarity protection in commercial, industrial, and automotive applications.

### **MECHANICAL DATA**

**Case:** SMF (DO-219AB) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

Polarity: color band denotes the cathode end

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	SS1F4	UNIT	
Device marking code		14		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	40	V	
Maximum average forward rectified current (fig.1)	I <sub>F(AV)</sub> <sup>(1)</sup>	1.0	А	
Peak forward surge current 8.3 ms single half sine-wave $T_{J \text{ (init)}} = 25 \text{ °C}$	I <sub>FSM</sub>	40	A	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	
Junction temperature in DC forward current without reverse bias	TJ	175	°C	

Note

<sup>(1)</sup> Free air, mounted on recommended copper pad area

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SS1F4



RoHS

COMPLIANT

HALOGEN

FREE

Cathode O Anode

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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST C	ONDITIONS	SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 0.7 A	– T <sub>A</sub> = 25 °C		0.43	-	v
	I <sub>F</sub> = 1.0 A		V <sub>F</sub> (1)	0.46	0.52	
	I <sub>F</sub> = 0.7 A	– T <sub>A</sub> = 125 °C	VF	0.33	-	
	I <sub>F</sub> = 1.0 A			0.37	0.43	
Reverse current	V 40.V	T <sub>A</sub> = 25 °C T <sub>A</sub> = 125 °C	1 (2)	-	150	μA
	V <sub>R</sub> = 40 V		I <sub>R</sub> <sup>(2)</sup>	7	25	mA
Typical junction capacitance	4.0 V, 1 MHz		CJ	85	-	pF

Notes

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 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25$ °c unless otherwise noted)				
PARAMETER	SYMBOL	SS1F4	UNIT	
Typical thermal resistance	R <sub>0JA</sub> (1)(2)(3)	125	°C/W	
	R <sub>0JM</sub> (2)(3)	16.5	0/11	

#### Notes

 $^{(1)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

<sup>(2)</sup> Device mounted on FR4 PCB, 2 oz. standard footprint

 $^{(3)}$  Thermal resistance  $R_{\theta JA}$  - junction to ambient;  $R_{\theta JM}$  - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
SS1F4-M3/H	0.015	Н	3000	7" diameter plastic tape and reel	
SS1F4-M3/I	0.015	l	10 000	13" diameter plastic tape and reel	
SS1F4HM3/H (1)	0.015	Н	3000	7" diameter plastic tape and reel	
SS1F4HM3/I <sup>(1)</sup>	0.015	I	10 000	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified



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## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

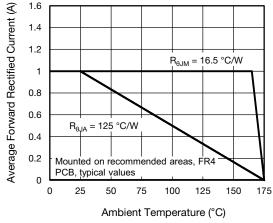
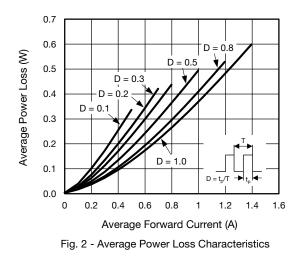
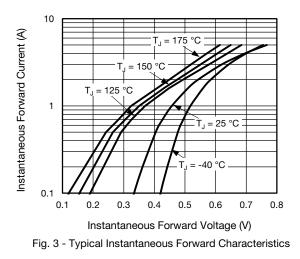


Fig. 1 - Maximum Forward Current Derating Curve





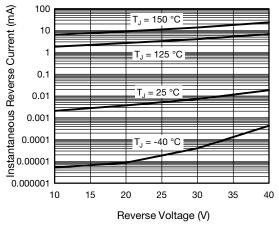
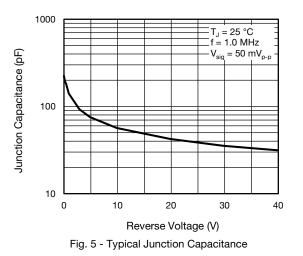


Fig. 4 - Typical Reverse Leakage Characteristics



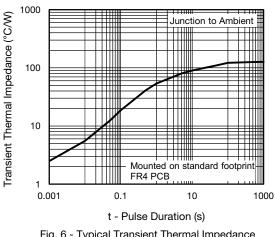


Fig. 6 - Typical Transient Thermal Impedance

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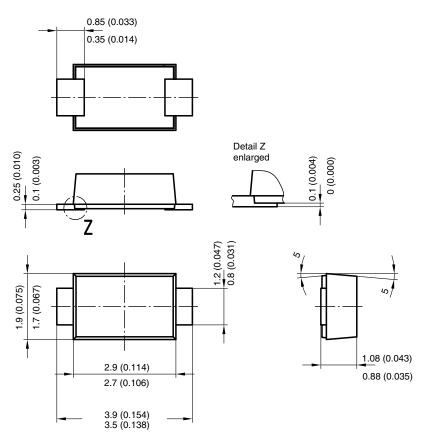
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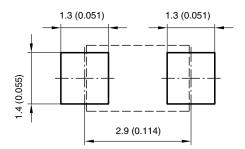
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## **PACKAGE OUTLINE DIMENSIONS** in millimeters (inches)



Foot print recommendation:



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