



COMPLEMENTARY PAIR ENHANCEMENT MODE MOSFET

Product Summary

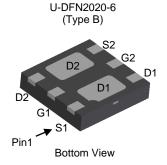
Device	BV _{DSS}	RDS(ON) max	ID MAX TA = +25°C
		$34m\Omega$ @ V _{GS} = 4.5V	5.1A
Q1	12V	40mΩ @ V _{GS} = 2.5V	4.7A
N-Channel	120	$50m\Omega$ @ $V_{GS} = 1.8V$	4.2A
		70mΩ @ V _{GS} = 1.5V	3.6A
		59mΩ @ V _{GS} = -4.5V	-3.9A
Q2	-12V	81mΩ @ V _{GS} = -2.5V	-3.3A
P-Channel		115mΩ @ V _{GS} = -1.8V	-2.8A
		215mΩ @ V _{GS} = -1.5V	-2.0A

Description

This MOSFET has been designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Load Switch
- **Power Management Functions**
- Portable Power Adaptors



Features

- Low On-Resistance
- Low Input Capacitance
- Low Profile, 0.6mm Max Height
- **FSD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can

https://www.diodes.com/products/automotive/automotiveproducts/.

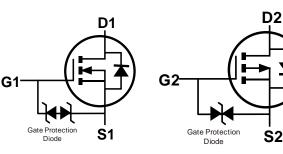
This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

An Automotive-Compliant Part is Available Under Separate Datasheet (DMC1030UFDBQ)

Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202. Method 208(e4)
- Terminals Connections: See Diagram Below
- Weight: 0.0065 grams (Approximate)



N-CHANNEL MOSFET

P-CHANNEL MOSFET

S2

Internal Schematic

Ordering Information (Note 4)

Part Number	Case	Packaging
DMC1030UFDB -7	U-DFN2020-6 (Type B)	3000/Tape & Reel
DMC1030UFDB -13	U-DFN2020-6 (Type B)	10000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/



Marking Information

Site 1



D3 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Date Code Ney												
Year	2014		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	В		Н		J	K	L	М	N	0	Р	R
	,	1	1		1			1	1	1	,	
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Site 2



D3 = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020)
W = Week (ex: a = Week 27; z Represents Week 52 and 53)
X = Internal Code (ex: U = Monday)

Date Code Key

Year	2014		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	4		0	1	2	3	4	5	6	7	8	9
Week	/eek 1-26				27-	-52		53				
Code		Α	-Z			a-z			Z			
Internal Code	Sur	1	Mon		Tue	W	ed	Thu		Fri		Sat
Code	Т		U		V	V	V	Х		Υ		Z



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Q1 N-CHANNEL	Q2 P-CHANNEL	Unit		
Drain-Source Voltage			VDSS	12	-12	V
Gate-Source Voltage	Vgss	±8	±8	V		
Continuous Drain Current (Note 5)	Steady State	T _A = +25°C T _A = +70°C	lD	5.1 4.1	-3.9 -3.1	А
N-Channel: V _G S = 4.5V P-Channel: V _G S = -4.5V	t < 5s	T _A = +25°C T _A = +70°C	lD	6.6 5.3	-5.0 -4.0	А
Maximum Continuous Body Diode Forward Curr	Is	2	-1.7	А		
Pulsed Drain Current (10µs Pulse, Duty Cycle =	1%)		I _{DM}	35	-25	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	Steady State	Pp	1.36	W
Total Fower Dissipation (Note 3)	t < 5s	Pυ	1.89	VV
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D:	92	
Thermal Resistance, Junction to Ambient (Note 3)	t < 5s	$R_{\theta JA}$	66	°C/W
Thermal Resistance, Junction to Case (Note 5)	R _θ JC	18		
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C	

Electrical Characteristics Q1 N-CHANNEL (@ TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition			
OFF CHARACTERISTICS (Note 6)	•								
Drain-Source Breakdown Voltage	BVDSS	12	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$			
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	_	_	1.0	μA	V _{DS} = 12V, V _{GS} = 0V			
Gate-Source Leakage	Igss	_	_	±10	μA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$			
ON CHARACTERISTICS (Note 6)									
Gate Threshold Voltage	Vgs(TH)	0.4	_	1	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$			
		_	17	34		$V_{GS} = 4.5V, I_{D} = 4.6A$			
Static Drain-Source On-Resistance	Pro/ow	_	20	40	mΩ	$V_{GS} = 2.5V, I_{D} = 4.2A$			
Static Drain-Source On-Nesistance	RDS(ON)	_	24	50	11122	$V_{GS} = 1.8V, I_D = 3.8A$			
		_	28	70		V _G S = 1.5V, I _D = 1.5A			
Diode Forward Voltage	VsD	_	0.7	1.2	V	V _G S = 0V, I _S = 4.8A			
DYNAMIC CHARACTERISTICS (Note 7)	DYNAMIC CHARACTERISTICS (Note 7)								
Input Capacitance	Ciss	_	1003	_	pF	.,			
Output Capacitance	Coss	_	132	_	pF	V _{DS} = 6V, V _{GS} = 0V, f = 1.0MHz			
Reverse Transfer Capacitance	Crss	_	115	_	pF	1 - 1.000112			
Gate Resistance	Rg	_	11.3	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$			
Total Gate Charge (V _{GS} = 4.5V)	0	_	12.2	_	nC				
Total Gate Charge (V _{GS} = 8V)	Qg	_	23.1	_	nC	\/ 40\/ I- C 0A			
Gate-Source Charge	Qgs	_	1.3	_	nC	$-V_{DS} = 10V, I_{D} = 6.8A$			
Gate-Drain Charge	Q_{gd}	_	1.5	_	nC]			
Turn-On Delay Time	td(on)	_	4.4	_	ns				
Turn-On Rise Time	t _R		7.4		ns	$V_{DD} = 6V, V_{GS} = 4.5V,$			
Turn-Off Delay Time	tD(OFF)		18.8		ns	$R_L = 1.1\Omega$, $R_G = 1\Omega$			
Turn-Off Fall Time	tr	_	4.9	_	ns	1			
Body Diode Reverse Recovery Time	trr	_	7.6	_	ns	Is = 5.4A, dI/dt = 100A/µs			
Body Diode Reverse Recovery Charge	Qrr	_	0.9	_	nC	Is = 5.4A, dI/dt = 100A/µs			

Notes:

- 5. Device mounted on 1" × 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
 6. Short duration pulse test used to minimize self-heating effect.
 7. Guaranteed by design. Not subject to product testing.

= 85°C

 $T_{A}' = 150^{\circ}C$

 $T_A = 85^{\circ}C$

T_A = -55°C

16 18

 $V_{GS} = 2.5V$ $I_{D} = 5.0A$

100 125

Figure 6 On-Resistance Variation with Temperature

20

25°C



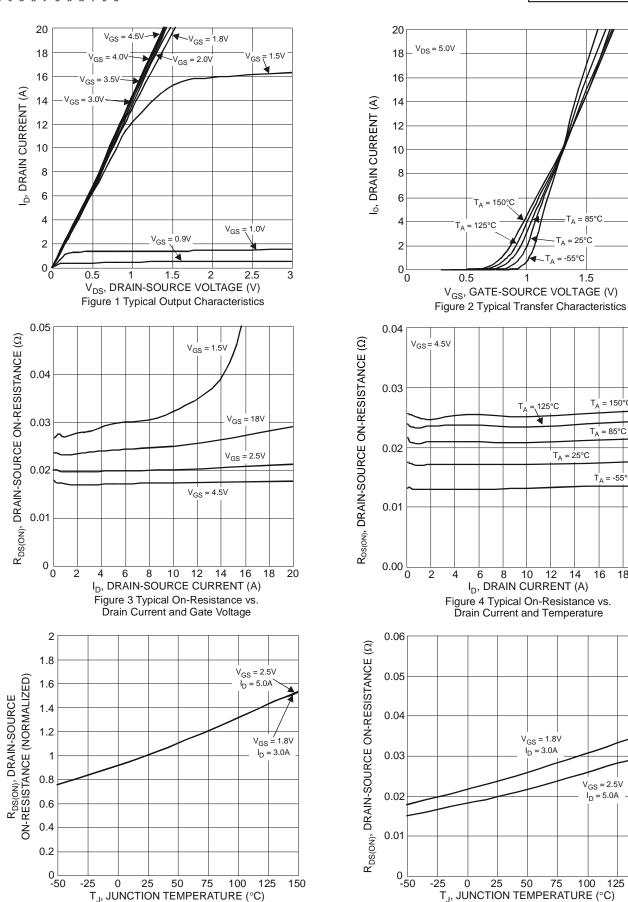


Figure 5 On-Resistance Variation with Temperature



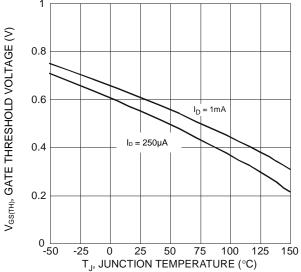
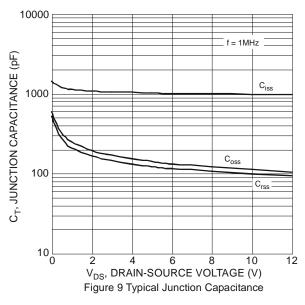
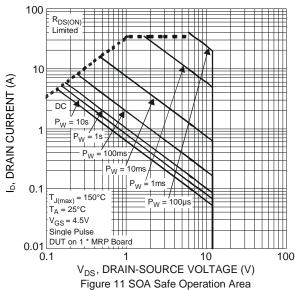
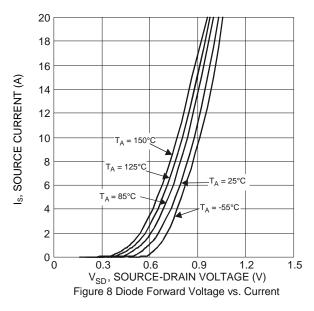
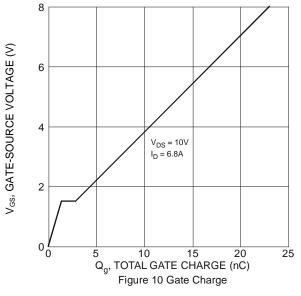


Figure 7 Gate Threshold Variation vs. Junction Temperature









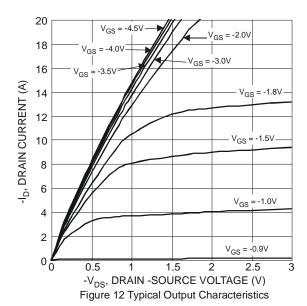


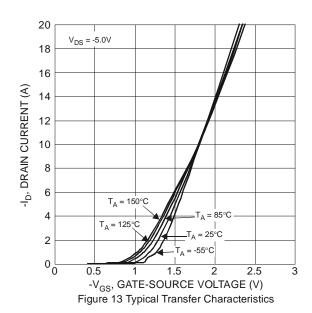
Electrical Characteristics Q2 P-CHANNEL (@ TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition				
OFF CHARACTERISTICS (Note 6)				•						
Drain-Source Breakdown Voltage	BV _{DSS}	-12	1	_	V	$V_{GS} = 0V, I_D = -250\mu A$				
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	_	_	-1.0	μA	V _{DS} = -12V, V _{GS} = 0V				
Gate-Source Leakage	Igss		_	±10	μA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$				
ON CHARACTERISTICS (Note 6)										
Gate Threshold Voltage	Vgs(TH)	-0.4	1	-1	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$				
			37	59		Vgs = -4.5V, ID = -3.6A				
Static Drain-Source On-Resistance	Pro/otil		48	81	mΩ	Vgs = -2.5V, ID = -3.1A				
Static Drain-Source On-Resistance	RDS(ON)		69	115	11122	$V_{GS} = -1.8V, I_D = -2.6A$				
		_	88	215		$V_{GS} = -1.5V, I_D = -0.5A$				
Diode Forward Voltage	VsD	_	-0.7	-1.2	V	Vgs = 0V, Is = -3.7A				
DYNAMIC CHARACTERISTICS (Note 7)				•						
Input Capacitance	Ciss		1028	_	pF	\\ \(\chi_1\)\\\ \(\chi_1\)\\\\ \(\chi_1\)\\\\\\ \(\chi_1\)\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\				
Output Capacitance	Coss		285	_	pF	V _{DS} = -6V, V _{GS} = 0V, -f = 1.0MHz				
Reverse Transfer Capacitance	Crss		254	_	pF	1 - 1.000112				
Gate Resistance	Rg		19.6	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$				
Total Gate Charge (V _{GS} = -4.5V)	0	_	13	_	nC					
Total Gate Charge (V _{GS} = -8V)	Qg	_	20.8	_	nC	\/ 40\/ I- 4.74				
Gate-Source Charge	Qgs		1.8	_	nC	V _{DS} = -10V, I _D = -4.7A				
Gate-Drain Charge	Q_{gd}		4.5	_	nC]				
Turn-On Delay Time	tD(ON)	_	5.6	_	ns					
Turn-On Rise Time	tR	_	12.8	_	ns	V _{DD} = -6V, V _{GS} = -4.5V,				
Turn-Off Delay Time	tD(OFF)	_	30.7	_	ns	$R_L = 1.6\Omega$, $R_G = 1\Omega$				
Turn-Off Fall Time	tF	_	25.4	_	ns	1				
Body Diode Reverse Recovery Time	trr	_	31.6	_	ns	Is = -3.6A, dI/dt = 100A/µs				
Body Diode Reverse Recovery Charge	QRR	_	7.8	_	nC	Is = -3.6A, dI/dt = 100A/μs				

Notes: 6. Short duration pulse test used to minimize self-heating effect.

^{7.} Guaranteed by design. Not subject to product testing.





TA = 85°C

 $T_A = -55^{\circ}C$

125



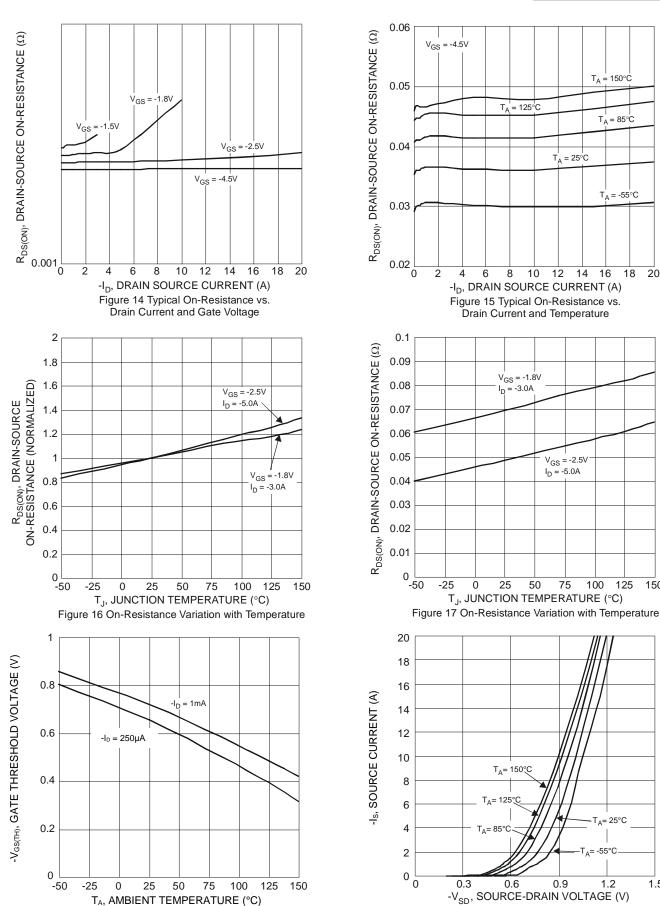


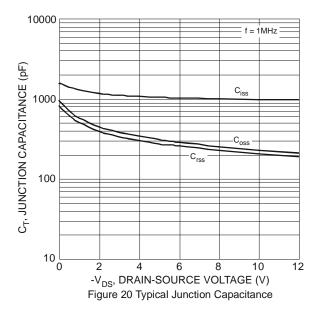
Figure 18 Gate Threshold Variation vs. Ambient Temperature

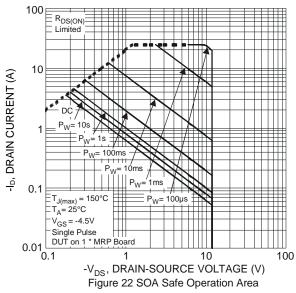
1.2

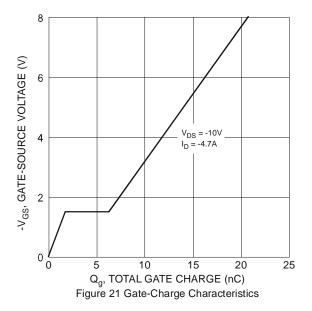
Figure 19 Diode Forward Voltage vs. Current

1.5

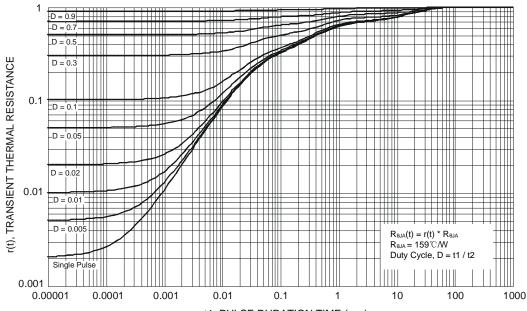












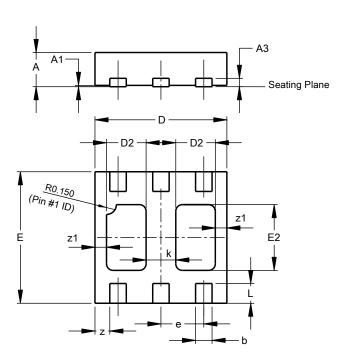
t1, PULSE DURATION TIME (sec) Figure 23 Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type B)

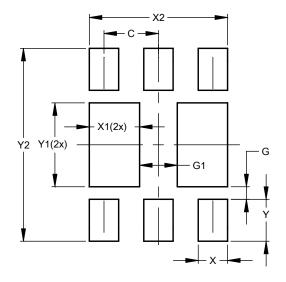


U-DFN2020-6								
Type B								
Dim	Min	Max	Тур					
Α	0.545	0.605	0.575					
A1	0.00	0.05	0.02					
A3	-	-	0.13					
b	0.20	0.30	0.25					
D	1.95	2.075	2.00					
D2	0.50	0.70	0.60					
е	-	-	0.65					
E	1.95	2.075	2.00					
E2	0.90	1.10	1.00					
k	-	-	0.45					
L	0.25	0.35	0.30					
Z	-	-	0.225					
z 1	-	-	0.175					
All	Dimens	ions in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type B)



Dimensions	Value (in mm)
С	0.650
G	0.150
G1	0.450
Х	0.350
X1	0.600
X2	1.650
Υ	0.500
Y1	1.000
Y2	2.300



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 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
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