



PNP PRE-BIASED SMALL SIGNAL SOT23 SURFACE MOUNT TRANSISTOR

Features

- Epitaxial Planar Die Construction •
- Complementary NPN Types Available (DDTC) •
- Built-In Biasing Resistors, R1 = R2
- "Lead Free", RoHS Compliant (Note 1)
- Halogen and Antimony Free "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT23 •
- Case material: Molded Plastic. "Green" Molding Compound. •
- Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish
- Weight: 0.008 grams (approximate)

Part Number	R1, R2 (NOM)
DDTA123ECA	2.2ΚΩ
DDTA143ECA	4.7ΚΩ
DDTA114ECA	10KΩ
DDTA124ECA	22ΚΩ
DDTA144ECA	47ΚΩ
DDTA115ECA	100ΚΩ

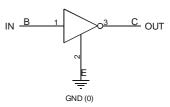
OUT 3

c

R2

2

GND(+)



SOT23





Device Schematic

1

IN

Equivalent Inverter Circuit

Ordering Information (Note 3 & 4)

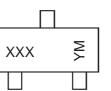
Product	Grade	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DDTA123ECA-7-F	Commercial	P04	7	8	3,000
DDTA143ECA-7-F	Commercial	P08	7	8	3,000
DDTA114ECA-7-F	Commercial	P13	7	8	3,000
DDTA114ECAQ-7-F	Automotive	P13	7	8	3,000
DDTA114ECAQ-13-F	Automotive	P13	13	8	10,000
DDTA124ECA-7-F	Commercial	P17	7	8	3,000
DDTA144ECA-7-F	Commercial	P20	7	8	3,000
DDTA115ECA-7-F	Commercial	P24	7	8	3,000

Notes: 1. No purposefully added lead.

2. Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com.

For packaging details, go to our website at http://www.diodes.com.
Products with Q-suffix are automotive grade. Automotive products are electrical and thermal the same as the commercial, except where specified.

Marking Information



XXX = Product Type Marking Code, See Ordering Information YM = Date Code Marking Y = Year (ex: X = 2010)M = Month (ex: 9 = September)

Date Code Key

Year	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017
Code	Ν	Р	R	S	Т	U	V	W	Х	Y	Z	Α	В	С	D	E
Month	Jan	F	eb	Mar	Apr	M	ay	Jun	Jul	A	ug	Sep	Oct	N	vo	Dec
Code	1		2	3	4		5	6	7	8	3	9	0	1	1	D



Maximum Ratings @T_A = 25°C unless otherwise specified

Chara	cteristic	Symbol	Value	Unit
Supply Voltage <pin: (2)="" (3)="" to=""></pin:>		Vcc	50	V
Input Voltage <pin: (1)="" (2)="" to=""></pin:>	DDTA123ECA DDTA143ECA DDTA114ECA DDTA124ECA DDTA124ECA DDTA144ECA DDTA115ECA	V _{IN}	+10 to -12 +10 to -30 +10 to -40 +10 to -40 +10 to -40 +10 to -40	V
Output Current	DDTA123ECA DDTA143ECA DDTA114ECA DDTA124ECA DDTA124ECA DDTA144ECA DDTA115ECA	lo	-100 -100 -50 -30 -30 -20	mA
Output Current		I _C (Max)	-100	mA

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5 & 6)	PD	200	mW
Thermal Resistance, Junction to Ambient Air (Note 5)	R _{0JA}	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes: 5. Mounted on FR4 PC Board with minimum recommended pad layout

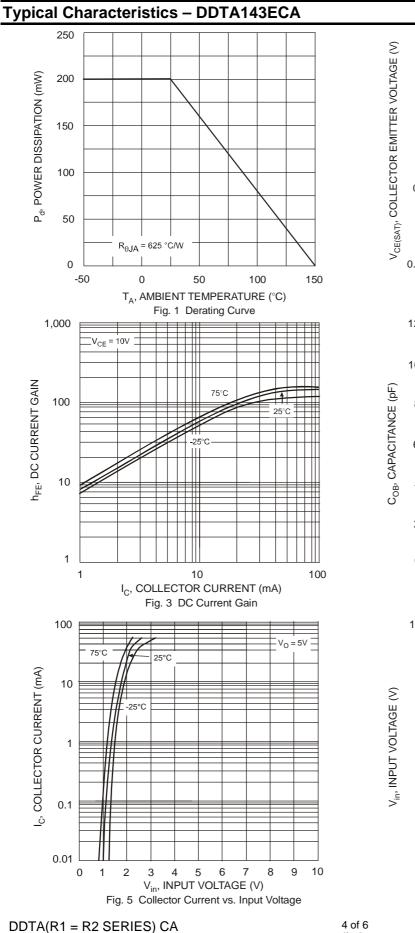
150mW per element must not be exceeded.

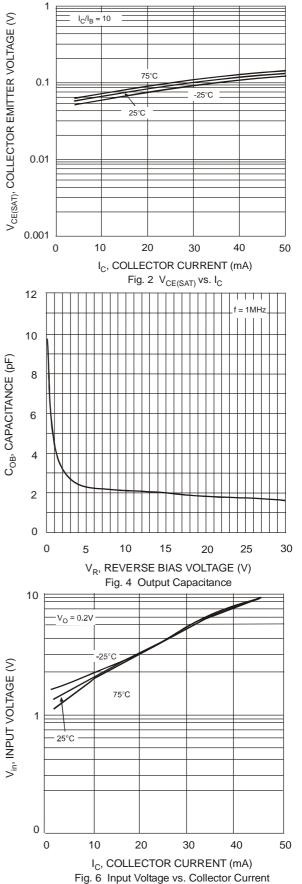


Characteristic Symbol Min Max Unit Test Condition Тур V_{I(off)} -0.5 -1.1 $V_{CC} = -5V, I_O = -100 \mu A$ Vo = -0.3V, Io = -20mA, DDTA123ECA V_O = -0.3V, I_O = -20mA, DDTA143ECA Input Voltage $V_{O} = -0.3V$, $I_{O} = -10mA$, DDTA114ECA V -1.9 -3 VI(on) $V_O = -0.3V$, $I_O = -5mA$, DDTA124ECA $V_0 = -0.3V, I_0 = -2mA, DDTA144ECA$ V_O = -0.3V, I_O = -1mA, DDTA115ECA I_O/I_I = -10mA/-0.5mA DDTA123ECA I_O/I_I = -10mA/-0.5mA DDTA143ECA $I_0/I_1 = -10 \text{mA}/-0.5 \text{mA}$ DDTA114ECA V **Output Voltage** -0.1 -0.3 V_{O(on)} $I_O/I_I = -10 \text{mA}/-0.5 \text{mA}$ DDTA124ECA $I_O/I_I = -10 \text{mA}/-0.5 \text{mA}$ DDTA144ECA $I_0/I_1 = -5mA/-0.25mA$ DDTA115ECA DDTA123ECA -3.8 DDTA143ECA -1.8 DDTA114ECA -0.88 Input Current $V_I = -5V$ h mΑ DDTA124ECA -0.36DDTA144ECA -0.18 DDTA115ECA -0.15 Output Current -0.5 $V_{CC} = -50V, V_I = 0V$ I_{O(off)} μA $V_0 = -5V, I_0 = -20mA$ DDTA123ECA 20 DDTA143ECA $V_0 = -5V, I_0 = -10mA$ 20 DDTA114ECA 30 $V_0 = -5V, I_0 = -5mA$ DC Current Gain G DDTA124ECA 56 $V_0 = -5V$, $I_0 = -5mA$ DDTA144ECA 68 $V_0 = -5V, I_0 = -5mA$ DDTA115ECA 82 $V_0 = -5V, I_0 = -5mA$ Input Resistor Tolerance ΔR_1 -30 +30 % Resistance Ratio Tolerance $\Delta R_2/R_1$ 0.8 1 1.2 % $V_{CE} = -10V, I_E = -5mA,$ MHz Gain-Bandwidth Product f_T 250 ____ f = 100MHz

Electrical Characteristics @T_A = 25°C unless otherwise specified







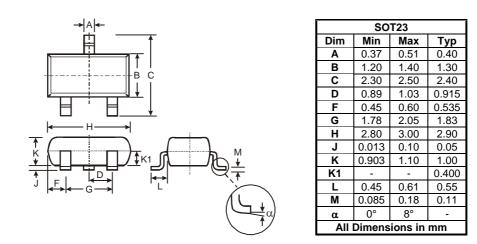
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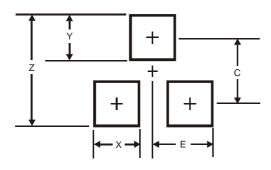
Document number: DS30333 Rev. 8 - 2



Package Outline Dimensions



Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35



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