MMDL6050T1G, SMMDL6050T1G

Switching Diode

Features

- AEC-Q101 Qualified and PPAP Capable
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Continuous Reverse Voltage	V _R	70	Vdc
Peak Forward Current	١ _F	200	mAdc
Peak Forward Surge Current	I _{FM(surge)}	500	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) @T _A = 25°C Derate above 25°C	P _D	200 1.57	mW mW/°C
Thermal Resistance, Junction-to-Ambient	RθJA	635	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to 150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. FR-4 Minimum Pad.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Reverse Breakdown Voltage (I _(BR) = 100 μAdc)	V _(BR)	70	-	Vdc
Reverse Voltage Leakage Current (V _R = 50 Vdc)	۱ _R	-	0.1	μAdc
Forward Voltage (I _F = 1.0 mAdc) (I _F = 100 mAdc)	V _F	0.55 0.85	0.7 1.1	Vdc
Reverse Recovery Time (I _F = I _R = 10 mAdc, I _{R(REC)} = 1.0 mAdc) (Figure 1)	t _{rr}	-	4.0	ns
Capacitance (V _R = 0 V)	С	-	2.5	pF



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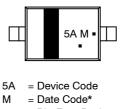
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PLASTIC SOD-323 **CASE 477** STYLE 1



MARKING DIAGRAM



= Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation may vary depending upon manufacturing location.

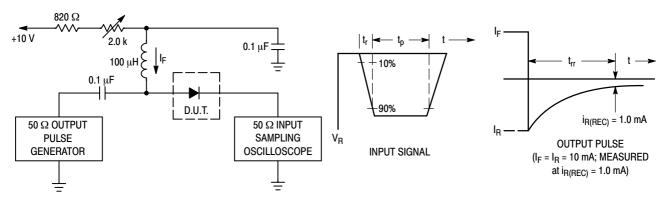
ORDERING INFORMATION

Device	Package	Shipping [†]
MMDL6050T1G	SOD-323 (Pb-Free)	3,000 / Tape & Reel
SMMDL6050T1G	SOD-323 (Pb-Free)	3,000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 10 mA. 2. Input pulse is adjusted so I_{R(peak)} is equal to 10 mA.

3. t_p » t_{rr}

Figure 1. Recovery Time Equivalent Test Circuit

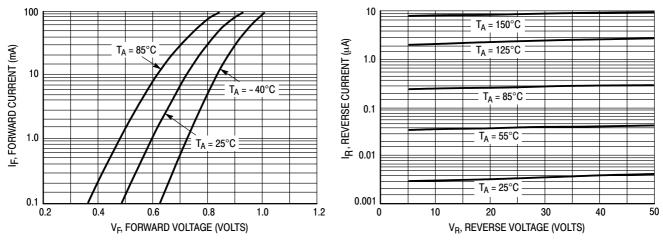
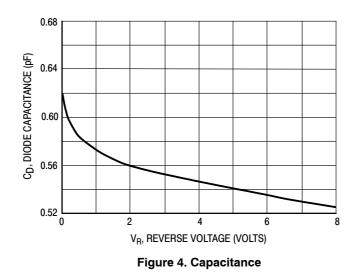


Figure 2. Forward Voltage

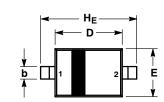
Figure 3. Leakage Current

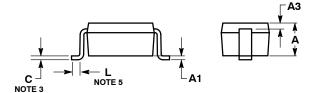




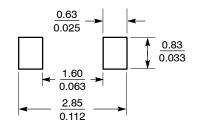








SOLDERING FOOTPRINT*



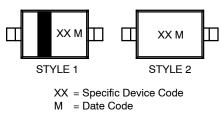
*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

DATE 13 MAR 2007

- NOTES:
 DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: MILLIMETERS.
 LEAD THICKNESS SPECIFIED PER L/F DRAWING WITH SOLDER PLATING.
 DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
 DIMENSION L IS MEASURED FROM END OF RADIUS.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.90	1.00	0.031	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A3	0.15 REF		0.006 REF			
b	0.25	0.32	0.4	0.010	0.012	0.016
С	0.089	0.12	0.177	0.003	0.005	0.007
D	1.60	1.70	1.80	0.062	0.066	0.070
Е	1.15	1.25	1.35	0.045	0.049	0.053
L	0.08			0.003		
HE	2.30	2.50	2.70	0.090	0.098	0.105

GENERIC **MARKING DIAGRAM***



*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present.

STYLE 1: PIN 1. CATHODE (POLARITY BAND) 2. ANODE STYLE 2: NO POLARITY

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SOD-323 CASE 477-02

ISSUE H

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