RB520S30T1G, RB520S30T5G

Schottky Barrier Diode

These Schottky barrier diodes are designed for high-speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand-held and portable applications where space is limited.

Features

- Extremely Fast Switching Speed
- Extremely Low Forward Voltage 0.6 V (max) @ $I_F = 200 \text{ mA}$
- Low Reverse Current
- ESD Rating: Class 3B per Human Body Model Class C per Machine Model
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Reverse Voltage	V_R	30	Vdc
Forward Current DC	ΙF	200	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR–5 Board, (Note 1) $T_A = 25$ °C	P _D	200	mW
Derate above 25°C		1.57	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	635	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C
Non–Repetitive Peak Forward Current, t _p < 10 msec	I _{FSM}	600	mA
Repetitive Peak Forward Current Pulse Wave = 1 sec, Duty Cycle = 66%	I _{FRM}	300	mA
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	635	°C/W

^{1.} FR-5 Minimum Pad.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Leakage (V _R = 10 V)	I _R	-	-	1.0	μΑ
Forward Voltage (I _F = 200 mA)	V _F	-	-	0.60	Vdc



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30 VOLT SCHOTTKY BARRIER DIODE





MARKING DIAGRAM



5J = Device Code M = Date Code*

■ = Pb-Free Package

(Note: Microdot may be in either location)

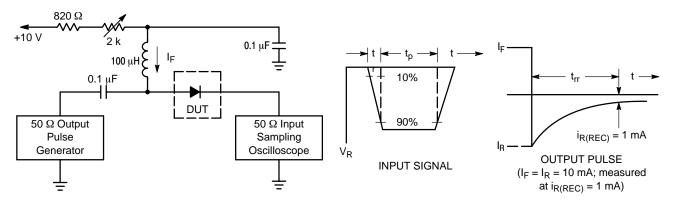
*Date Code orientation position may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
RB520S30T1G	SOD-523 (Pb-Free)	4 mm Pitch 3000/Tape & Reel
RB520S30T5G	SOD-523 (Pb-Free)	2 mm Pitch 8000/Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 10 mA.

- 2. Input pulse is adjusted so $I_{R(peak)}$ is equal to 10 mA.
- 3. t_p » t_{rr}

Figure 1. Recovery Time Equivalent Test Circuit

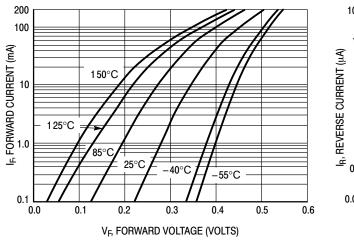


Figure 2. Forward Voltage

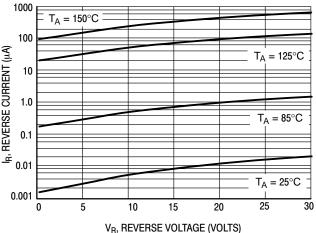


Figure 3. Leakage Current

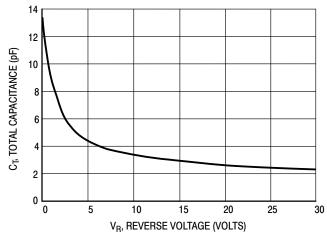


Figure 4. Total Capacitance

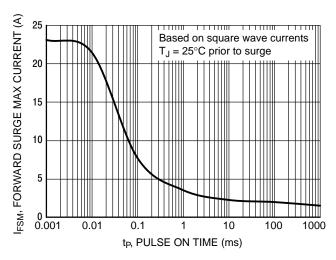
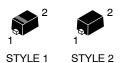
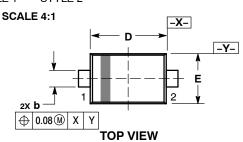


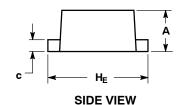
Figure 5. Forward Surge Current

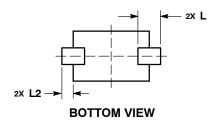


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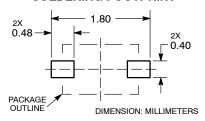
DATE 28 SEP 2010







RECOMMENDED SOLDERING FOOTPRINT*



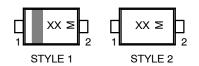
*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS.
 MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PRO-TRUSIONS, OR GATE BURRS.

	MILLIMETERS		
DIM	MIN	NOM	MAX
Α	0.50	0.60	0.70
b	0.25	0.30	0.35
С	0.07	0.14	0.20
D	1.10	1.20	1.30
E	0.70	0.80	0.90
HE	1.50	1.60	1.70
L	0.30 REF		
L2	0.15	0.20	0.25

GENERIC MARKING DIAGRAM*



XX = Specific Device Code Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

STYLE 2: NO POLARITY STYLE 1: PIN 1. CATHODE (POLARITY BAND) 2. ANODE

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