

Product Overview

MC74HC238A: 1-of-8 Decoder/Demultiplexer

For complete documentation, see the data sheet.

The MC74HC238A is identical in pinout to the LS238. The device inputs are compatible with standard CMOS outputs; with pullup resistors, they are compatible with LSTTL outputs. The HC238A decodes a three bit Address to one of eight active high outputs. This device features three Chip Select inputs, two active low and one active high to facilitate the demultiplexing, cascading, and chip selecting functions. The demultiplexing function is accomplished by using the Address inputs to select the desired device output; one of the Chip Selects is used as a data input while the other Chip Selects are held in their active states.

Features

- Output Drive Capability: 10 LSTTL Loads
- Outputs Directly Interface to CMOS, NMOS and TTL
- Operating Voltage Range: 2.0 to 6.0 V
- Low Input Current: 1.0 μ A
- High Noise Immunity Characteristic of CMOS Devices
- In Compliance with the Requirements Defined by JEDEC Standard No. 7A
- Chip Complexity: 100 FETs or 29 Equivalent Gates
- These are PbFree Devices

Applications

- Industrial

Part Electrical Specifications

Product	Compliance	Status	Channels	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC74HC238ADG	Pb-free Halide free	Active	1	2	6	19	null	SOIC-16
MC74HC238ADR2G	Pb-free Halide free	Active	1	2	6	19	null	SOIC-16
MC74HC238ADTG	Pb-free Halide free	Active	1	2	6	19	null	TSSOP-16
MC74HC238ADTR2G	Pb-free Halide free	Active	1	2	6	19	null	TSSOP-16
NLV74HC238ADR2G	Pb-free Halide free	Active	1	2	6	19	null	SOIC-16
NLV74HC238ADTR2G	AEC Qualified PPAP Capable Pb-free Halide free	Active	1	2	6	19	null	TSSOP-16

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