



Low Loss DuoPack : IGBT in **TrenchStop**[®] and Fieldstop technology with soft, fast recovery anti-parallel Emitter Controlled HE diode

- Approx. 1.0V reduced V_{CE(sat)} and 0.5V reduced V_F compared to BUP313D
 - Short circuit withstand time 10µs
- Designed for :
 - Frequency Converters
 - Uninterrupted Power Supply
- **TrenchStop**[®] and Fieldstop technology for 1200 V applications offers :
 - very tight parameter distribution
 - high ruggedness, temperature stable behavior
- NPT technology offers easy parallel switching capability due to
- positive temperature coefficient in $V_{CE(sat)}$
- Low EMI
- Low Gate Charge
- Very soft, fast recovery anti-parallel Emitter Controlled HE diode
- Qualified according to JEDEC¹ for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : <u>http://www.infineon.com/igbt/</u>

Туре	V _{CE}	<i>I</i> c	V _{CE(sat),Tj=25℃}	T _{j,max}	Marking Code	Package	
IKW15T120	1200V	15A	1.7V	150°C	K15T120	PG-TO-247-3	

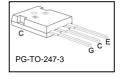
Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V _{CE}	1200	V
DC collector current $T_{\rm C} = 25^{\circ}{\rm C}$ $T_{\rm C} = 100^{\circ}{\rm C}$	I _C	30 15	A
Pulsed collector current, t_p limited by T_{jmax}	I _{Cpuls}	45	
Turn off safe operating area $V_{CE} \le 1200 V, T_{j} \le 150^{\circ} C$	-	45	
Diode forward current	I _F		
$T_{\rm C} = 25^{\circ}{\rm C}$		30	
$T_{\rm C} = 100^{\circ}{\rm C}$		15	
Diode pulsed current, t_p limited by T_{jmax}	I _{Fpuls}	45	
Gate-emitter voltage	V _{GE}	±20	V
Short circuit withstand time ²⁾ $V_{GE} = 15V, V_{CC} \le 1200V, T_j \le 150^{\circ}C$	t _{SC}	10	μS
Power dissipation	P _{tot}	110	W
$T_{\rm C} = 25^{\circ}{\rm C}$			
Operating junction temperature	Tj	-40+150	°C
Storage temperature	T _{stg}	-55+150	

¹ J-STD-020 and JESD-022

²⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.





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Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit	
Characteristic					
IGBT thermal resistance,	$R_{\rm thJC}$		1.1	K/W	
junction – case					
Diode thermal resistance,	R _{thJCD}		1.5		
junction – case					
Thermal resistance,	R _{thJA}		40		
junction – ambient					

Electrical Characteristic, at T_j = 25 °C, unless otherwise specified

Devementer	Cumbal	Conditions	Value			11
Parameter	Symbol		min.	typ.	max.	Unit
Static Characteristic						
Collector-emitter breakdown voltage	V _{(BR)CES}	$V_{\rm GE} = 0V, I_{\rm C} = 0.5 {\rm mA}$	1200	-	-	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{\rm GE} = 15 V, I_{\rm C} = 15 A$				
		T _j =25°C	-	1.7	2.2	
		<i>T</i> _j =125°C	-	2.0	-	
		<i>T</i> _j =150°C	-	2.2	-	
Diode forward voltage	V _F	$V_{\rm GE} = 0V, I_{\rm F} = 15A$				
		T _j =25°C	-	1.7	2.2	
		<i>T</i> _j =125°C	-	1.7	-	
		<i>T</i> _j =150°C	-	1.7	-	
Gate-emitter threshold voltage	V _{GE(th)}	$I_{\rm C}=0.6{\rm mA}, V_{\rm CE}=V_{\rm GE}$	5.0	5.8	6.5	
Zero gate voltage collector current	I _{CES}	V _{CE} =1200V, V _{GE} =0V				mA
		T _j =25°C	-	-	0.2	
		<i>T</i> _j =150°C	-	-	2.0	
Gate-emitter leakage current	I _{GES}	$V_{CE}=0V, V_{GE}=20V$	-	-	100	nA
Transconductance	$g_{ m fs}$	$V_{\rm CE}$ =20V, $I_{\rm C}$ =15A	-	10	-	S
Integrated gate resistor	R _{Gint}			none	-	Ω



Dynamic Characteristic

Input capacitance	Ciss	$V_{\rm CE}=25\rm V,$	-	1100	-	pF
Output capacitance	Coss	$V_{\rm GE}=0V$,	-	100	-	
Reverse transfer capacitance	Crss	<i>f</i> =1MHz	-	50	-	
Gate charge	Q _{Gate}	$V_{\rm CC} = 960 \text{V}, \ I_{\rm C} = 15 \text{A}$	-	85	-	nC
		$V_{GE}=15V$				
Internal emitter inductance	L _E		-	13	-	nH
measured 5mm (0.197 in.) from case						
Short circuit collector current ¹⁾	I _{C(SC)}	$V_{GE} = 15V, t_{SC} \le 10 \mu s$ $V_{CC} = 600V,$ $T_j = 25^{\circ}C$	-	90	-	A

Switching Characteristic, Inductive Load, at T_j =25 °C

Deremeter	Symbol	Conditions	Value			11
Parameter			min.	typ.	max.	Unit
IGBT Characteristic						
Turn-on delay time	t _{d(on)}	$T_{j}=25^{\circ}C,$ $V_{CC}=600V, I_{C}=15A,$ $V_{GE}=0/15V,$ $R_{G}=56\Omega,$ $L_{\sigma}^{2)}=180nH,$ $C_{\sigma}^{2)}=39pF$ Energy losses include "tail" and diode reverse recovery.	-	50	-	ns
Rise time	t _r		-	30	-	
Turn-off delay time	t _{d(off)}		-	520	-	
Fall time	t _f		-	60	-	
Turn-on energy	Eon		-	1.3	-	mJ
Turn-off energy	E _{off}		-	1.4	-	
Total switching energy	Ets		-	2.7	-	
Anti-Parallel Diode Characteristic						
Diode reverse recovery time	t _{rr}	<i>T</i> _j =25°C,	-	140	-	ns
Diode reverse recovery charge	Q _{rr}	$V_{\rm R}$ =600V, $I_{\rm F}$ =15A, $di_{\rm F}/dt$ =600A/µs	-	1.9	-	μC
Diode peak reverse recovery current	<i>I</i> _{rrm}		-	17	-	А
Diode peak rate of fall of reverse recovery current during $t_{\rm b}$	di _{rr} /dt		-	230	-	A/μs

¹⁾ Allowed number of short circuits: <1000; time between short circuits: >1s. ²⁾ Leakage inductance L_{σ} and Stray capacity C_{σ} due to dynamic test circuit in Figure E.



Switching Characteristic, Inductive Load, at T_i =150 °C

Devementer	Symbol	Conditions	Value			11
Parameter			min.	typ.	max.	Unit
IGBT Characteristic						
Turn-on delay time	t _{d(on)}	<i>T</i> _j =150°C,	-	50	-	ns
Rise time	t _r	$V_{CC} = 600V, I_C = 15A,$ $V_{GE} = 0/15V,$ $R_G = 56\Omega$ $L_{\sigma}^{(1)} = 180nH,$ $C_{\sigma}^{(1)} = 39pF$	-	35	-	
Turn-off delay time	$t_{d(off)}$		-	600	-	
Fall time	t _f		-	120	-	
Turn-on energy	Eon		-	2.0	-	mJ
Turn-off energy	E _{off}	Energy losses include "tail" and diode	-	2.1	-	
Total switching energy	E _{ts}	reverse recovery.	-	4.1	-	
Anti-Parallel Diode Characteristic						•
Diode reverse recovery time	t _{rr}	<i>T</i> _j =150°C	-	330	-	ns
Diode reverse recovery charge	Q _{rr}	V _R =600V, <i>I</i> _F =15A,	-	3.4	-	μC
Diode peak reverse recovery current	I _{rrm}	<i>di</i> _F / <i>dt</i> =600A/μs	-	21	-	A
Diode peak rate of fall of reverse recovery current during $t_{\rm b}$	di _{rr} /dt		-	190	-	A/μs

 $^{1)}$ Leakage inductance L_{σ} and Stray capacity C_{σ} due to dynamic test circuit in Figure E.



TrenchStop® Series

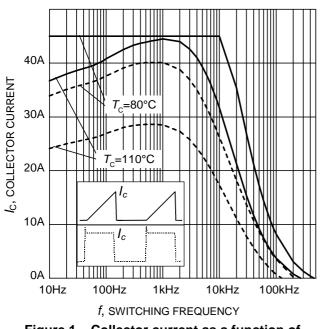
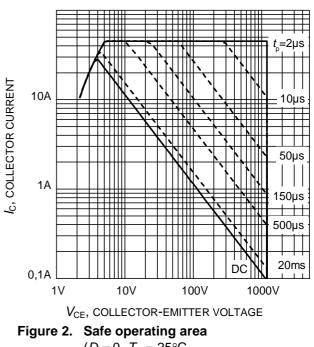
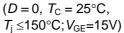
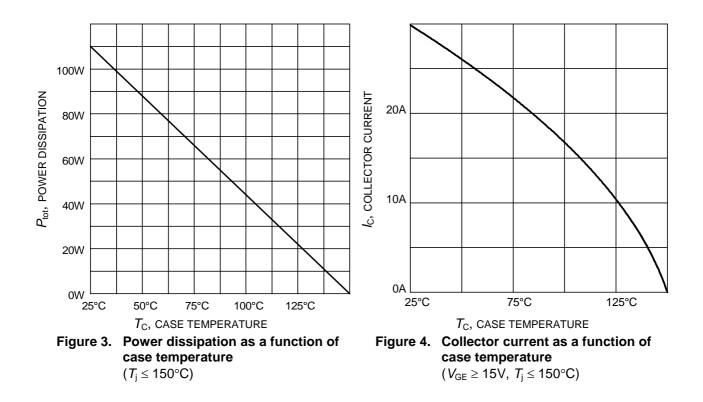


Figure 1. Collector current as a function of switching frequency $(T_i \le 150^{\circ}C, D = 0.5, V_{CE} = 600V,$

 $V_{\rm GE} = 0/+15 \text{V}, R_{\rm G} = 56 \Omega$





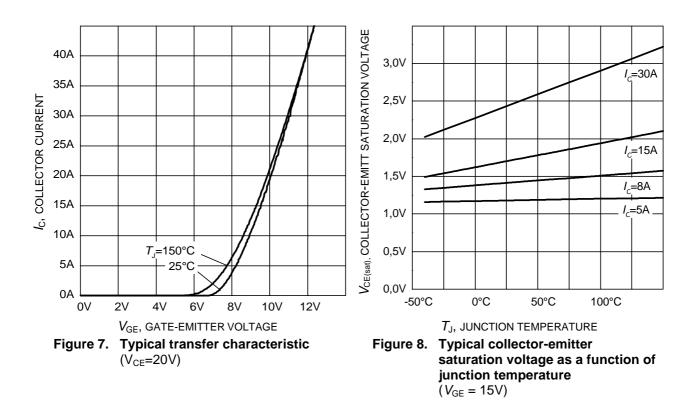




 $l_{\rm C}$, collector current

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40A 40A $V_{GE} = 17V$ $V_{\text{GE}} = 17 \text{V}$ Ic, COLLECTOR CURRENT 15V İ5₩ 30A 30A 13V-13 11V 11V 20A 20A -9V l٩١ 7V 10A 10A 0A 0A 2V 3V 4V 6V 0V 1V 2V ЗV 4V 5V 6V 0V 1V 5V V_{CE} , COLLECTOR-EMITTER VOLTAGE V_{CE} , COLLECTOR-EMITTER VOLTAGE Figure 5. Typical output characteristic Figure 6. Typical output characteristic $(T_{i} = 25^{\circ}C)$ $(T_{i} = 150^{\circ}C)$





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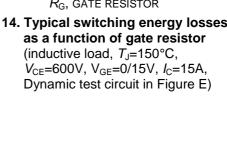
1µs t, SWITCHING TIMES t, SWITCHING TIMES 100ns 100ns 10ns t_{r} 10ns 1ns 1ns 0A 10A 20A 10Ω 35Ω 60Ω 85Ω 110Ω I_C , COLLECTOR CURRENT $R_{\rm G}$, gate resistor Figure 9. Typical switching times as a Figure 10. Typical switching times as a function of gate resistor function of collector current (inductive load, $T_{\rm l}$ =150°C, (inductive load, $T_{\rm l}$ =150°C, $V_{\rm CF}$ =600V, $V_{\rm GF}$ =0/15V, $R_{\rm G}$ =56 Ω , V_{CE}=600V, V_{GE}=0/15V, I_C=15A, Dynamic test circuit in Figure E) Dynamic test circuit in Figure E) t_{d(off)} 7V V_{GE(th)}, GATE-EMITT TRSHOLD VOLTAGE 6V t, SWITCHING TIMES max. 5V typ. 100ns 4V min. ЗV l_{d(on} 2V t, 1V 0V 10ns 0°C 50°C -50°C 50°C 100°C 150°C 0°C 100°C 150°C $T_{\rm J}$, JUNCTION TEMPERATURE $T_{\rm J}$, JUNCTION TEMPERATURE Figure 11. Typical switching times as a Figure 12. Gate-emitter threshold voltage as function of junction temperature a function of junction temperature (inductive load, V_{CE}=600V, $(I_{\rm C} = 0.6 {\rm mA})$ $V_{GE}=0/15V, I_{C}=15A, R_{G}=56\Omega,$ Dynamic test circuit in Figure E)

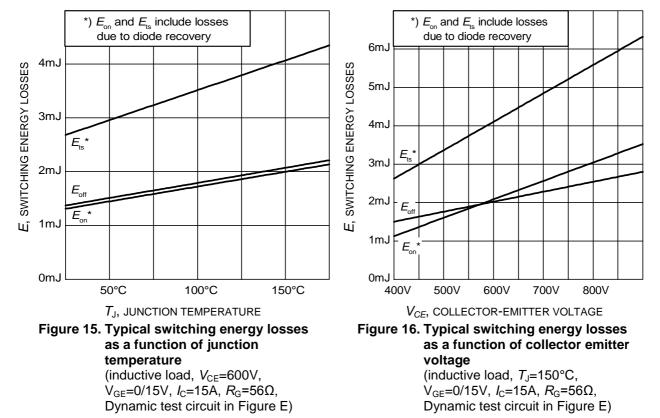


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*) E_{on} and E_{ts} include losses *) E_{on} and E_{ts} include losses 5 mJ due to diode recovery due to diode recovery E_{ts}^* 8,0mJ SWITCHING ENERGY LOSSES SWITCHING ENERGY LOSSES 4 mJ 6,0mJ E 3 mJ 4,0mJ E 2 mJ E_{ts} ш Щ́ 2,0mJ E 1 mJ E 0,0mJ 0 mJ 25A 5A 10A 20A 15A 50 30Ω 55Ω 80Ω 105Ω $I_{\rm C}$, COLLECTOR CURRENT $R_{\rm G}$, gate resistor Figure 14. Typical switching energy losses Figure 13. Typical switching energy losses as a function of collector current as a function of gate resistor (inductive load, $T_{l}=150^{\circ}C$,

 V_{CE} =600V, V_{GE} =0/15V, R_{G} =56 Ω , Dynamic test circuit in Figure E)







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$C_{\rm iss}$ 1nF V_{GE}, GATE-EMITTER VOLTAGE 15V C, CAPACITANCE 240V 960V 10V C 100pF C, 5V 10pF 0١ 0nC 10V 50nC 100nC 0V 20V Q_{GE} , GATE CHARGE V_{CE} , COLLECTOR-EMITTER VOLTAGE Figure 17. Typical gate charge Figure 18. Typical capacitance as a function $(I_{\rm C}=15~{\rm A})$ of collector-emitter voltage

 $l_{\mathrm{c(sc)}}$, short circuit collector current tsc, SHORT CIRCUIT WITHSTAND TIME 15µs 125A 100A 10µs 75A 50A 5µs 25A 0µs └─ 12V 0A └─ 12V 14V 16V 14V 16V 18V V_{GE} , GATE-EMITTETR VOLTAGE V_{GE} , GATE-EMITTETR VOLTAGE Figure 20. Typical short circuit collector Figure 19. Short circuit withstand time as a function of gate-emitter voltage current as a function of gate- $(V_{CE}=600V, \text{ start at } T_{J}=25^{\circ}C)$ emitter voltage

 $(V_{CE} \le 600 \text{V}, T_{i} \le 150^{\circ} \text{C})$

 $(V_{GF}=0V, f = 1 \text{ MHz})$



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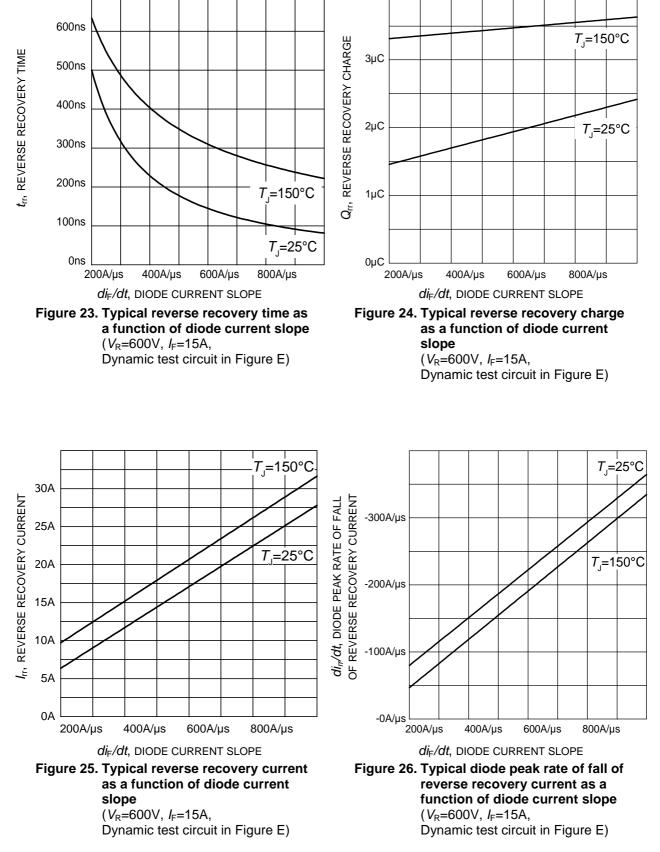
V_{CE}, COLLECTOR-EMITTER VOLTAGE V_{CE} 600V 30A 30A 600V COLLECTOR CURRENT 400V 20A 20A 400V I_c <u>,</u> 200V 10A 10A 200V $V_{\rm CE}$ **і**с 0V 0\ 0A 0A 0.5us 0us 1us 1.5us 0us 1us 1.5us 0.5us t, time t, time Figure 21. Typical turn on behavior Figure 22. Typical turn off behavior $(V_{GE}=0/15V, R_{G}=56\Omega, T_{i}=150^{\circ}C,$ $(V_{GE}=15/0V, R_{G}=56\Omega, T_{i}=150^{\circ}C,$ Dynamic test circuit in Figure E) Dynamic test circuit in Figure E) $Z_{ ext{thJC}}$, TRANSIENT THERMAL RESISTANCE 10[°]K/W $Z_{ m thJC}$, TRANSIENT THERMAL RESISTANCE 10[°]K/W =0 D=0.5 R, (K/W)(s τ, 0.360 7.30*10 R,(K/W)(s) 0.477 8.13*10 0.121 1.73*10 0.434 1.09*10 *10 0 0.372 2 1.55*10 0.224 2.57*10 0.381 0.0 10⁻¹K/W R_2 10⁻¹K/W 71*10 0.226 2 -0 R 0.02 0.01 $C_1 = \tau_1 / R_1$ $C_2 = \tau_2 / R_2$ single pulse 0.01 $C_1 =$ τ_1/R_1 $C_2 = \tau_2 / R_2$ single pulse 10⁻²K/W 10⁻²K/W 10µs 100µs 1ms 10ms 100ms 10µs 100µs 1ms 10ms 100ms t_P, PULSE WIDTH t_P, PULSE WIDTH Figure 23. IGBT transient thermal resistance Figure 24. Diode transient thermal impedance as a function of pulse $(D = t_p / T)$ width

 $(D=t_{\rm P}/T)$

IFAG IPC TD VLS



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40A $T_{J}=25^{\circ}C$ 150^{\circ}C 30A 20A 10A 0A 0V 1V 2V V_F, FORWARD VOLTAGE

Figure 27. Typical diode forward current as a function of forward voltage

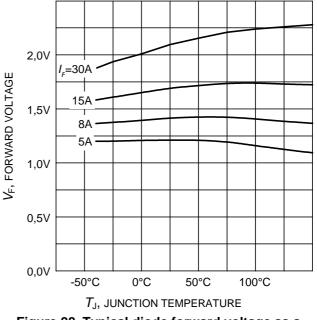
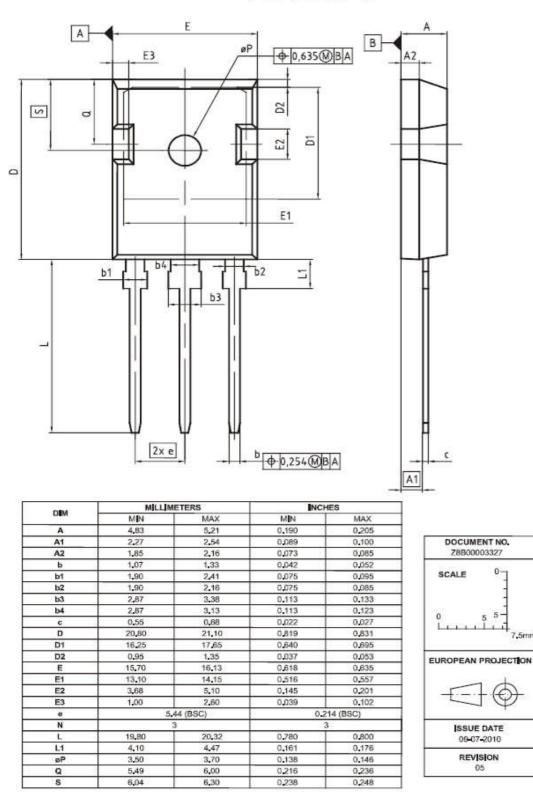


Figure 28. Typical diode forward voltage as a function of junction temperature



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PG-TO247-3



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7.5mm



V_{GE}

 $I_{\mathbf{C}}$

 V_{CE}

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i,v - 90% V_{GE} di_ /dt $t_{\rm rr} = t_{\rm S} + t_{\rm F}$ $Q_{\mu} = Q_{s} + Q_{p}$ 10% V_{GE} I_F t 10% /_{rrm} /dt V_R di **,** 90% / _{, , m} 90% I_C 90% I_C 10% I_c 10% I_c Figure C. Definition of diodes switching characteristics 90% //_{CE} τ1 τ_n r₂ p(t) **r**₂ r_n r1 f_{d(off)} †_f f_{d(on)} t_r SIS00053 Figure A. Definition of switching times Т_С V_{GE} 90% V_{GE} Figure D. Thermal equivalent circuit 10% V_{GE} I_C **½∙L**σ D.U.T. (Diode) C_{σ} 1% I_C V_{CE} U D.U.T. (IGBT) $E_{\text{OFF}} = \int_{f_1}^{2} V_{\text{CE}} \times I_{\text{C}} \times dt$ $E_{\rm ON} = \int_{t_3} V_{\rm CE} \times I_{\rm C} \times dt$ 3% V_{CE} ¹∕₂•L_σ Æ t, t_3 *†*4

Figure E. Dynamic test circuit Leakage inductance L_{σ} =180nH and Stray capacity C_{σ} =39pF.

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Figure B. Definition of switching losses

SIS



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