

60 V, 350 mA dual N-channel Trench MOSFET

28 December 2022

Product data sheet

1. General description

Dual N-channel enhancement mode Field-Effect Transistor (FET) in an ultra small SOT666 Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Logic-level compatible
- Very fast switching
- Trench MOSFET technology

3. Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transist	tor						
V _{DS}	drain-source voltage	T _{amb} = 25 °C		-	-	60	V
V _{GS}	gate-source voltage	_		-20	-	20	V
ID	drain current	V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	-	350	mA
Static chara	acteristics (per transistor)						
R _{DSon}	drain-source on-state resistance	V_{GS} = 10 V; I _D = 500 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.01; T _j = 25 °C		-	1	1.6	Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².



5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S1	source1	6 5 4	D1 D2
2	G1	gate1		
3	D2	drain2		
4	S2	source2	o	
5	G2	gate2		G1 S1 S2 G2
6	D1	drain1	SOT666	017aaa254

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
2N7002PV	SOT666	plastic, surface-mounted package; 6 leads; 0.5 mm pitch; 1.6 mm x 1.2 mm x 0.55 mm body	SOT666

7. Marking

Table 4. Marking codes					
Type number	Marking code				
2N7002PV	ZF				

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8. Limiting values

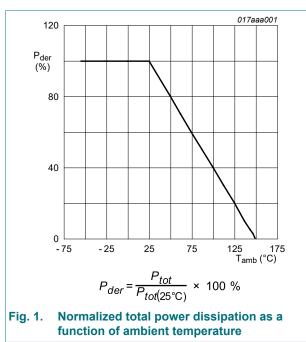
Table 5. Limiting values

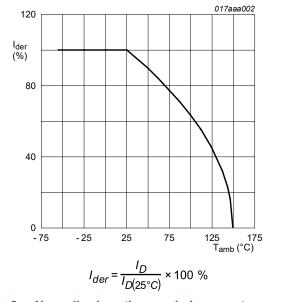
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
Per transiste	or					
V _{DS}	drain-source voltage	T _{amb} = 25 °C		-	60	V
V _{GS}	gate-source voltage			-20	20	V
ID	drain current	V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	350	mA
		V _{GS} = 10 V; T _{amb} = 100 °C	[1]	-	250	mA
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	1.2	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	330	mW
			[1]	-	390	mW
		T _{sp} = 25 °C		-	1090	mW
Per device				I		
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	500	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-draii	n diode					
I _S	source current	T _{amb} = 25 °C	[1]	-	350	mA

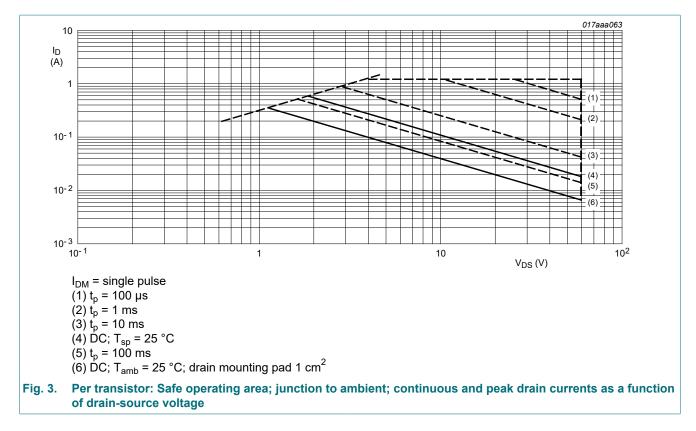
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.









2N7002PV

Product data sheet

4/14

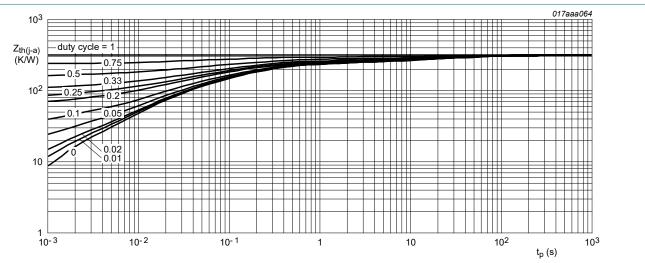
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per device			·				
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	250	K/W
Per transisto	or			I			
R _{th(j-a)}	thermal resistance from	in free air	[1]	-	330	380	K/W
	junction to ambient		[2]	-	280	320	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	115	K/W

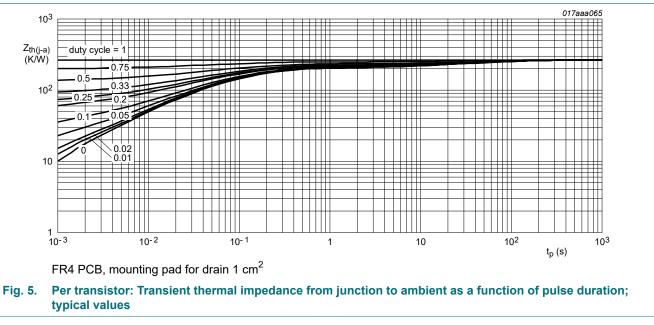
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².



FR4 PCB; standard footprint





2N7002PV

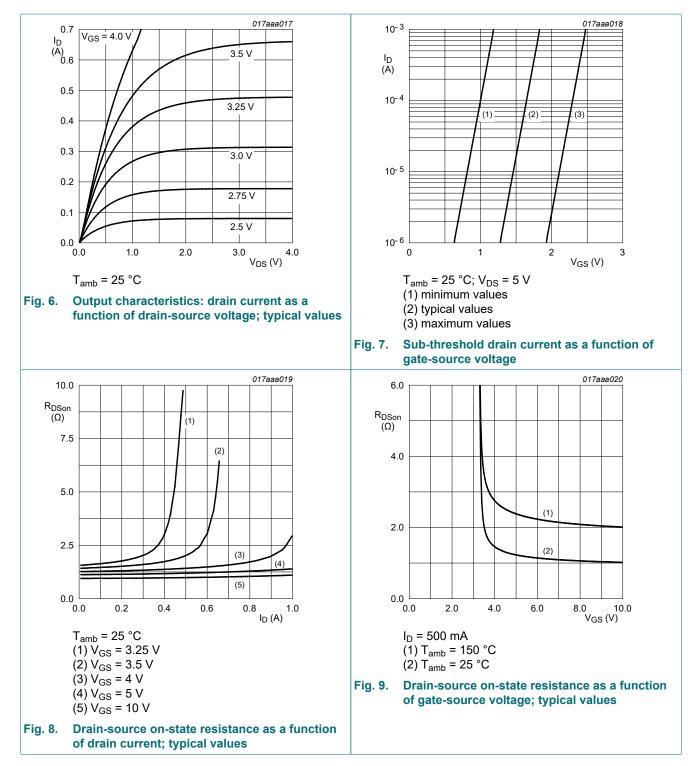
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	cteristics (per transistor)					
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 10 μA; V _{GS} = 0 V; T _j = 25 °C	60	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = 250 μA; V _{DS} =V _{GS} ; T _j = 25 °C	1.1	1.75	2.4	V
I _{DSS}	drain leakage current	$V_{DS} = 60 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	1	μA
		V _{DS} = 60 V; V _{GS} = 0 V; T _j = 150 °C	-	-	10	μA
I _{GSS}	gate leakage current	V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	100	nA
		V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-100	nA
DOON	drain-source on-state resistance	V_{GS} = 5 V; I _D = 50 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.01; T _j = 25 °C	-	1.3	2	Ω
		V_{GS} = 10 V; I _D = 500 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.01; T _j = 25 °C	-	1	1.6	Ω
9 _{fs}	forward transconductance	V_{DS} = 10 V; I _D = 200 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.01; T _j = 25 °C	-	400	-	mS
Dynamic ch	aracteristics (per transist	or)				
Q _{G(tot)}	total gate charge	V _{DS} = 30 V; I _D = 300 mA; V _{GS} = 4.5 V;	-	0.6	0.8	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.2	-	nC
Q _{GD}	gate-drain charge		-	0.2	-	nC
C _{iss}	input capacitance	V _{DS} = 10 V; f = 1 MHz; V _{GS} = 0 V;	-	30	50	pF
C _{oss}	output capacitance	T _j = 25 °C	-	7	-	pF
C _{rss}	reverse transfer capacitance		-	4	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 50 V; R _L = 250 Ω; V _{GS} = 10 V;	-	3	6	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	4	-	ns
t _{d(off)}	turn-off delay time	1	-	10	20	ns
t _f	fall time		-	5	-	ns
Source-drai	n diode (per transistor)					
V _{SD}	source-drain voltage	I _S = 115 mA; V _{GS} = 0 V; T _i = 25 °C	0.47	0.75	1.1	V

2N7002PV

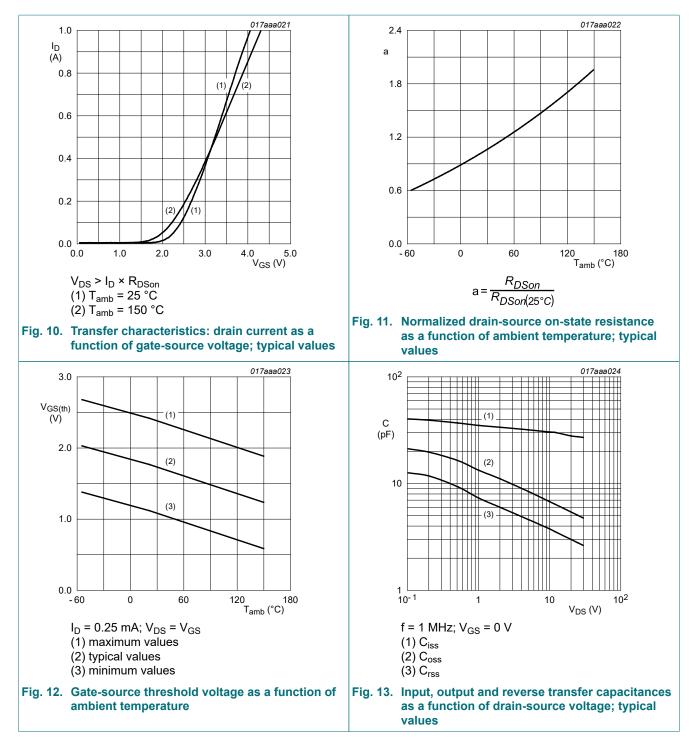
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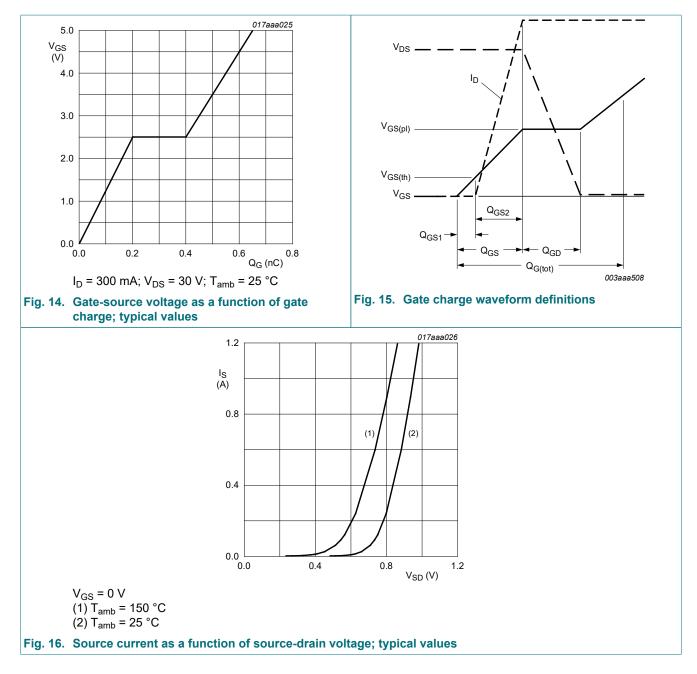
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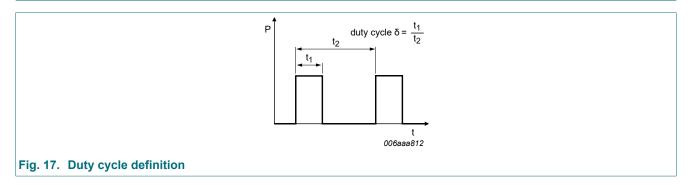


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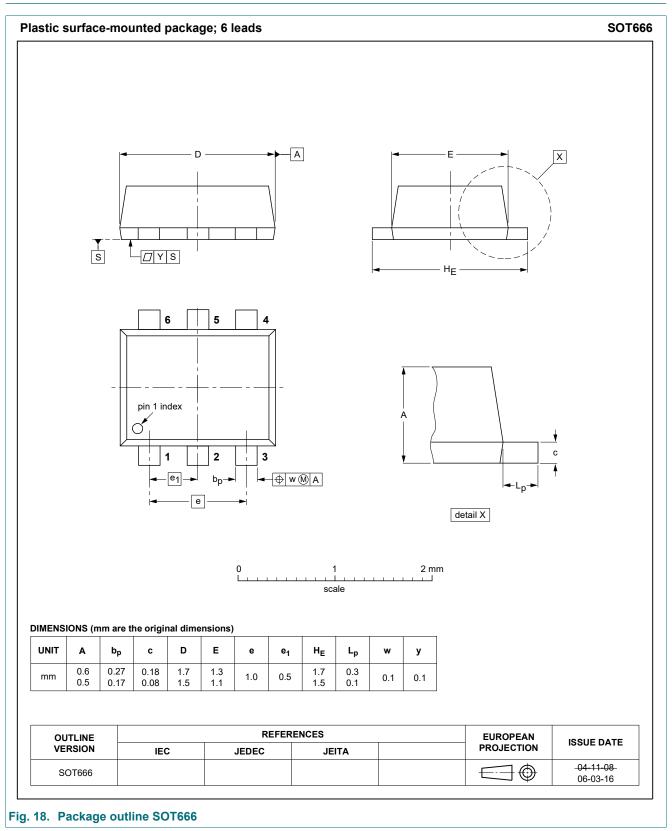


11. Test information

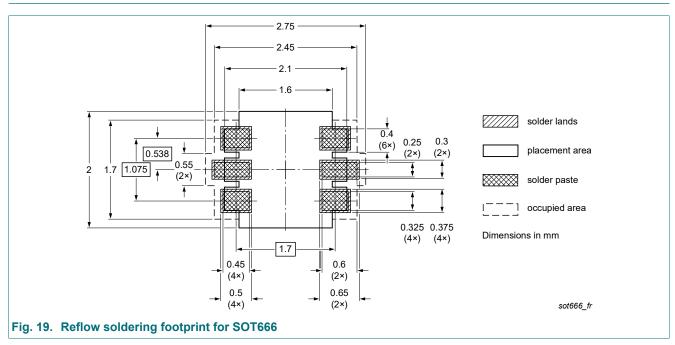


2N7002PV

12. Package outline



13. Soldering



2N7002PV

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14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
2N7002PV v.3	20221228	Product data sheet	-	2N7002PV v.2		
Modifications:	Product change	Product changed to non-automotive qualification				
2N7002PV v.2	20201123	Product data sheet	-	2N7002PV v.1		
2N7002PV v.1	20100805	Product data sheet	-	-		

2N7002PV

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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2N7002PV

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Contents

1. General description	1
2. Features and benefits	1
3. Applications	1
4. Quick reference data	1
5. Pinning information	2
6. Ordering information	2
7. Marking	2
8. Limiting values	
9. Thermal characteristics	5
10. Characteristics	6
11. Test information	9
12. Package outline	
13. Soldering	
14. Revision history	12
15. Legal information	
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2N7002PV