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Vishay Semiconductors

High Speed Infrared Emitting Diodes, 940 nm, **Surface Emitter Technology**





VSMY2940GX01



DESCRIPTION

As part of the SurfLight portfolio, the VSMY2940 series are infrared, 940 nm emitting diodes based on GaAlAs surface emitter chip technology with extreme high radiant intensities, high optical power and high speed, molded in clear, untinted plastic packages (with lens) for surface mounting (SMD).

APPLICATIONS

- · Miniature light barrier
- Photointerrupters
- · Optical switch
- Emitter source for proximity sensors

FEATURES

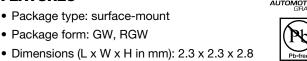
Package type: surface-mount



Peak wavelength: λ_p = 940 nm

- AEC-Q101 qualified
- High radiant power
- · Very high radiant intensity
- Angle of half intensity: $\varphi = \pm 10^{\circ}$
- · Suitable for high pulse current operation
- · Terminal configurations: gullwing or reverse gullwing
- Package matches with detector VEMD2000X01 series
- Floor life: 4 weeks, MSL 2a, according to J-STD-020
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912









PRODUCT SUMMARY					
COMPONENT	I _e (mW/sr)	φ (deg)	λ _P (nm)	t _r (ns)	
VSMY2940RGX01	145	± 10	940	10	
VSMY2940GX01	145	± 10	940	10	

Note

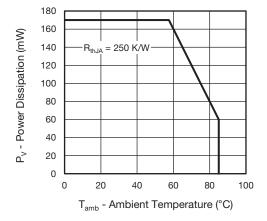
Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VSMY2940RGX01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Reverse gullwing	
VSMY2940GX01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Gullwing	

· MOQ: minimum order quantity



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V_{R}	5	V
Forward current		I _F	100	mA
Peak forward current	$t_p/T = 0.5, t_p = 100 \mu s$	I _{FM}	200	mA
Surge forward current	t _p = 100 μs	I _{FSM}	1	А
Power dissipation		P_V	170	mW
Junction temperature		T _j	100	°C
Operating temperature range		T _{amb}	-40 to +85	°C
Storage temperature range		T _{stg}	-40 to +100	°C
Soldering temperature	According to Fig. 10, J-STD-020	T _{sd}	260	°C
Thermal resistance junction-to-ambient	J-STD-051, soldered on PCB	R _{thJA}	250	K/W



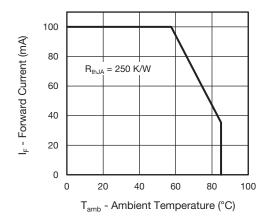


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Famusard valtage	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	V_{F}	-	1.4	1.8	V
Forward voltage	$I_F = 1 \text{ A}, t_p = 100 \ \mu\text{s}$	V _F	-	2.5	-	V
Temperature coefficient of V _F	I _F = 100 mA	TK _{VF}	-	-0.7	-	mV/K
Reverse current		I _R	Not designed for reverse operation μA		μΑ	
Junction capacitance	$V_R = 0 \text{ V}, f = 1 \text{ MHz}, E = 0 \text{ mW/cm}^2$	CJ	-	55	-	pF
Dedicatists with	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	I _e	75	145	215	mW/sr
Radiant intensity	$I_F = 1 \text{ A}, t_p = 100 \mu \text{s}$	I _e	-	1000	-	mW/sr
Radiant power	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	φ _e	-	55	-	mW
Temperature coefficient of radiant power	I _F = 100 mA	TKφ _e	-	-0.2	-	%/K
Angle of half intensity		φ	-	± 10	-	deg
Peak wavelength	I _F = 100 mA	λ_{p}	920	940	960	nm
Spectral bandwidth	I _F = 100 mA	Δλ	-	50	-	nm
Temperature coefficient of λ _p	I _F = 100 mA	$TK\lambda_p$	-	0.25	-	nm/K
Rise time	I _F = 100 mA, 10 % to 90 %	t _r	-	10	-	ns
Fall time	I _F = 100 mA, 10 % to 90 %	t _f	-	10	-	ns

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

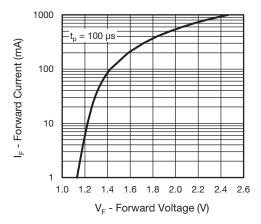


Fig. 3 - Forward Current vs. Forward Voltage

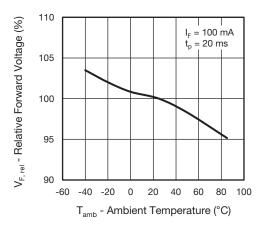


Fig. 4 - Relative Forward Voltage vs. Ambient Temperature

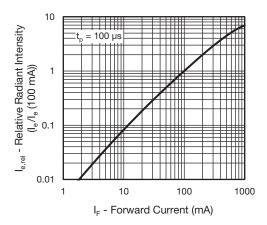


Fig. 5 - Relative Radiant Intensity vs. Forward Current

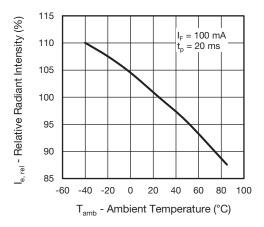


Fig. 6 - Relative Radiant Intensity vs. Ambient Temperature

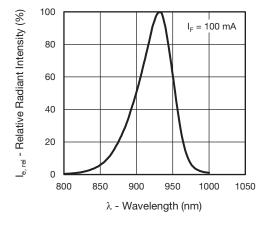


Fig. 7 - Relative Radiant Intensity vs. Wavelength

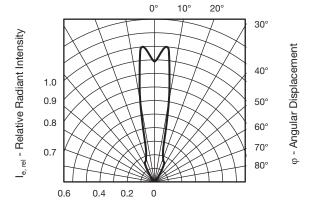


Fig. 8 - Relative Radiant Intensity vs. Angular Displacement

SOLDER PROFILE

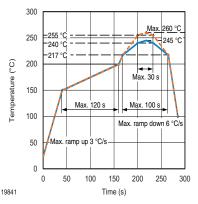


Fig. 9 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 4 weeks

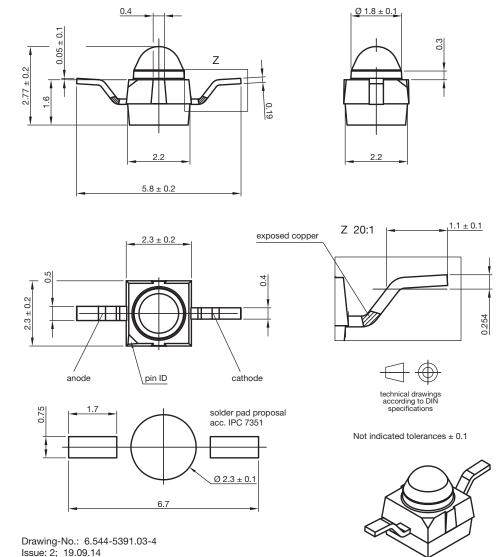
Conditions: T_{amb} < 30 °C, RH < 60 %

Moisture sensitivity level 2a, according to J-STD-020.

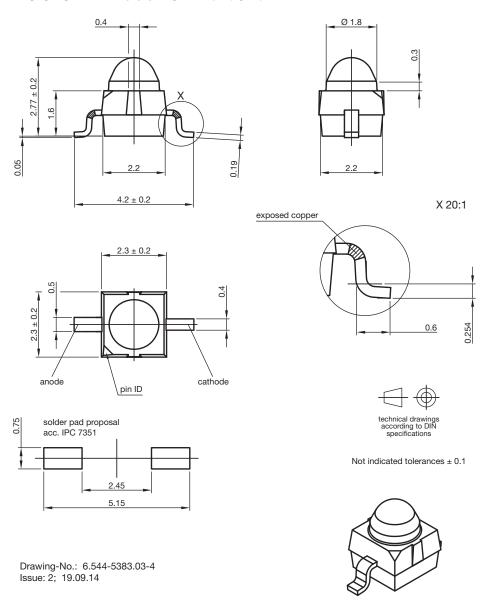
DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 $^{\circ}$ M.

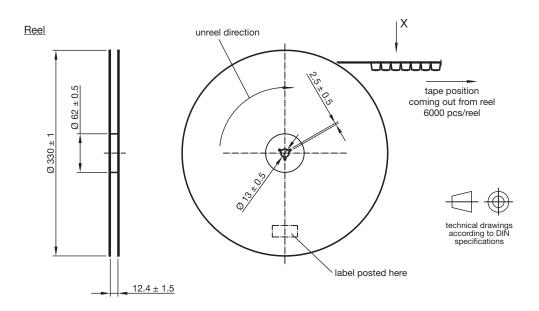
PACKAGE DIMENISONS in millimeters: VSMY2940RGX01



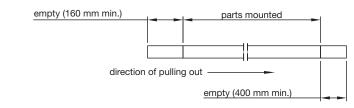
PACKAGE DIMENSIONS in millimeters: VSMY2940GX01



TAPING AND REEL DIMENSIONS in millimeters: VSMY2940RGX01



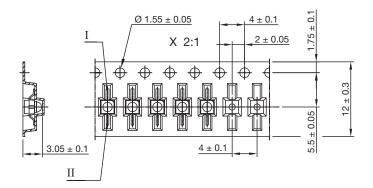
Leader and trailer tape



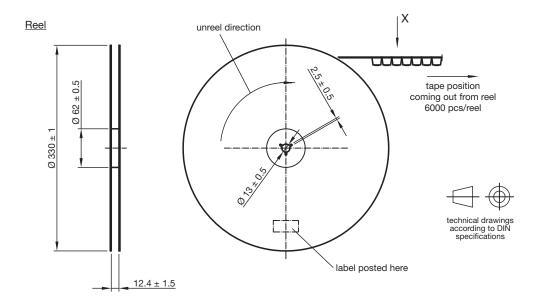
Terminal position in tape

Lead I	Lead II
Callagtar	Fmitter
Collector	Ellillel
Cathode	Anode
Anada	Cathode
Anoue	Callioue
	Collector

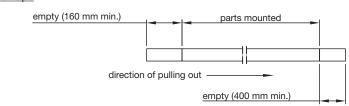
Drawing-No.: 9.800-5100.01-4 Issue: 4; 19.09.14



TAPING AND REEL DIMENSIONS in millimeters: VSMY2940GX01

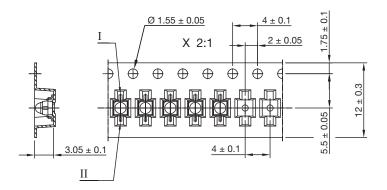


Leader and trailer tape



Terminal position in tape

Device	Lead I	Lead II
VSMB2020		
VSMG2020		
VEMD2020	Cathode	Anode
VEMD2520		
VSMF2890G		
VEMT2020	Collector	Emitter
VEMT2520	Collector	Emitter
VSMY2850G	Anode	Cathode
VSMY2940G	Anoue	Califode



Drawing-No.: 9.800-5091.01-4

Issue: 5; 19.09.14



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