

Low profile high gain silicon NPN RF bipolar transistor



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Product description

The BFP520F is a low noise device based on a grounded emitter (SIEGET[™]) that is part of Infineon's established fifth generation RF bipolar transistor family. Its transition frequency f_T of 45 GHz, high gain and low noise make the device suitable for applications up to 15 GHz. It remains cost competitive without compromising on ease of use.



Feature list

- Minimum noise figure NF_{min} = 0.95 dB at 1.8 GHz, 2 V, 2 mA
- High gain G_{ms} = 22.5 dB at 1.8 GHz, 2 V, 20 mA
- OIP₃ = 23.5 dBm at 1.8 GHz, 2 V, 20 mA

Product validation

Qualified for industrial applications according to the relevant tests of JEDEC47/20/22.

Potential applications

- Radio-frequency oscillators such as local oscillator in LNB
- Broadband low noise amplifiers (LNAs) for CATV, DVB-T, DAB/DMB and FM/AM radio
- LNAs for wireless communications such as cordless phones

Device information

Table 1 **Part information**

Product name / Ordering code	Package	Pin configuration				Marking	Pieces / Reel
BFP520F / BFP520FH6327XTSA1	TSFP-4-1	1 = B	2 = E	3 = C	4 = E	APs	3000

Attention: ESD (Electrostatic discharge) sensitive device, observe handling precautions

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Absolute maximum ratings

Absolute maximum ratings 1

Table 2 Absolute maximum ratings at $T_A = 25$ °C (unless otherwise specified)

Parameter	Symbol	Values		Unit	Note or test condition
	Min.	Max.			
Collector emitter voltage	V _{CEO}	_	2.5	٧	Open base
			2.4		T _A = -55 °C, open base
Collector emitter voltage	V _{CES}		10		E-B short circuited
Collector base voltage	V_{CBO}		10		Open emitter
Emitter base voltage	V_{EBO}		1		Open collector
Base current	I _B		5	mA	_
Collector current	Ic		50		
Total power dissipation 1)	P _{tot}		120	mW	<i>T</i> _S ≤ 98 °C
Junction temperature	TJ		150	°C	-
Storage temperature	$T_{ m Stg}$	-55			

Attention: Stresses above the max. values listed here may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect device reliability. Exceeding only one of these values may cause irreversible damage to the integrated circuit.

 T_S is the soldering point temperature. T_S is measured on the emitter lead at the soldering point of the PCB.



Thermal characteristics

2 Thermal characteristics

Table 3 Thermal resistance

Parameter	Symbol		Values			Note or test condition
		Min.	Тур.	Max.		
Junction - soldering point	R _{thJS}	_	430	_	K/W	-

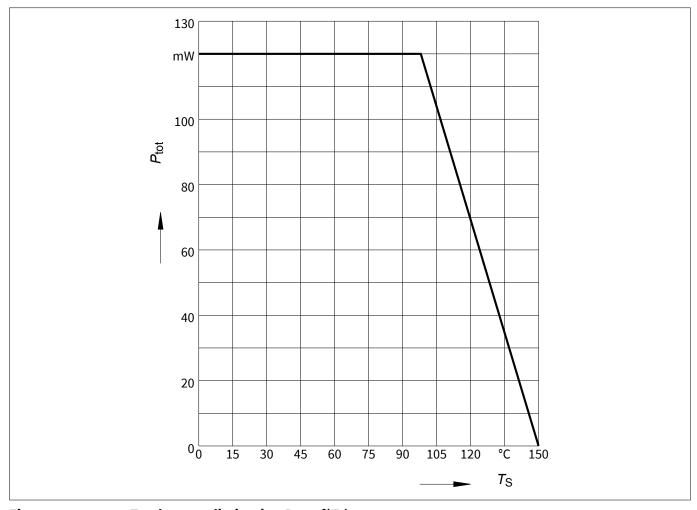


Figure 1 Total power dissipation $P_{\text{tot}} = f(T_S)$



Electrical characteristics

3 Electrical characteristics

3.1 DC characteristics

Table 4 DC characteristics at $T_A = 25 \,^{\circ}\text{C}$

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Тур.	Мах.		
Collector emitter breakdown voltage	V _{(BR)CEO}	2.5	3	3.5	V	$I_{C} = 1 \text{ mA}, I_{B} = 0,$ open base
Collector emitter leakage current	I _{CES}	_	-	10 ²⁾	μΑ	$V_{CE} = 10 \text{ V}, V_{BE} = 0,$ E-B short circuited
Collector base leakage current	I _{CBO}			200 ²⁾	nA	$V_{\text{CB}} = 5 \text{ V}, I_{\text{E}} = 0,$ open emitter
Emitter base leakage current	I _{EBO}			35 ²⁾	μΑ	$V_{\text{EB}} = 1 \text{ V}, I_{\text{C}} = 0,$ open collector
DC current gain	h _{FE}	70	110	170		$V_{CE} = 2 \text{ V}, I_{C} = 20 \text{ mA},$ pulse measured

3.2 General AC characteristics

Table 5 General AC characteristics at $T_A = 25$ °C

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Тур.	Max.		
Transition frequency	f_{T}	32	45	-	GHz	$V_{CE} = 2 \text{ V}, I_{C} = 30 \text{ mA},$ f = 2 GHz
Collector base capacitance	C _{CB}	_	0.07	0.14	pF	$V_{\text{CB}} = 2 \text{ V}, V_{\text{BE}} = 0,$ f = 1 MHz, emitter grounded
Collector emitter capacitance	C _{CE}		0.25	-		$V_{CE} = 2 \text{ V}, V_{BE} = 0,$ f = 1 MHz, base grounded
Emitter base capacitance	C _{EB}		0.31			$V_{EB} = 0.5 \text{ V}, V_{CB} = 0,$ f = 1 MHz, collector grounded

² Maximum values not limited by the device but by the short cycle time of the 100% test.



Electrical characteristics

Frequency dependent AC characteristics 3.3

Measurement setup is a test fixture with Bias-T's in a 50 Ω system, T_A = 25 °C.

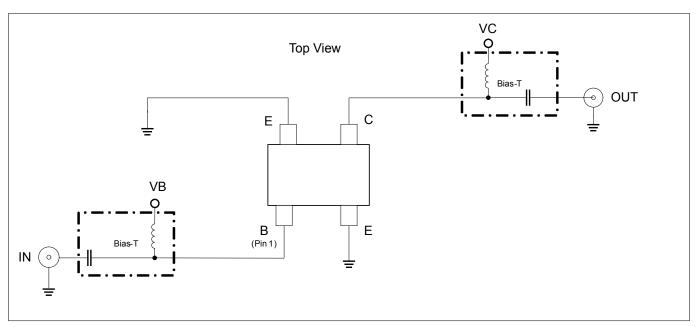


Figure 2 **Testing circuit**

Table 6 AC characteristics, $V_{CE} = 2 \text{ V}$, f = 1.8 GHz

Parameter	Symbol		Values		Unit	Note or test condition	
		Min.	Тур.	Max.			
Power gain		_		_	dB		
Maximum power gain	G _{ms}		22.5			$I_{\rm C}$ = 20 mA	
• Transducer gain	$ S_{21} ^2$		20.5				
Noise figure							
Minimum noise figure	<i>NF</i> _{min}		0.95			$I_{\rm C}$ = 2 mA	
Linearity					dBm	$Z_{\rm S} = Z_{\rm S.opt}$, $Z_{\rm L} = Z_{\rm L.opt}$,	
3rd order intercept point at output	OIP ₃		23.5			$Z_{\rm S} = Z_{\rm S,opt}, Z_{\rm L} = Z_{\rm L,opt},$ $I_{\rm C} = 20 \text{ mA}$	
• 1 dB gain compression point at output	OP _{1dB}		10.5				

Note:

 $G_{\rm ms}$ = $IS_{21}/S_{12}I$ for k < 1; $G_{\rm ma}$ = $IS_{21}/S_{12}I$ (k-(k^2 -1) $^{1/2}$) for k > 1. In order to get the NF_{min} values stated in this chapter, the test fixture losses have been subtracted from all measured results. OIP₃ value depends on termination of all intermodulation frequency components. Termination used for this measurement is 50 Ω from 0.1 MHz to 6 GHz.



Package information TSFP-4-1

4 Package information TSFP-4-1

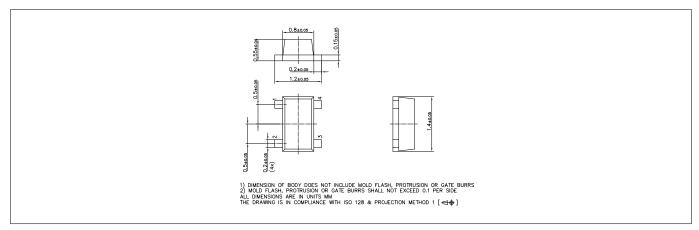


Figure 3 Package outline

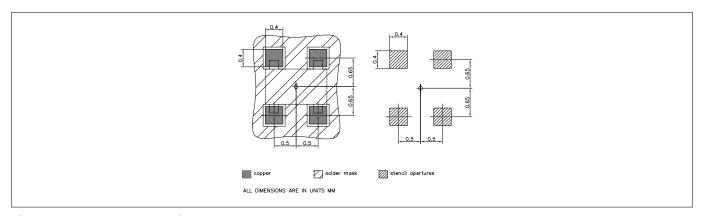


Figure 4 Foot print

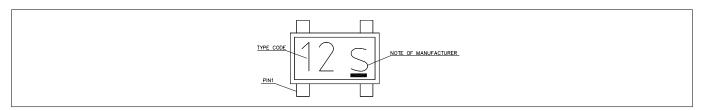


Figure 5 Marking layout example

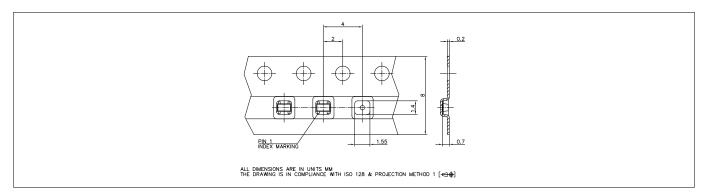


Figure 6 Tape dimensions

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Revision history

Revision history

Document version	Date of release	Description of changes
Revision 2.0	2019-01-25	New datasheet layout.

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