



# dsPIC30F6010A

## dsPIC30F6010A 80-pin to 80-pin TQFP Plug-In Module (PIM) Information Sheet

### OVERVIEW

The dsPIC30F6010A PIM is designed to demonstrate the capabilities of the dsPIC30F6010A family of devices using an 80-pin PIM interface.

The dsPIC30F6010A is a high performance 16-bit Digital Signal Controller in an 80-pin TQFP package.

Table 1 shows the mapping between the 80-pin PIM interface board functions and the device pins.

**TABLE 1: 80-PIN TO 80-PIN PIM**

Device Pin #	dsPIC30F6010A Functional Description	PIM Pin #
1	PWM3H/RE5	12
2	PWM4L/RE6	13
3	PWM4H/RE7	14
4	T2CK/RC1	15
5	T4CK/RC3	16
6	SCK2/CN8/RG6	17
7	SDI2/CN9/RG7	18
8	SDO2/CN10/RG8	19
9	MCLR	20
10	SS2/CN11/RG9	21
11	Vss	23
12	VDD	24
13	FLTA/INT1/RE8	25
14	FLTB/INT2/RE9	26
15	AN5/QEB/CN7/RB5	27
16	AN4/QEA/CN6/RB4	28
17	AN3/INDX/CN5/RB3	29
18	AN2/SS1/CN4/RB2	30
19	PGC/EMUC/AN1/CN3/RB1	31
20	PGD/EMUD/AN0/CN2/RB0	32
21	AN6/OCFA/RB6	33
22	AN7/RB7	34
23	VREF-/RA9	35
24	VREF+/RA10	36
25	AVDD	37
26	AVSS	38
27	AN8/RB8	39
28	AN9/RB9	4
29	AN10/RB10	41
30	AN11/RB11	42
31	Vss	44
32	VDD	45
33	AN12/RB12	46
34	AN13/RB13	47

Device Pin #	dsPIC30F6010A Functional Description	PIM Pin #
35	AN14/RB14	48
36	AN15/OCFB/CN12/RB15	49
37	IC7/CN20/RD14	50
38	IC8/CN21/RD15	51
39	U2RX/CN17/RF4	52
40	U2TX/CN18/RF5	53
41	U1TX/RF3	54
42	U1RX/RF2	55
43	EMUD3/SDO1/RF8	56
44	SDI1/RF7	57
45	EMUC3/SCK1/INT0/RF6	58
46	SDA/RG3	59
47	SCL/RG2	60
48	VDD	61
49	OSC1/CLKI	62
50	OSC2/CLKO/RC15	63
51	Vss	65
52	INT3/RA14	66
53	INT4/RA15	67
54	IC1/RD8	68
55	IC2/RD9	69
56	IC3/RD10	70
57	IC4/RD11	71
58	EMUC2/OC1/RD0	72
59	EMUD1/SOSCI/CN1/RC13	73
60	EMUC1/SOSCO/T1CK/CN0/RC14	74
61	EMUD2/OC2/RD1	75
62	OC3/RD2	76
63	OC4/RD3	77
64	IC5/RD12	78
65	IC6/CN19/RD13	79
66	OC5/CN13/RD4	80
67	OC6/CN14/RD5	81
68	OC7/CN15/RD6	82

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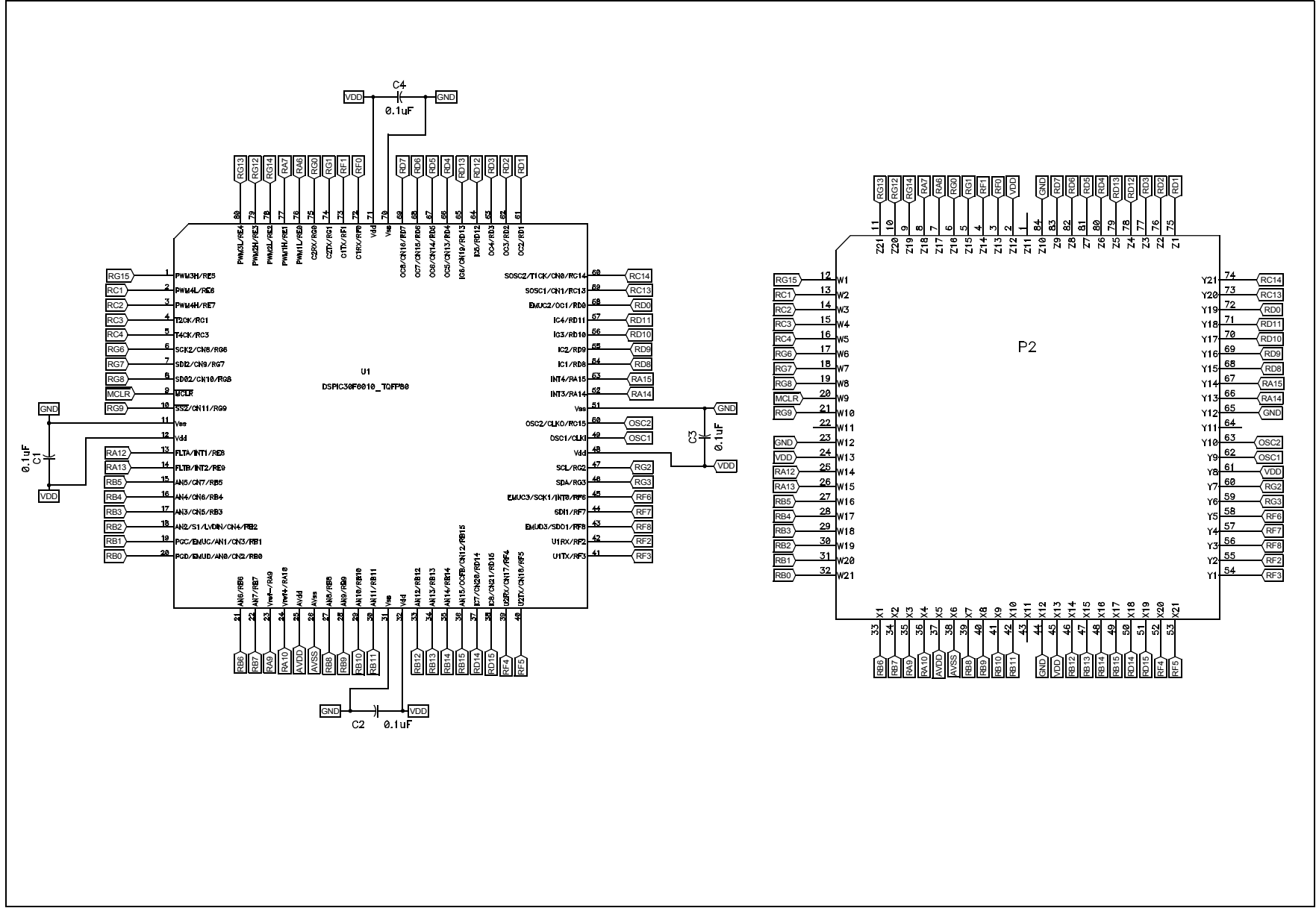
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**TABLE 1: 80-PIN TO 80-PIN PIM (CONTINUED)**

Device Pin #	dsPIC30F6010A Functional Description	PIM Pin #
69	OC8/UPDN/CN16/RD7	83
70	Vss	84
71	VDD	2
72	C1RX/RF0	3
73	C1TX/RF1	4
74	C2TX/RG1	5

Device Pin #	dsPIC30F6010A Functional Description	PIM Pin #
75	C2RX/RG0	6
76	PWM1L/RE0	7
77	PWM1H/RE1	8
78	PWM2L/RE2	9
79	PWM2H/RE3	10
80	PWM3L/RE4	11

FIGURE 1: 80-PIN TO 80-PIN PIM SCHEMATIC



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NOTES:

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