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A Product Line of **Diodes Incorporated** 

Case Material: Molded Plastic, "Green" Molding Compound.

Terminals: Finish - Matte Tin annealed over Copper leadframe.

UL Flammability Classification Rating 94V-0

Moisture Sensitivity: Level 1 per J-STD-020

Solderable per MIL-STD-202, Method 208 @3

Weight: 0.093 grams (approximate)



DXTP19020DP5

#### **20V PNP HIGH GAIN TRANSISTOR** PowerDl<sup>®</sup>5

### Features

- 43% smaller than SOT223; 60% smaller than TO252
- Maximum height just 1.1mm
- Rated up to 1.3W
- $V_{CEO} = -20V$
- I<sub>C</sub> = -8A; I<sub>CM</sub> = -15A
- Low Saturation voltage, high gain transistor
- Lead, Halogen and Antimony Free, RoHS Compliant (Note 1)
- "Green" Device (Note 2)

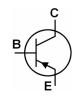
### **Applications**

- Load disconnect switch
- Battery charging





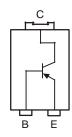
Bottom View



**Device Schematic** 

**Mechanical Data** 

Case: PowerDl<sup>®</sup>5



Pin-out diagram

### Ordering Information (Note 3)

Part Number	Case	Packaging
DXTP19020DP5-13	PowerDI <sup>®</sup> 5	5000/Tape & Reel

1. No purposefully added lead. Halogen and Antimony Free.

Top View

2. Diodes Inc's "Green" Policy can be found on our website at http://www.diodes.com

3. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

# Marking Information

Notes:



DTP1920D = Product Type Marking Code DII = Manufacturers' Code Marking K = Factory Designator YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 09 for 2009) WW = Week code (01 to 53)

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# Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-25	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-20	V
Emitter-Collector Voltage (Reverse Blocking)	V <sub>ECO</sub>	-4	V
Emitter-Base Voltage	V <sub>EBO</sub>	-7	V
Continuous Collector Current	IC	-8	A
Base Current	Ι <sub>Β</sub>	-1	A
Peak Pulse Current	I <sub>CM</sub>	-15	А

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation @ $T_A = 25^{\circ}C$ (Note 4)	PD	1.3	W
Thermal Resistance, Junction to Ambient Air (Note 4) @T <sub>A</sub> = 25°C	$R_{ ext{ heta}JA}$	96.1	°C/W
Power Dissipation @ $T_A = 25^{\circ}C$ (Note 5)	PD	3	W
Thermal Resistance, Junction to Ambient Air (Note 5) $@T_A = 25^{\circ}C$	$R_{ ext{ heta}JA}$	41.7	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

 Notes:
 4. Device mounted on FR-4 PCB, 2 oz. copper, minimum recommended pad layout.

 5. Device mounted on FR-4 PCB, 2 oz. copper, collector pad dimensions 0.42inch<sup>2</sup>.

### **Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	V <sub>(BR)</sub> CBO	-25	-55		V	I <sub>C</sub> = -100μA
Collector-Emitter Breakdown Voltage (Note 6)	V <sub>(BR)CEO</sub>	-20	-50		V	I <sub>C</sub> = -10mA
Emitter-Collector Breakdown Voltage (Reverse Blocking)	V <sub>(BR)ECX</sub>	-4	-8.6	—	V	$I_E = -100 \mu A$ , $R_{BC} < 1 k \Omega$ or
	V (BR)ECX					$0.25V > V_{CB} > -0.25V$
Emitter-Base Breakdown Voltage (Reverse Blocking)	V <sub>(BR)ECO</sub>	-4	-8.6	_	V	I <sub>E</sub> = -100μA
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	-7	-8.2		V	$I_E = -100 \mu A$
Collector Cutoff Current	1		<1	50	nA	$V_{CB} = -25V$
	I <sub>CBO</sub>			0.5	μA	V <sub>CB</sub> = -25V, T <sub>amb</sub> = 100 °C
Emitter Cutoff Current	I <sub>EBO</sub>	_	<1	-50	nA	V <sub>EB</sub> = -5.6V
			-40	-47		I <sub>C</sub> = -1A, I <sub>B</sub> = -100mA
Collector Emitter Seturation Voltage (Note 6)	N/	_	-97	-130	mV	$I_{C} = -1A, I_{B} = -10mA$
Collector-Emitter Saturation Voltage (Note 6)	V <sub>CE(sat)</sub>		-115	-145	mv	$I_{\rm C} = -2A, I_{\rm B} = -40 {\rm mA}$
			-220	-275		I <sub>C</sub> = -8A, I <sub>B</sub> = -800mA
Base-Emitter Saturation Voltage (Note 6)	V <sub>BE(sat)</sub>	_	-1050	-1150	mV	I <sub>C</sub> = -8A, I <sub>B</sub> = -800mA
Base-Emitter Turn-On Voltage (Note 6)	V <sub>BE(on)</sub>		-930	-1000	mV	I <sub>C</sub> = -8A, V <sub>CE</sub> = -2V
		300	450	900	_	I <sub>C</sub> = -100mA, V <sub>CE</sub> = -2V
DC Current Coin (Note C)	<b>L</b>	200 45	290	_		$I_{C} = -2A, V_{CE} = -2V$
DC Current Gain (Note 6)	h <sub>FE</sub>		70			$I_{C} = -8A, V_{CE} = -2V$
		—	25			I <sub>C</sub> = -15A, V <sub>CE</sub> = -2V
	4	_	176	_	MHz	$I_{C} = -50 \text{mA}, V_{CE} = -10 \text{V},$
Transition Frequency	f <sub>T</sub>		170			f = 50MHz
Input Capacitance (Note 6)	Cibo			400	pF	$V_{EB} = -0.5V, f = 1MHz$
Output Capacitance (Note 6)	Cobo	_	36	45	pF	V <sub>CB</sub> = -10V, f = 1MHz
Delay Time	t <sub>d</sub>	_	23	_		
Rise Time	tr		18.4			$I_{C} = -1A, V_{CC} = -10V,$
Storage Time	ts		266		ns	$I_{B1} = -I_{B2} = -50 \text{mA}$
Fall Time	t <sub>f</sub>	_	49.6	_	1	

Notes: 6. Pulse Test: Pulse width  $\leq$ 300 $\mu$ s. Duty cycle  $\leq$ 2.0%.

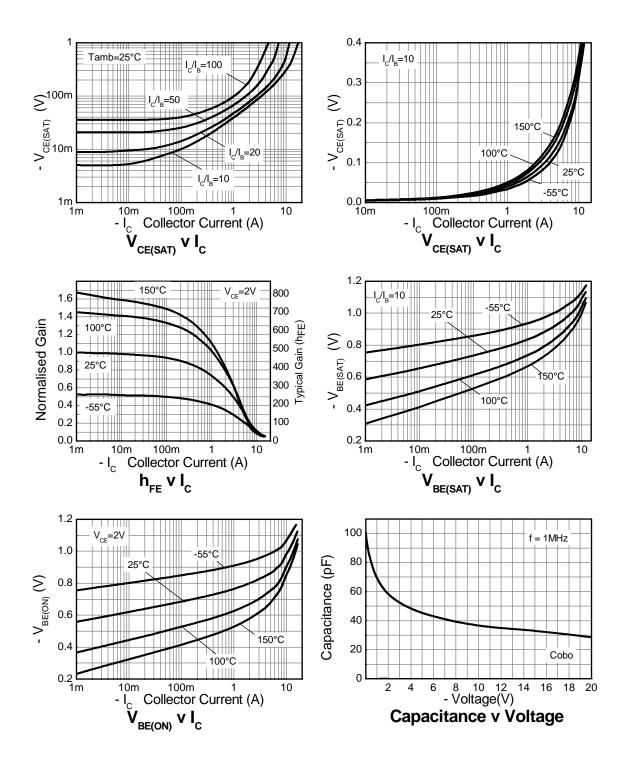
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# **Typical Characteristic**

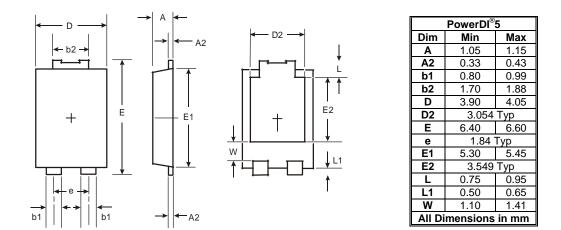


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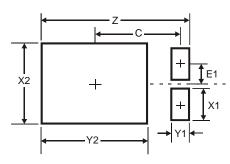




# **Package Outline Dimensions**



# Suggested Pad Layout



Dimensions	Value (in mm)
Z	6.6
X1	1.4
X2	3.6
Y1	0.8
Y2	4.7
С	3.87
E1	0.9

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