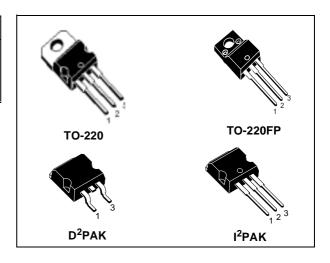


STP9NK50Z - STP9NK50ZFP STB9NK50Z - STB9NK50Z-1

N-CHANNEL 500V - 0.72Ω - 7.2A TO-220/FP/D²PAK/I²PAK Zener-Protected SuperMESH™ MOSFET

TYPE	V _{DSS}	R _{DS(on)}	I _D	Pw
STP9NK50Z	500 V	< 0.85 Ω	7.2 A	110 W
STP9NK50ZFP	500 V	< 0.85 Ω	7.2 A	30 W
STB9NK50Z	500 V	< 0.85 Ω	7.2 A	110 W
STB9NK50Z-1	500 V	< 0.85 Ω	7.2 A	110 W

- TYPICAL $R_{DS}(on) = 0.72 \Omega$
- EXTREMELY HIGH dv/dt CAPABILITY
- 100% AVALANCHE TESTED
- GATE CHARGE MINIMIZED
- VERY LOW INTRINSIC CAPACITANCES
- VERY GOOD MANUFACTURING REPEATIBILITY

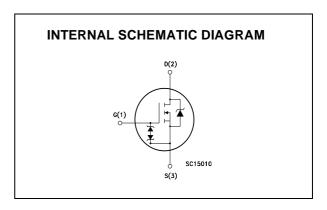


DESCRIPTION

The SuperMESH™ series is obtained through an extreme optimization of ST's well established strip-based PowerMESH™ layout. In addition to pushing on-resistance significantly down, special care is taken to ensure a very good dv/dt capability for the most demanding applications. Such series complements ST full range of high voltage MOSFETs including revolutionary MDmesh™ products.

APPLICATIONS

- HIGH CURRENT, HIGH SPEED SWITCHING
- IDEAL FOR OFF-LINE POWER SUPPLIES, ADAPTORS AND PFC
- LIGHTING



ORDERING INFORMATION

SALES TYPE	MARKING	PACKAGE	PACKAGING
STP9NK50Z	P9NK50Z	TO-220	TUBE
STP9NK50ZFP	P9NK50ZFP	TO-220FP	TUBE
STB9NK50ZT4	B9NK50Z	D ² PAK	TAPE & REEL
STB9NK50Z-1	B9NK50Z	I ² PAK	TUBE

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STP9NK50Z - STP9NK50ZFP - STB9NK50Z - STB9NK50Z-1

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Valu	ie	Unit
		STP9NK50Z STB9NK50Z STB9NK50Z-1	STP9NK50ZFP	
V _{DS}	Drain-source Voltage (V _{GS} = 0)	500)	V
V _{DGR}	Drain-gate Voltage ($R_{GS} = 20 \text{ k}\Omega$)	500)	V
V _{GS}	Gate- source Voltage	± 30	0	V
I _D	Drain Current (continuous) at T _C = 25°C	7.2	7.2 (*)	Α
ID	Drain Current (continuous) at T _C = 100°C	4.5	4.5 (*)	А
I _{DM} (•)	Drain Current (pulsed)	28.8	28.8 (*)	Α
P _{TOT}	Total Dissipation at T _C = 25°C	110	30	W
	Derating Factor	0.88	0.24	W/°C
V _{ESD(G-S)}	Gate source ESD(HBM-C=100pF, R=1.5KΩ)	350	0	V
dv/dt (1)	Peak Diode Recovery voltage slope	4.5	i	V/ns
V _{ISO}	Insulation Withstand Voltage (DC)	-	2500	V
T _j T _{stg}	Operating Junction Temperature Storage Temperature		-55 to 150 -55 to 150	

THERMAL DATA

		TO-220 / D ² PAK / TO-220FP		
Rthj-case	Thermal Resistance Junction-case Max	1.14	4.2	°C/W
Rthj-amb	Thermal Resistance Junction-ambient Max	62.5		°C/W
T _I	Maximum Lead Temperature For Soldering Purpose	300	°C	

AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I _{AR}	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T_j max)	7.2	Α
E _{AS}	Single Pulse Avalanche Energy (starting $T_j = 25$ °C, $I_D = I_{AR}$, $V_{DD} = 50$ V)	190	mJ

GATE-SOURCE ZENER DIODE

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
BV _{GSO}	Gate-Source Breakdown Voltage	Igs=± 1mA (Open Drain)	30			V

PROTECTION FEATURES OF GATE-TO-SOURCE ZENER DIODES

The built-in back-to-back Zener diodes have specifically been designed to enhance not only the device's ESD capability, but also to make them safely absorb possible voltage transients that may occasionally be applied from gate to souce. In this respect the Zener voltage is appropriate to achieve an efficient and cost-effective intervention to protect the device's integrity. These integrated Zener diodes thus avoid the usage of external components.

^(•) Pulse width limited by safe operating area (1) I_{SD} ≤7.2A, di/dt ≤200A/µs, V_{DD} ≤ V_{(BR)DSS}, T_j ≤ T_{JMAX}. (*) Limited only by maximum temperature allowed

ELECTRICAL CHARACTERISTICS (TCASE =25°C UNLESS OTHERWISE SPECIFIED) ON/OFF

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source Breakdown Voltage	I _D = 1 mA, V _{GS} = 0	500			V
I _{DSS}	Zero Gate Voltage Drain Current (V _{GS} = 0)	V _{DS} = Max Rating V _{DS} = Max Rating, T _C = 125 °C			1 50	μA μA
I _{GSS}	Gate-body Leakage Current (V _{DS} = 0)	V _{GS} = ± 20V			±10	μA
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 100\mu A$	3	3.75	4.5	V
R _{DS(on)}	Static Drain-source On Resistance	V _{GS} = 10V, I _D = 3.6 A		0.72	0.85	Ω

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
g _{fs} (1)	Forward Transconductance	V _{DS} = 15 V, I _D = 3.6 A		5.3		S
C _{iss} C _{oss} C _{rss}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	V _{DS} = 25V, f = 1 MHz, V _{GS} = 0		910 125 30		pF pF pF
Coss eq. (3)	Equivalent Output Capacitance	$V_{GS} = 0V, V_{DS} = 0V \text{ to } 400V$		75		pF

SWITCHING ON

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
t _{d(on)} t _r	Turn-on Delay Time Rise Time	V_{DD} = 250 V, I_D = 3.6 A R_G = 4.7 Ω V _{GS} = 10 V (Resistive Load see, Figure 3)		17 20		ns ns
$egin{array}{c} Q_{g} \ Q_{gs} \ Q_{gd} \end{array}$	Total Gate Charge Gate-Source Charge Gate-Drain Charge	$V_{DD} = 400V, I_D = 7.2 A,$ $V_{GS} = 10V$		32 6 18		nC nC nC

SWITCHING OFF

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
t _{d(off)} t _f	Turn-off Delay Time Fall Time	V_{DD} = 250 V, I_D = 3.6 A R_G = 4.7 Ω V _{GS} = 10 V (Resistive Load see, Figure 3)		45 22		ns ns
t _{r(Voff)} t _f t _c	Off-voltage Rise Time Fall Time Cross-over Time	$V_{DD} = 400 \text{V}, I_D = 7.2 \text{ A},$ $R_G = 4.7 \Omega, V_{GS} = 10 \text{V}$ (Inductive Load see, Figure 5)		15 13 30		ns ns ns

SOURCE DRAIN DIODE

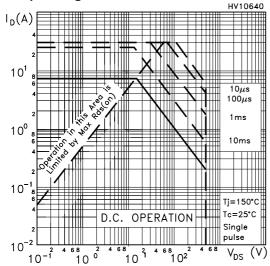
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I _{SD} I _{SDM} (2)	Source-drain Current Source-drain Current (pulsed)				7.2 28.8	A A
V _{SD} (1)	Forward On Voltage	I _{SD} = 7.2 A, V _{GS} = 0			1.6	V
t _{rr} Q _{rr} I _{RRM}	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current	I_{SD} = 7.2 A, di/dt = 100A/ μ s V_{DD} = 40 V, T_j = 150°C (see test circuit, Figure 5)		238 1.5 12.6		ns µC A

Note: 1. Pulsed: Pulse duration = $300 \mu s$, duty cycle 1.5 %.

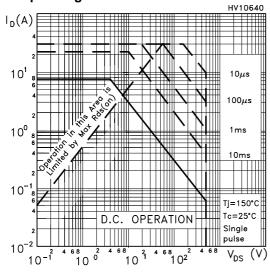
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r uise uuration = 300 µs, duty cycle 1.5 %.
 Pulse width limited by safe operating area.
 C_{oss eq.} is defined as a constant equivalent capacitance giving the same charging time as C_{oss} when V_{DS} increases from 0 to 80% V_{DSs}.

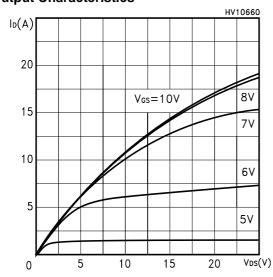
Safe Operating Area For TO-220/D2PAK/I2PAK



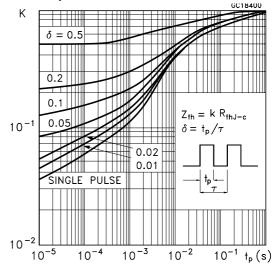
Safe Operating Area For TO-220FP



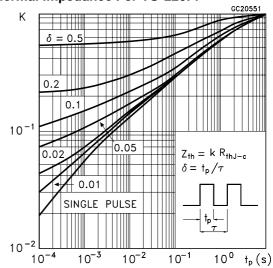
Output Characteristics



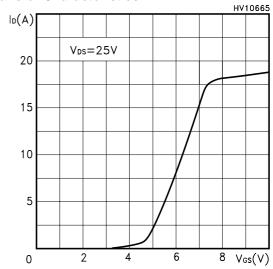
Thermal Impedance For TO-220/D2PAK/I2PAK



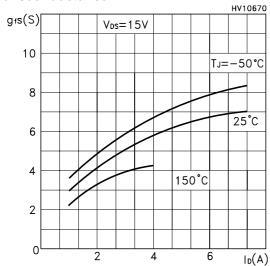
Thermal Impedance For TO-220FP



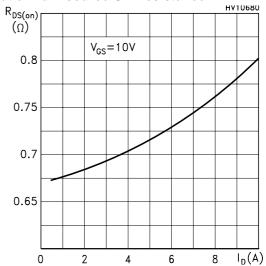
Transfer Characteristics



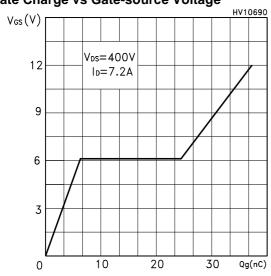
Transconductance



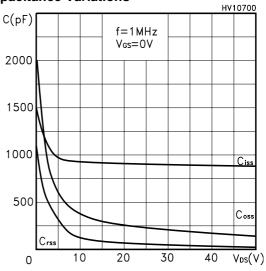
Static Drain-source On Resistance



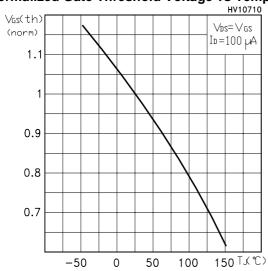
Gate Charge vs Gate-source Voltage



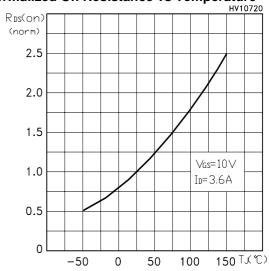
Capacitance Variations



Normalized Gate Threshold Voltage vs Temp.



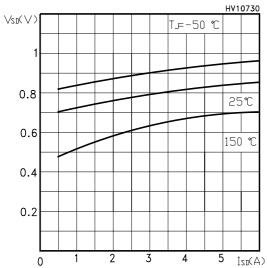
Normalized On Resistance vs Temperature

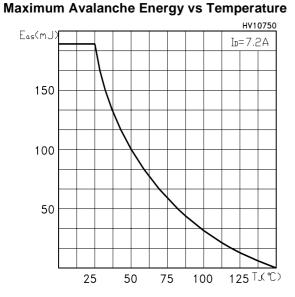


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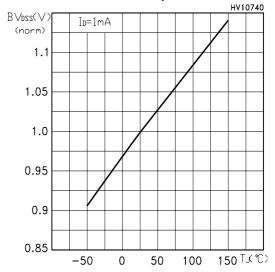
STP9NK50Z - STP9NK50ZFP - STB9NK50Z - STB9NK50Z-1

Source-drain Diode Forward Characteristics





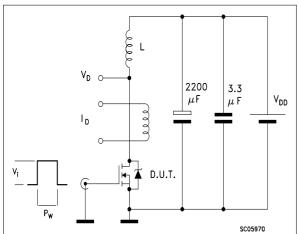
Normalized BVDSS vs Temperature



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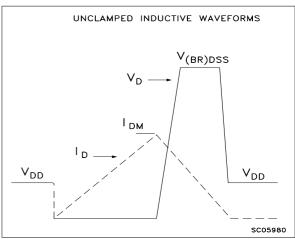
Fig. 2: Unclamped Inductive Waveform

Fig. 1: Unclamped Inductive Load Test Circuit



300370

SC05990



R_L 2200 3.3 μF V_{DD} D.U.T.

Fig. 3: Switching Times Test Circuit For

Resistive Load

Fig. 5: Test Circuit For Inductive Load Switching And Diode Recovery Times

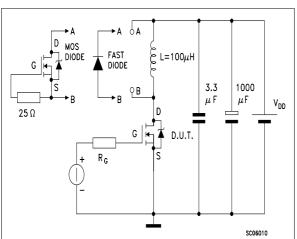
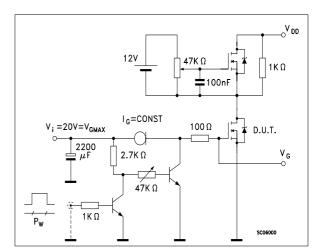
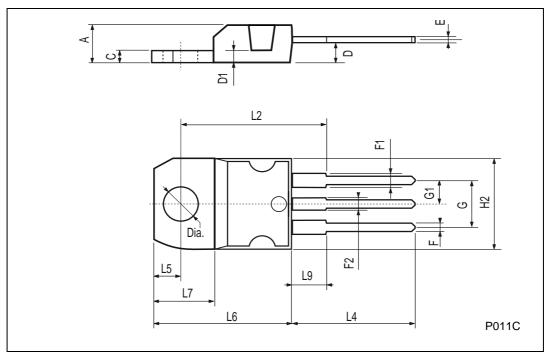


Fig. 4: Gate Charge test Circuit



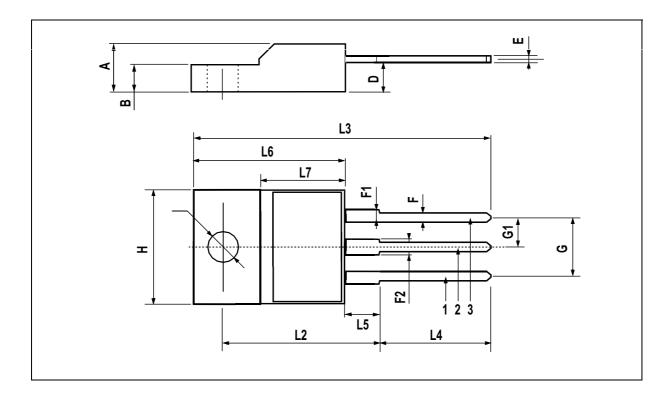
TO-220 MECHANICAL DATA

DIM.		mm			inch	
DIN.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	4.40		4.60	0.173		0.181
С	1.23		1.32	0.048		0.051
D	2.40		2.72	0.094		0.107
D1		1.27			0.050	
E	0.49		0.70	0.019		0.027
F	0.61		0.88	0.024		0.034
F1	1.14		1.70	0.044		0.067
F2	1.14		1.70	0.044		0.067
G	4.95		5.15	0.194		0.203
G1	2.4		2.7	0.094		0.106
H2	10.0		10.40	0.393		0.409
L2		16.4			0.645	
L4	13.0		14.0	0.511		0.551
L5	2.65		2.95	0.104		0.116
L6	15.25	_	15.75	0.600		0.620
L7	6.2		6.6	0.244		0.260
L9	3.5		3.93	0.137		0.154
DIA.	3.75		3.85	0.147		0.151



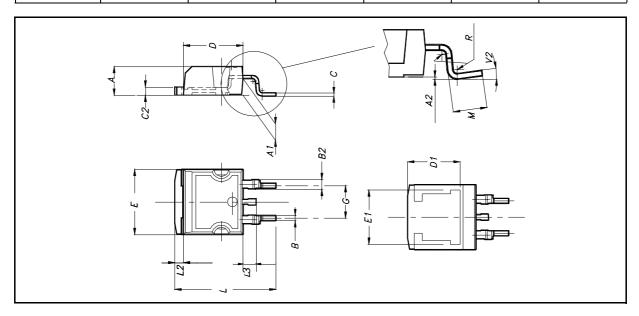
TO-220FP MECHANICAL DATA

DIM	mm.			inch		
DIM.	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
А	4.4		4.6	0.173		0.181
В	2.5		2.7	0.098		0.106
D	2.5		2.75	0.098		0.108
Е	0.45		0.7	0.017		0.027
F	0.75		1	0.030		0.039
F1	1.15		1.5	0.045		0.067
F2	1.15		1.5	0.045		0.067
G	4.95		5.2	0.195		0.204
G1	2.4		2.7	0.094		0.106
Н	10		10.4	0.393		0.409
L2		16			0.630	
L3	28.6		30.6	1.126		1.204
L4	9.8		10.6	.0385		0.417
L5	2.9		3.6	0.114		0.141
L6	15.9		16.4	0.626		0.645
L7	9		9.3	0.354		0.366
Ø	3		3.2	0.118		0.126



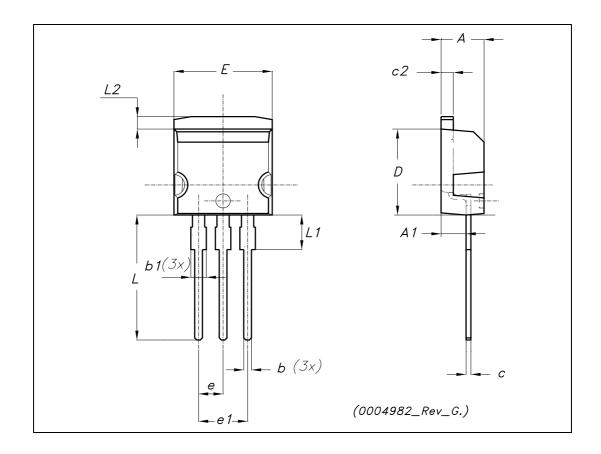
D²PAK MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
А	4.4		4.6	0.173		0.181
A1	2.49		2.69	0.098		0.106
A2	0.03		0.23	0.001		0.009
В	0.7		0.93	0.027		0.036
B2	1.14		1.7	0.044		0.067
С	0.45		0.6	0.017		0.023
C2	1.23		1.36	0.048		0.053
D	8.95		9.35	0.352		0.368
D1		8			0.315	
E	10		10.4	0.393		
E1		8.5			0.334	
G	4.88		5.28	0.192		0.208
L	15		15.85	0.590		0.625
L2	1.27		1.4	0.050		0.055
L3	1.4		1.75	0.055		0.068
М	2.4		3.2	0.094		0.126
R		0.4			0.015	
V2	00		80			



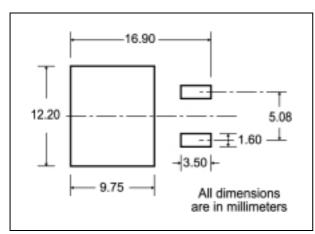
TO-262 (I²PAK) MECHANICAL DATA

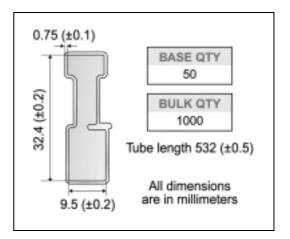
DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
А	4.40		4.60	0.173		0.181
A1	2.40		2.72	0.094		0.107
b	0.61		0.88	0.024		0.034
b1	1.14		1.70	0.044		0.066
С	0.49		0.70	0.019		0.027
c2	1.23		1.32	0.048		0.052
D	8.95		9.35	0.352		0.368
е	2.40		2.70	0.094		0.106
e1	4.95		5.15	0.194		0.202
E	10		10.40	0.393		0.410
L	13		14	0.511		0.551
L1	3.50		3.93	0.137		0.154
L2	1.27		1.40	0.050		0.055



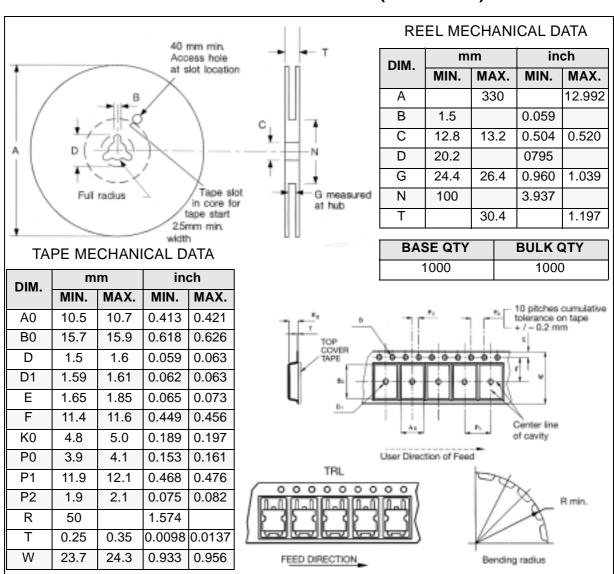
D²PAK FOOTPRINT

TUBE SHIPMENT (no suffix)*





TAPE AND REEL SHIPMENT (suffix "T4")*



on sales type

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