PolySwitch® Resettable PPTCs

Surface Mount > High Temperature SMD Series

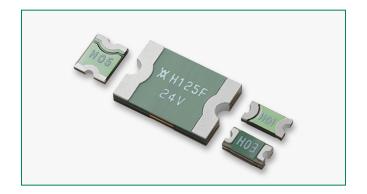
High Temperature SMD Series











Agency Approvals

AGENCY	AGENCY FILE NUMBER
c FL °us	E74889

Additional Information







Description

The High Temperature SMD is the high temperature PPTC series from Littelfuse for industrial and automotive applications. It provides surface mount overcurrent protection for applications where space is a prime concern and resettable protection is desired.

Features

- Products meet applicable industry standards
- Compatible with high-volume electronics assembly
- High Operation Temperature: -40°C~125°C
- Resettable solution against overcurrent and short-circuit
- RoHS compliant, and ISO/TS16949 certificated
- Surface-mount form factor

Applications

- · Industrial motor driver
- Industrial power supply
- Automotive Infotainment
- Automotive ECU protection
- Automotive brake sensor switch FET protection
- Automotive current sensor protection
- Actuators and medium motors
- Trace protection
- Harness/junction box protection
- Powered outputs

Electrical Characteristics

Part	l _H	l _T	V_{MAX}	I _{MAX}	P _{D MAX}	Max Time-to-trip		R _{MIN}	R _{1MAX}		
Number	(A)	(A)	(V _{DC})	(A)	(W)	(A)	(s)	(Ω)	(Ω)		
Hig	High Temperature SMD Series — Size 3216mm/1206mils, 3225mm/1210mils & 7555mm/2920mils										
nanoSMDCH010F	0.10	0.35	30	10	0.80	1.00	0.10	1.10	10.00		
nanoSMDCH030F/24	0.30	0.82	24	10	1.00	8.00	0.10	0.50	1.95		
nanoSMDH075F	0.75	2.00	6	10	1.10	8.00	0.10	0.10	0.36		
microSMDCH010F	0.10	0.35	30	10	0.90	1.00	0.10	1.20	11.00		
microSMDCH050F	0.50	1.50	6	10	1.10	8.00	0.05	0.19	0.90		
SMDCH125F/24	1.25	2.50	24	20	2.00	8.00	0.50	0.08	0.24		

Notes:

: Hold current: maximum current device will pass without interruption in 20°C still air.

: Trip current: minimum current that will switch the device from low resistance to high resistance in 20°C still air.

: Maximum continuous voltage device can withstand without damage at rated current.

: Maximum fault current device can withstand without damage at rated voltage. : Power dissipated from device when in the tripped state in 20°C still air

: Minimum resistance of device as supplied at 20°C unless otherwise specified.

R_{IMAX}: Maximum resistance measured one hour post-trip or post-reflow at 20°C

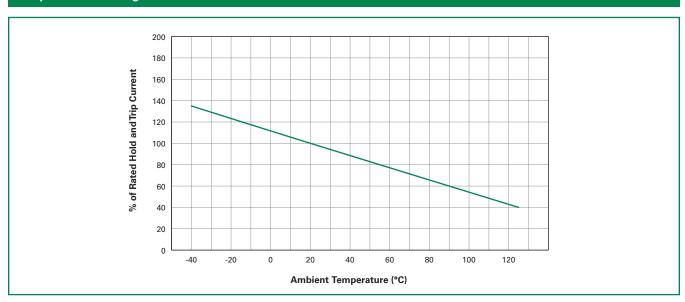
PolySwitch® Resettable PTCs





Temperature Rera	Temperature Rerating											
Maximum Ambient Temperature												
Part Number	-40°C	-20°C	0°C	20°C	25°C	40°C	50°C	60°C	70°C	80°C	85°C	125°C
					Hold Curr	ent (A)						
	High Temp	erature S	MD Series	— Size 3	216mm/12	206mils, 32	225mm/12	210mils &	7555mm/2	2920mils		
nanoSMDCH010F	0.18	0.16	0.15	0.11	0.10	0.09	0.08	0.07	0.07	0.06	0.06	0.03
nanoSMDCH030F/24	0.45	0.40	0.35	0.31	0.30	0.25	0.22	0.20	0.18	0.15	0.13	0.05
nanoSMDH075F	1.07	0.98	0.90	0.78	0.75	0.70	0.66	0.61	0.56	0.52	0.50	0.30
microSMDCH010F	0.18	0.16	0.15	0.11	0.10	0.09	0.08	0.07	0.06	0.05	0.05	0.02
microSMDCH050F	0.85	0.78	0.75	0.54	0.50	0.48	0.45	0.42	0.38	0.35	0.35	0.18
SMDCH125F/24	1.80	1.65	1.50	1.30	1.25	1.15	1.08	1.00	0.90	0.82	0.80	0.28

Temperature Rerating Curve



PolySwitch® Resettable PPTCs

Typical Time-to-Trip Curves at 20°C

High Temperature SMD

A = nanoSMDCH010F

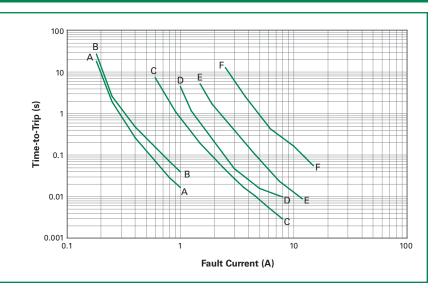
B = microSMDCH010F

C = nanoSMDCH030F/24

D = microSMDCH050F

E = nanoSMDH075F

F = SMDCH125F/24



Physical Specifications

Terminal Pad Material	100% Matte Tin with Nickel Underplate				
Soldering Characteristics ANSI/J-STD-002 Category 3					
Solder Heat Withstand	per IEC-STD 68-2-20, Test Tb, Section 5, Method 1a				
Flammability Resistance	per IEC 695-2-2 Needle Flame Test for 20 seconds				

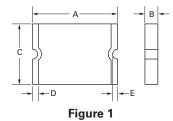
Environmental Specifications

Test	Test Method	Conditions	Resistance Change
	PS300, Section 5.3.2	60°C, 1000 hrs	±3% typ
Storage Life	7-5500, Section 5.5.2	85°C, 1000 hrs	±3% typ
Humidity Aging	PS300, Section 5.3.1	85°C, 85% R.H., 100 hrs	±1.2% typ
Thermal Shock	MII CTD 202 Mathed 107C	85°C, -40°C (20 Times)	-33% typ
Thermal Shock	MIL-STD-202, Method 107G	125°C, -55°C (10 Times)	-33% typ
Vibration	MIL-STD-883C	per MIL-STD-883C	No Change
		Freon	No Change
Solvent Resistance	PS300, Section 5.2.2	Trichloroethane	No Change
		Hydrocarbons	No Change

Moisture Resistance Level	Level 1, J-STD-020
Storage Conditions	40°C max, 70% RH max; devices should remain in original sealed bags prior to use. Devices may not meet specified values if these storage conditions are exceeded.



Dimension Figures



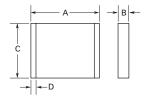
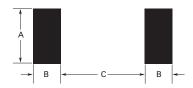


Figure 2

Dimensions

	Dimensions in Millimeters (Inches)										
Part Number	А		E	В		С		D		E	
	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	
High	Temperatu	ire SMD Se	eries — Siz	e 3216mm	/1206mils,	3225mm/	1210mils 8	2 7555mm	/2920mils		
nanoSMDCH010F	3.00 (0.118)	3.40 (0.134)	0.30 (0.012)	0.70 (0.028)	1.37 (0.054)	1.80 (0.071)	0.25 (0.010)	0.75 (0.030)	0.076 (0.003)	_	1
nanoSMDCH030F/24	3.00 (0.118)	3.40 (0.134)	0.85 (0.033)	1.25 (0.049)	1.40 (0.055)	1.80 (0.071)	0.25 (0.010)	0.75 (0.030)	0.076 (0.003)	_	1
nanoSMDH075F	3.00 (0.118)	3.40 (0.134)	0.60 (0.023)	1.00 (0.039)	1.40 (0.055)	1.80 (0.071)	0.20 (0.008)	0.80 (0.032)	_	_	2
microSMDCH010F	3.00 (0.118)	3.43 (0.135)	0.57 (0.022)	0.97 (0.038)	2.35 (0.092)	2.80 (0.110)	0.25 (0.010)	0.75 (0.030)	0.076 (0.003)	_	1
microSMDCH050F	3.00 (0.118)	3.43 (0.135)	0.24 (0.009)	0.64 (0.025)	2.35 (0.092)	2.80 (0.110)	0.25 (0.010)	0.75 (0.030)	0.076 (0.003)	_	1
SMDCH125F/24	7.30 (0.287)	7.70 (0.303)	0.90 (0.035)	1.40 (0.055)	4.90 (0.193)	5.30 (0.209)	0.95 (0.037)	1.45 (0.057)	0.35 (0.014)	_	1

Recommended Pad Layout



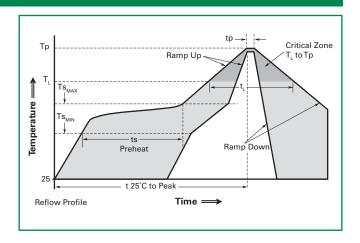
Packaging and Marking Information

				Recommended Pad Layout Figures [mm (in)]						
Part Number	Tape and Reel Quantity	Standard Package	Part Marking	Dimension A (Nom)	Dimension B (Nom)	Dimension C (Nom)	Agency Recognition			
н	High Temperature SMD Series — Size 3216mm/1206mils, 3225mm/1210mils & 7555mm/2920mils									
nanoSMDCH010F	4,000	20,000	H01	1.60 (0.063)	1.00 (0.039)	2.00 (0.079)	_			
nanoSMDCH030F/24	3,000	15,000	H03	1.60 (0.063)	1.00 (0.039)	2.00 (0.079)	UL			
nanoSMDH075F	3,000	15,000	H75	1.60 (0.063)	1.00 (0.039)	2.00 (0.079)	_			
microSMDCH010F	3,000	15,000	H01	2.50 (0.098)	1.00 (0.039)	2.00 (0.079)	_			
microSMDCH050F	4,000	20,000	H05	2.50 (0.098)	1.00 (0.039)	2.00 (0.079)	_			
SMDCH125F/24	3,000	15,000	H125F 24V	5.30 (0.209)	2.00 (0.079)	4.60 (0.181)	UL			

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Solder Reflow Recommendations

Profile Feature	Pb-Free Assembly
Average ramp up rate (Ts _{MAX} to Tp)	3°C/s max
Preheat	
Temperature min (Ts _{MIN})	150°C
Temperature max (Ts _{MAX})	200°C
• Time (ts _{MIN} to ts _{MAX})	60-120 s
Time maintained above:	
Temperature (T _L)	217°C
• Time (t _L)	60-150 s
Peak/Classification temperature (Tp)	260°C
Time within 5°C of actual peak temperature	
Time (tp)	30 s max
Ramp down rate	3°C/s max
Time 25°C to peak temperature	8 min max
Note: All temperatures refer to topside of the package, meas	ured on the nackage hody



Note: All temperatures refer to topside of the package, measured on the package body surface.

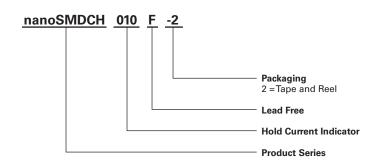
Solder Reflow

- · Recommended reflow method: IR, hot air, nitrogen.
- Recommended maximum paste thickness: 0.25mm (0.010in)
- Devices can be cleaned using standard methods and aqueous solvents.
- Experience has shown the optimum conditions for forming acceptable solder fillets occur when a reasonable amount of solder paste is placed underneath each device's termination. As such, we request that customers comply with our recommended solder pad layouts.
- Customer should validate that the solder paste amount and reflow recommendations meet its application.
- We request that customer board layouts refrain from placing raised features (e.g. vias, nomenclature, traces, etc.) underneath PolySwitch
 devices. It is possible that raised features could negatively impact solderability performance of our devices.

Rework

• Standard industry practices. (Please also avoid direct contact to the device.)

Part Ordering Number System



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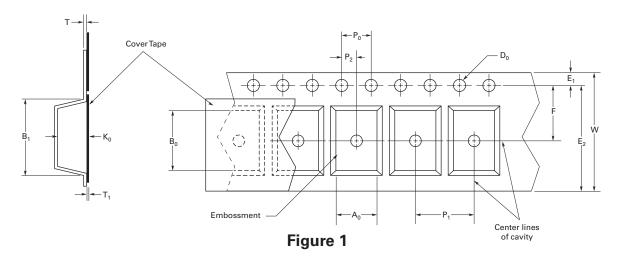




Tape and Reel Specifications

	EIA 481-1									
Description	nanoSMDCH010F	nanoSMDCH030F/24	nanoSMDH075F	microSMDCH010F	microSMDCH050F	SMDCH125F/24				
W	8.0 ± 0.30	8.0 ± 0.30	8.0 ± 0.30	8.0 ± 0.30	8.0 ± 0.30	16.0 ± 0.30				
P ₀	4.0 ± 0.10	4.0 ± 0.10	4.0 ± 0.10	4.0 ± 0.10	4.0 ± 0.10	4.0 ± 0.10				
P ₁	4.0 ± 0.10	4.0 ± 0.10	4.0 ± 0.10	4.0 ± 0.10	4.0 ± 0.10	8.0 ± 0.10				
P ₂	2.0 ± 0.05	2.0 ± 0.05	2.0 ± 0.05	2.0 ± 0.05	2.0 ± 0.05	2.0 ± 0.10				
A _o	1.95 ± 0.10	1.95 ± 0.10	1.95 ± 0.10	2.9 ± 0.10	2.9 ± 0.10	5.5 ± 0.1				
B _o	3.50 ± 0.1/-0.08	3.5 ± 0.1	3.5 ± 0.1	3.55 ± 0.1	3.5 ± 0.1	8.0 ± 0.1				
B ₁ max	4.35	4.35	4.35	4.35	4.35	12.1				
D ₀	1.55 ± 0.05	1.55 ± 0.05	1.55 ± 0.05	1.55 ± 0.05	1.55 ± 0.05	1.5 + 0.10/00				
F	3.50 ± 0.05	3.50 ± 0.05	3.50 ± 0.05	3.50 ± 0.05	3.50 ± 0.05	7.50 ± 0.10				
E ₁	1.75 ± 0.10	1.75 ± 0.10	1.75 ± 0.10	1.75 ± 0.10	1.75 ± 0.10	1.75 ± 0.10				
E ₂ min	6.25	6.25	6.25	6.25	6.25	14.25				
T max	0.3	0.3	0.3	0.3	0.3	0.35				
T ₁ max	0.1	0.1	0.1	0.1	0.1	0.1				
K _o	0.89 ± 0.1	1.35 ± 0.1	1.27 ± 0.1	1.27 ± 0.1	0.9 ± 0.1	2.0 ± 0.1				
A max	185	185	185	185	185	330				
N min	50	50	50	50	50	50				
W ₁	8.4 + 1.5/00	8.4 + 1.5/00	8.4 + 1.5/00	8.4 + 1.5/00	8.4 + 1.5/00	16.4 + 2.0/00				
W ₂ max	14.4	14.4	14.4	14.4	14.4	22.4				

Tape and Reel Diagrams



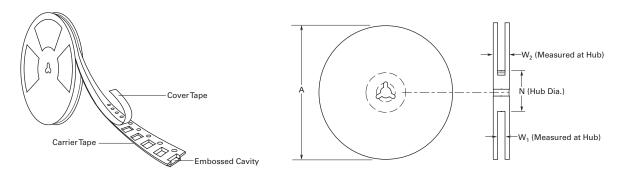


Figure 2

WARNING

- · Users should independently evaluate the suitability of and test each product selected for their own application.
- Operation beyond the maximum ratings or improper use may result in device damage and possible electrical arcing and flame.
- These devices are intended for protection against damage caused by occasional overcurrent or overtemperature fault conditions and should not be used when repeated fault conditions or prolonged trip events are anticipated.
- Contamination of the PPTC material with certain silicone-based oils or some aggressive solvents can adversely impact the performance of the devices.
- Device performance can be impacted negatively if devices are handled in a manner inconsistent with recommended electronic, thermal, and mechanical procedures for electronic components.
- PPTC devices are not recommended for installation in applications where the device is constrained such that its PTC properties are inhibited, for example in rigid potting materials or in rigid housings, which lack adequate clearance to accommodate device expansion.
- · Operation in circuits with a large inductance can generate a circuit voltage (Ldi/dt) above the rated voltage of the device.

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