# **Single 2-Input AND Gate**

## MC74VHC1G08, MC74VHC1GT08

The MC74VHC1G08 / MC74VHC1GT08 is a single 2 input AND gate in tiny footprint packages. The MC74VHC1G08 has CMOS-level input thresholds while the MC74VHC1GT08 has TTL-level input thresholds.

The input structures provide protection when voltages up to 5.5 V are applied, regardless of the supply voltage. This allows the device to be used to interface 5 V circuits to 3 V circuits. The output structures also provide protection when  $V_{CC}$  = 0 V and when the output voltage exceeds  $V_{CC}$ . These input and output structures help prevent device destruction caused by supply voltage – input/output voltage mismatch, battery backup, hot insertion, etc.

#### **Features**

- Designed for 2.0 V to 5.5 V V<sub>CC</sub> Operation
- 3.5 ns t<sub>PD</sub> at 5 V (typ)
- Inputs/Outputs Over-Voltage Tolerant up to 5.5 V
- I<sub>OFF</sub> Supports Partial Power Down Protection
- Source/Sink 8 mA at 3.0 V
- Available in SC-88A, SC-74A, TSOP-5, SOT-553, SOT-953 and UDFN6 Packages
- Chip Complexity < 100 FETs
- NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

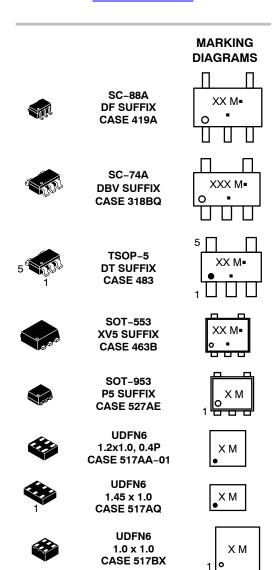


Figure 1. Logic Symbol



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XX = Specific Device Code
M = Date Code\*
Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or position may vary depending upon manufacturing location.

#### ORDERING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 7 of this data sheet.