

Header Board Specification

WHY DO I NEED A HEADER TO DEBUG?

Some PIC[®] microcontrollers, particularly low pin count devices (with 20 pins or less), generally must use a header for debugging. This is done to free up I/O lines for your application and to make production parts more affordable. Optional headers are also available for high pin count devices (with 64 pins or higher).

Debugging requires a two-line connection (plus VDD, VSS and VPP) to communicate with the device. In a high pin count device, losing a few I/O lines is generally not a problem for most designs. But in a low pin count device, it can be a critical problem. Imagine having to do an 8-pin design where there are only 5 I/Os, having used up 2 I/Os just for debugging!

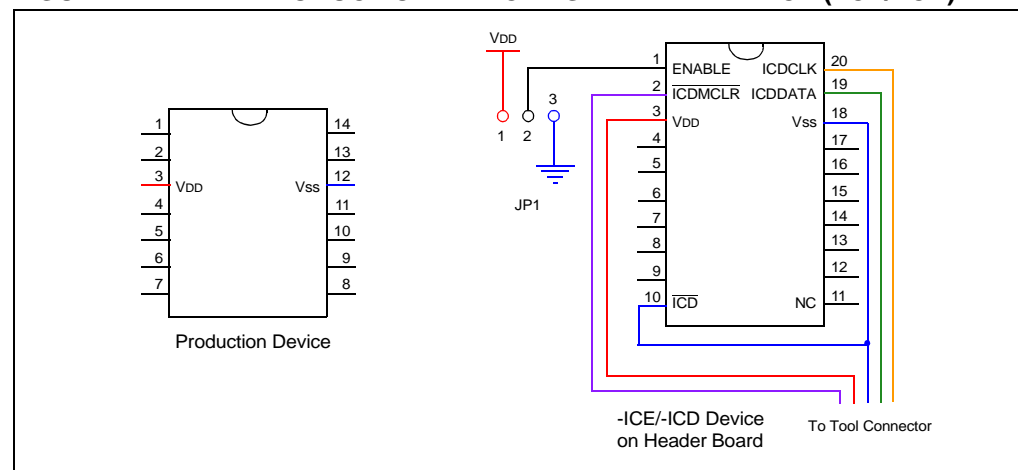
Headers are also used to save you money. In high pin count devices, adding debugging to the silicon can generally be done at little or no cost since the silicon is already fairly large. However, low pin count devices are low cost specifically because they use very little silicon. So, adding debugging circuitry on-board these parts would add significant cost since it would raise the amount of silicon used by a considerable percentage. The header places the cost for debugging up front and frees your production parts from the extra cost of an unused debug module.

Microchip also makes optional debug modules, usually for high pin count devices. The module is optional because you can still do basic debugging without a header, but if you use one, you get back I/O lines, and may also gain additional debugging features. Only certain devices can use an optional header, see the device page on our website for details.

Microchip lists what header must be ordered to work with your device, if one is required. Simply consult the device page on our website, or our other ordering guides.

Note that in all cases, devices can be programmed “in circuit” (called ICSP[™]) with very few exceptions. Even devices without an internal debug feature can still be programmed by connecting the programming/debugging tool to the in-circuit programming lines. These devices simply cannot perform debugging without a header.

FIGURE 1: PRODUCTION DEVICE VS. HEADER DEVICE (-ICE/-ICD)



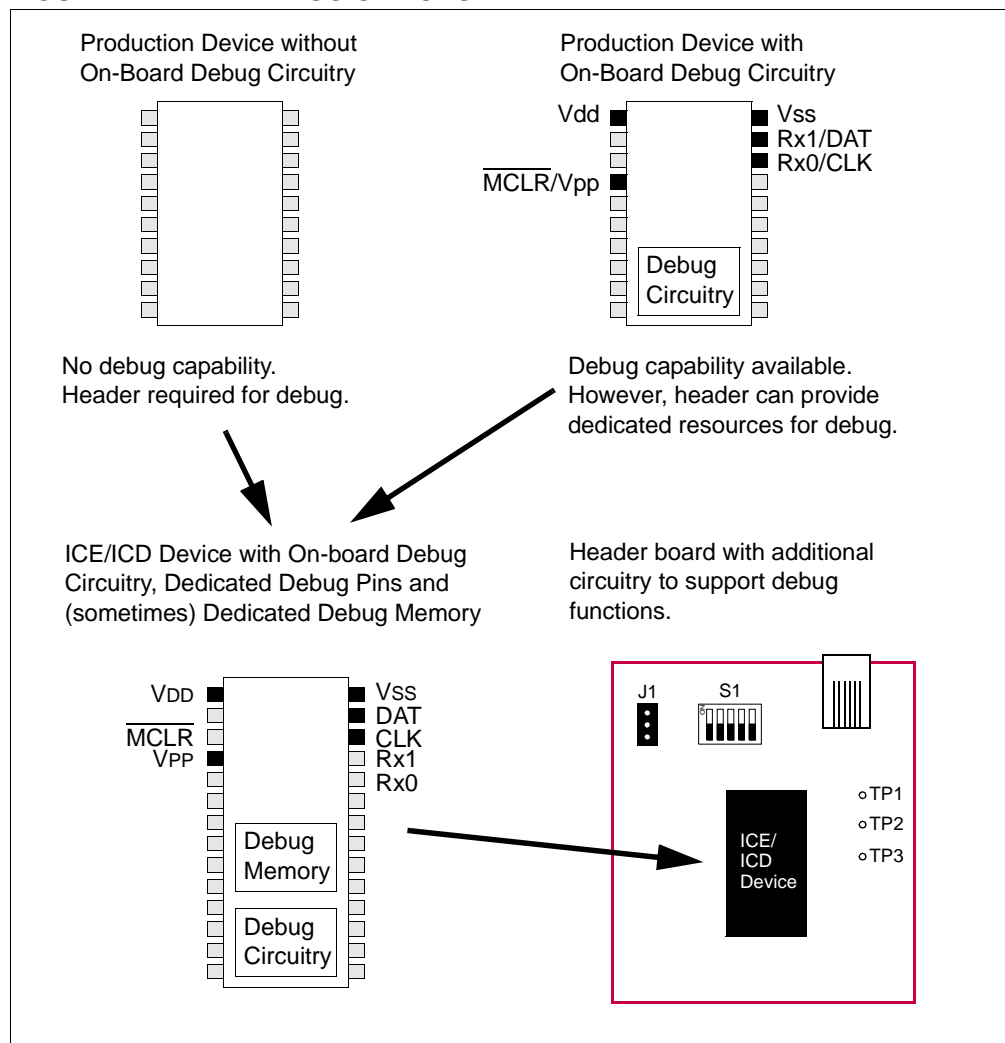
Header Board Specification

DEBUG DETAILS

Next generation in-circuit emulators (such as the MPLAB® REAL ICE™ in-circuit emulator) and in-circuit debuggers work with devices that have on-chip debug circuitry. Sometimes the actual production device will have this circuitry and sometimes a special version of this device is required or available for code debugging. This special version of the chip, with the suffix -ICD or -ICE, is mounted on a header board (Figure 2).

Note: ICE/ICD devices are only used on the header; they are not sold separately.

FIGURE 2: DEBUG OPTIONS



In general, ICE devices are designed for next-generation in-circuit emulator use and ICD devices are designed for in-circuit debugger use. However, ICE devices may be used with an in-circuit debugger and ICD devices may be used with a next-generation in-circuit emulator, but will provide only basic ICD functionality.

To determine what device resources must be dedicated to debugging for either a device with on-board debug capability or the special ICE/ICD device, see the “Resources used by ...” section of the in-circuit emulator or in-circuit debugger on-line help file.

PROGRAMMING DETAILS

The header board is designed to be used with the in-circuit emulator or the in-circuit debugger selected as a debugger, not a programmer, in MPLAB IDE. Any programming of the ICE/ICD device on the header is for debug purposes and includes the debug executive. See your related debug tool documentation for details on using it as a debugger.

To program production (non-ICE/ICD) devices with your debug tool, use the Universal Programming Module (AC162049) or design a modular interface connector on the target. See the appropriate specification for connections. For the most up-to-date device programming specifications, see the Microchip website (www.microchip.com).

Also, production devices may be programmed with the following tools:

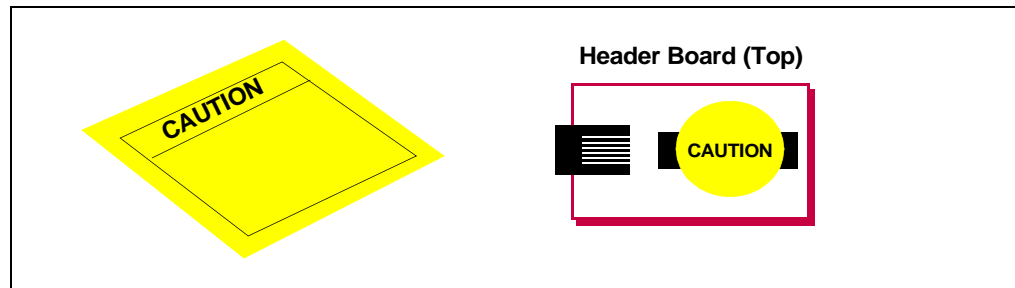
- MPLAB PM3 device programmer
- PICSTART® Plus development programmer
- PICkit™ 1, 2 or 3 development programmer
- MPLAB ICD 3
- MPLAB REAL ICE in-circuit emulator

GENERAL HEADER SETUP

To set up your header, do the following:

1. Check the header board for any stickers and the header box for any paper inserts that may specify special operating instructions (Figure 3). Follow these instructions before doing anything else.

FIGURE 3: SPECIAL HEADER INSTRUCTIONS



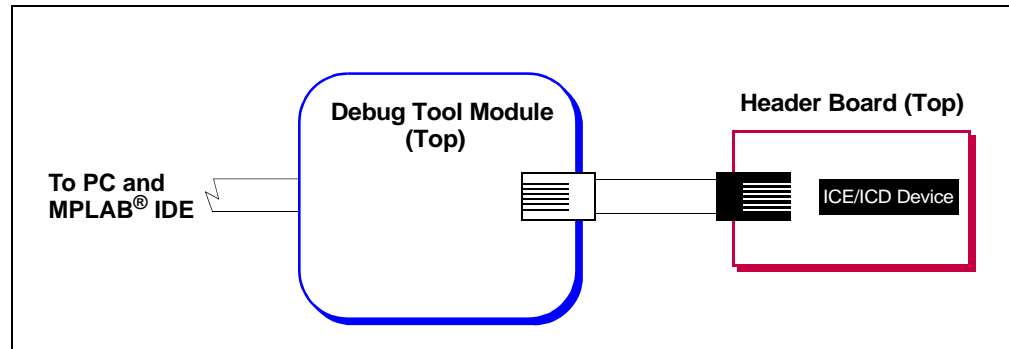
2. Set any jumpers or switches on the header to determine device functionality or selection as specified for that header. See the sections “Optional Headers” or “Required Headers” for information on how to set up individual headers.

Header Board Specification

3. Connect the header to your desired debug tool by consulting the tool documentation for connection options. An example connection is shown in Figure 4.

The special ICE/ICD device is mounted on the top of a header and its signals are routed to the emulator or debugger connector. These special device versions are labeled with the appropriate suffix (i.e., either *Device-ICE* or *Device-ICD*).

FIGURE 4: CONNECT HEADER TO DEBUG TOOL



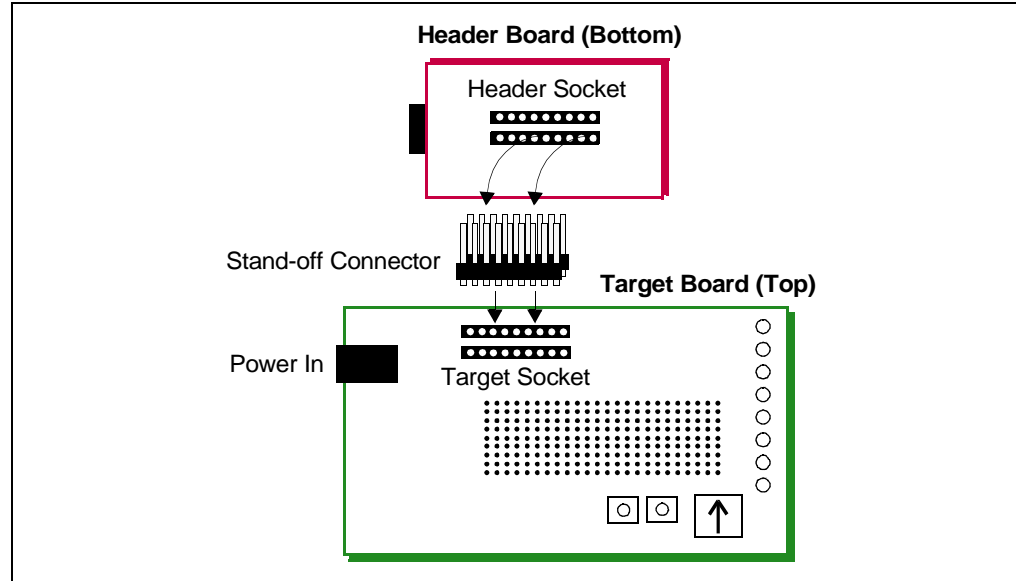
4. Connect the header to the target board. On the bottom of the header is a socket that is used to connect to the target board. The header may be connected to the target board as follows:
 - a) PDIP header socket to PDIP target socket with a stand-off (male-to-male) connector
 - b) Header socket to plug on the target board
 - c) Header socket to target socket with a transition socket (see the "*Transition Socket Specification*", DS51194)

An example connection is shown in Figure 5.

The header socket will have the same pin count as your selected device. The ICE/ICD device on the top of the header usually has a larger pin count because it has additional pins that are dedicated to debug.

Header Board Specification

FIGURE 5: CONNECT HEADER TO TARGET



5. If using a debug tool that can power the target, power that tool now.
6. Power the target, if needed.

CALIBRATION BITS

The calibration bits for the band gap and internal oscillator are always preserved to their factory settings.

PERFORMANCE ISSUES

The PIC[®] MCU devices do not support partial program memory erase; therefore, users may experience slower performance than with other devices.

Also, see either the in-circuit emulator or the in-circuit debugger Help file for information on specific device limitations that may affect performance.

RELATED DEBUG TOOLS

The following tools support the use of headers:

- MPLAB REAL ICE In-Circuit Emulator
- MPLAB ICD 2 In-Circuit Debugger
- MPLAB ICD 3 In-Circuit Debugger
- PICkit 2 or 3 Debug Express

See the Microchip website (<http://www.microchip.com>) for the latest documentation.

Header Board Specification

NOTES:

Required Headers

INTRODUCTION

Some devices have no built-in debug circuitry. Therefore, special ICE/ICD versions of these devices are required for debug tool operation.

Currently available headers and their associated ICE/ICD devices are shown below by supported device.

TABLE 1: REQUIRED HEADERS BY DEVICE

Device	Pin Count	Header Part Number	ICE/ICD Device Used	VDD Max
PIC10F200/2/4/6	8/14	AC162059	PIC16F505-ICD	5.5V
PIC10F220/2	8/14	AC162070	PIC16F506-ICD	5.5V
PIC12F508/509	8/14	AC162059	PIC16F505-ICD	5.5V
PIC12F510	8/14	AC162070	PIC16F506-ICD	5.5V
PIC12F519	8/14	AC162096	PIC16F526-ICD	5.5V
PIC12F609/HV609	28	AC162083	PIC16F616-ICD	5.5V
PIC12F615/HV615	28	AC162083	PIC16F616-ICD	5.5V
PIC12F629	8	AC162050	PIC12F675-ICD ⁽¹⁾	5.5V
PIC12F635	14	AC162057	PIC16F636-ICD	5.5V
PIC12F675	8	AC162050	PIC12F675-ICD ⁽¹⁾	5.5V
PIC12F683	8	AC162058	PIC12F683-ICD	5.5V
PIC16F505	8/14	AC162059	PIC16F505-ICD	5.5V
PIC16F506	8/14	AC162070	PIC16F506-ICD	5.5V
PIC16F526	8/14	AC162096	PIC16F526-ICD	5.5V
PIC16F610/HV610	14/16	AC162083	PIC16F616-ICD	5.5V
PIC16F616/HV616	14/16	AC162083	PIC16F616-ICD	5.5V
PIC16F627A/628A	18	AC162053	PIC16F648A-ICD	5.5V
PIC16F630	14	AC162052	PIC16F676-ICD	5.5V
PIC16F631	20	AC162061	PIC16F690-ICD	5.5V
PIC16F636	14	AC162057	PIC16F636-ICD	5.5V
PIC16F639 (Dual die)	20	AC162066	PIC16F636-ICD	5.5V
PIC16F648A	18	AC162053	PIC16F648A-ICD	5.5V
PIC16F676	14	AC162052	PIC16F676-ICD	5.5V
PIC16F677	20	AC162061	PIC16F690-ICD	5.5V
PIC16F684	14	AC162055	PIC16F684-ICD	5.5V
PIC16F685/687	20	AC162061	PIC16F690-ICD	5.5V
PIC16F688	14	AC162056	PIC16F688-ICD	5.5V
PIC16F689/690	20	AC162061	PIC16F690-ICD	5.5V

Required Headers

TABLE 1: REQUIRED HEADERS BY DEVICE (CON'T)

Device	Pin Count	Header Part Number	ICE/ICD Device Used	VDD Max
PIC16F716	18	AC162054	PIC16F716-ICD ⁽¹⁾	5.5V
PIC16F785/HV785	20	AC162060	PIC16F785-ICD	5.5V
PIC18F13K50 PIC18F14K50	20	AC244023	PIC18F14K50-ICE	5.5V
PIC18LF13K50 PIC18LF14K50	20	AC244024	PIC18LF14K50-ICE	3.6V 2.75V ⁽²⁾

Note 1: These ICE/ICD devices have limitations. See header section for details.

2: VDDCORE Max

AC162050, AC162058

Header Identification

The header AC number is used for ordering the header. However, this number is not on the header board, as the board is often used for multiple headers by inserting different ICE/ICD devices. To identify these header boards, use the following information.

Header	ICE/ICD Device	Board Assembly Number
AC162050	PIC12F675-ICD	02-01662
AC162058	PIC12F683-ICD	

Header Setup and Operation

For these headers, device peripherals need to be selected by setting jumper J1 to the appropriate position. For AC162050, this will have the effect of selecting the device.

Header	Jumper J1	Function	Device Selected
AC162050	2-3	A/D Disabled	PIC12F629
	1-2	A/D Enabled	PIC12F675
AC162058	1-2	A/D Enabled	PIC12F683

AC162050 Limitation:

These headers cannot be programmed while the GP1/RA1 pin is high (VIH) due to an -ICD debug silicon issue. There are two work arounds:

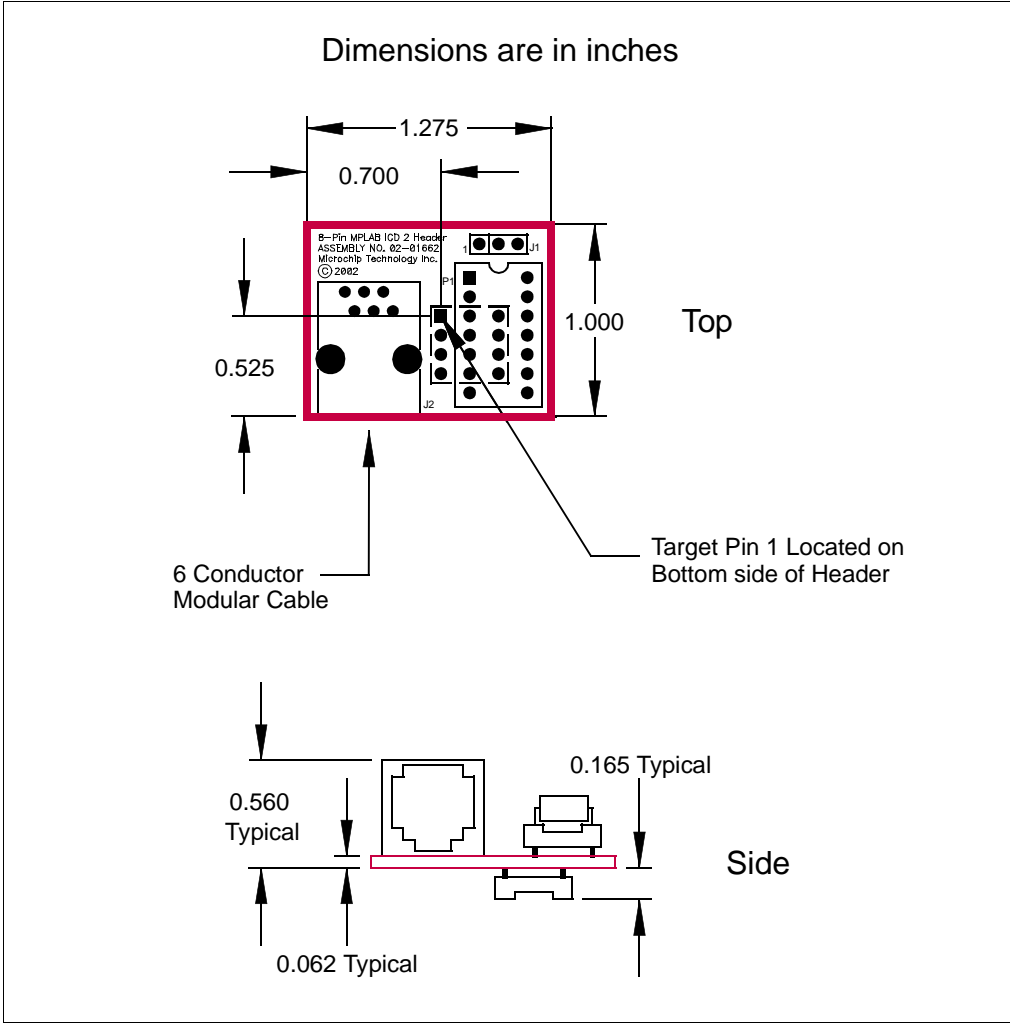
1. Move the circuitry that makes GP1/RA1 high to another I/O pin during programming.
2. Manually make GP1/RA1 low during programming (for debuggers that can supply power to the target):
 - a) Disconnect the header from the target circuit.
 - b) Select *Debugger>Settings*, **Power** tab, and check "Power target circuit from ..." if it is not already checked.
 - c) Connect GP1 to Vss on the header.
 - d) Program the header by selecting *Debugger>Program*.
 - e) Disconnect GP1 from Vss on the header.
 - f) If you were NOT using a debug tool to power your target board, select *Debugger>Settings*, **Power** tab, and uncheck "Power target circuit from ...".
 - g) Insert the header into the target board.
 - h) Code is now programmed into the device and ready to be debugged.
 - i) Repeat the process to reprogram the device.

Header Dimensions

The figure below lists the dimensions for the header board. Dimensions are design values in inches.

If the length and/or width of the header board is too large a footprint for the target board, consider using stand-offs, transition sockets or other extenders in the header connection socket to raise the header above the target.

FIGURE 6: DIMENSIONS – AC162050, AC162058



Required Headers

AC162052, AC162055, AC162056, AC162057

Header Identification

The header AC number is used for ordering the header. However, this number is not on the header board, as the board is often used for multiple headers by inserting different ICE/ICD devices. To identify these header boards, use the following information.

Header	ICE/ICD Device	Board Assembly Number
AC162052	PIC16F676-ICD	02-01686
AC162055	PIC16F684-ICD	
AC162056	PIC16F688-ICD	
AC162057	PIC16F636-ICD	

Header Setup and Operation

For these headers, device peripherals need to be selected by setting jumper J1 to the appropriate position. For AC162052 and AC162057, this will have the effect of selecting the device.

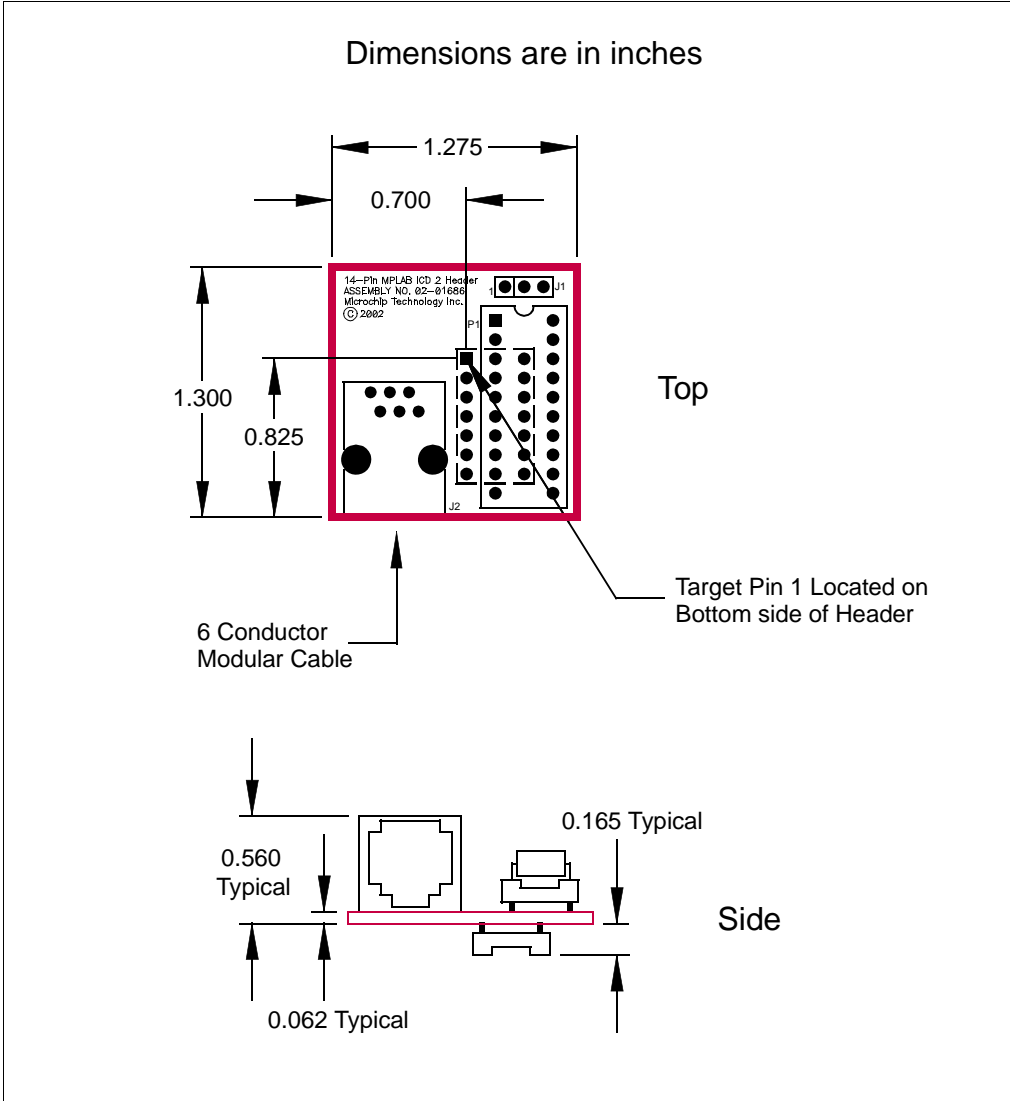
Header	Jumper J1	Function	Device Selected
AC162052	2-3	A/D Disabled	PIC16F630
	1-2	A/D Enabled	PIC16F676
AC162055	Don't care	N/A	PIC16F684
AC162056	Don't care	N/A	PIC16F688
AC162057	2-3	PORTC, Comparator 2 Disabled	PIC12F635
	1-2	PORTC, Comparator 2 Enabled	PIC16F636

Header Dimensions

The figure below lists the dimensions for the header board. Dimensions are design values in inches.

If the length and/or width of the header board is too large a footprint for the target board, consider using stand-offs, transition sockets or other extenders in the header connection socket to raise the header above the target.

FIGURE 7: DIMENSIONS – AC162052, AC162055, AC162056, AC162057



Required Headers

AC162053, AC162054

Header Identification

The header AC number is used for ordering the header. However, this number is not on the header board, as the board is often used for multiple headers by inserting different ICE/ICD devices. To identify these header boards, use the following information.

Header	ICE/ICD Device	Board Assembly Number
AC162053	PIC16F648A-ICD	02-01695
AC162054	PIC16F716-ICD	

Header Setup and Operation – AC162053

For this headers, there are no jumpers/switches. The device with the most program memory is always selected.

If PIC16F627A or PIC16F628A devices are selected for development in MPLAB IDE, the warning “Invalid target device ID” may be received in the build window and as a dialog. The reason is the PIC16F648A-ICD device supports PIC16F648A, PIC16F627A and PIC16F628A, but only reports the device ID for the PIC16F648A.

Ignore this warning or disable it under the **Warnings** tab on the ICD Programming dialog.

Header Setup and Operation – AC162054

This header supports one device (PIC16F716) so there are no jumpers or switches.

Limitation:

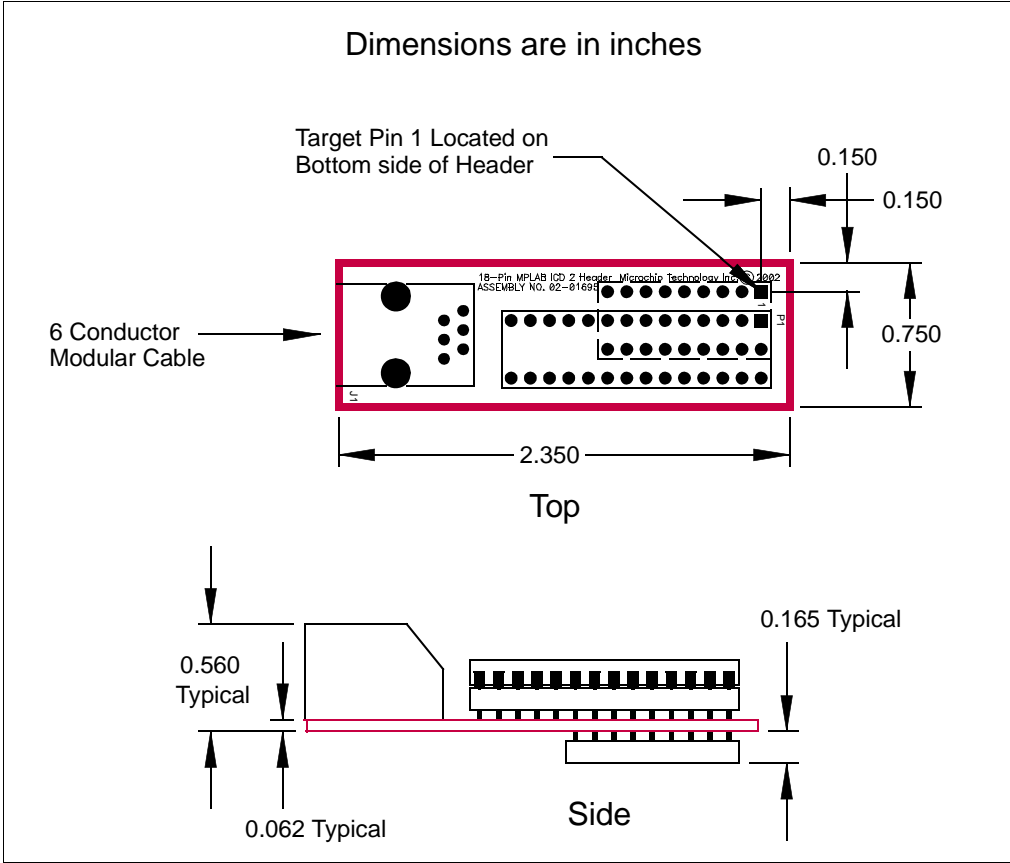
When driving a clock oscillator of more than 4 MHz into OSC1 in HS oscillator mode, the device will not go into debug mode. Therefore crystal caps will be required. The 32 kHz to 4 MHz range does not have this issue.

Header Dimensions

The figure below lists the dimensions for the header board. Dimensions are design values in inches.

If the length and/or width of the header board is too large a footprint for the target board, consider using stand-offs, transition sockets or other extenders in the header connection socket to raise the header above the target.

FIGURE 8: DIMENSIONS – AC162053, AC162054



Required Headers

AC162059, AC162070, AC162096

Header Identification

The header AC number is used for ordering the header. However, this number is not on the header board, as the board is often used for multiple headers by inserting different ICE/ICD devices. To identify these header boards, use the following information.

Header	ICE/ICD Device	Board Assembly Number
AC162059	PIC16F505-ICD	02-01803
AC162070	PIC16F506-ICD	
AC162096	PIC16F526-ICD	

Header Setup and Operation

The ICD devices on these headers are specifically designed to select a device without the use of additional jumpers or switches.

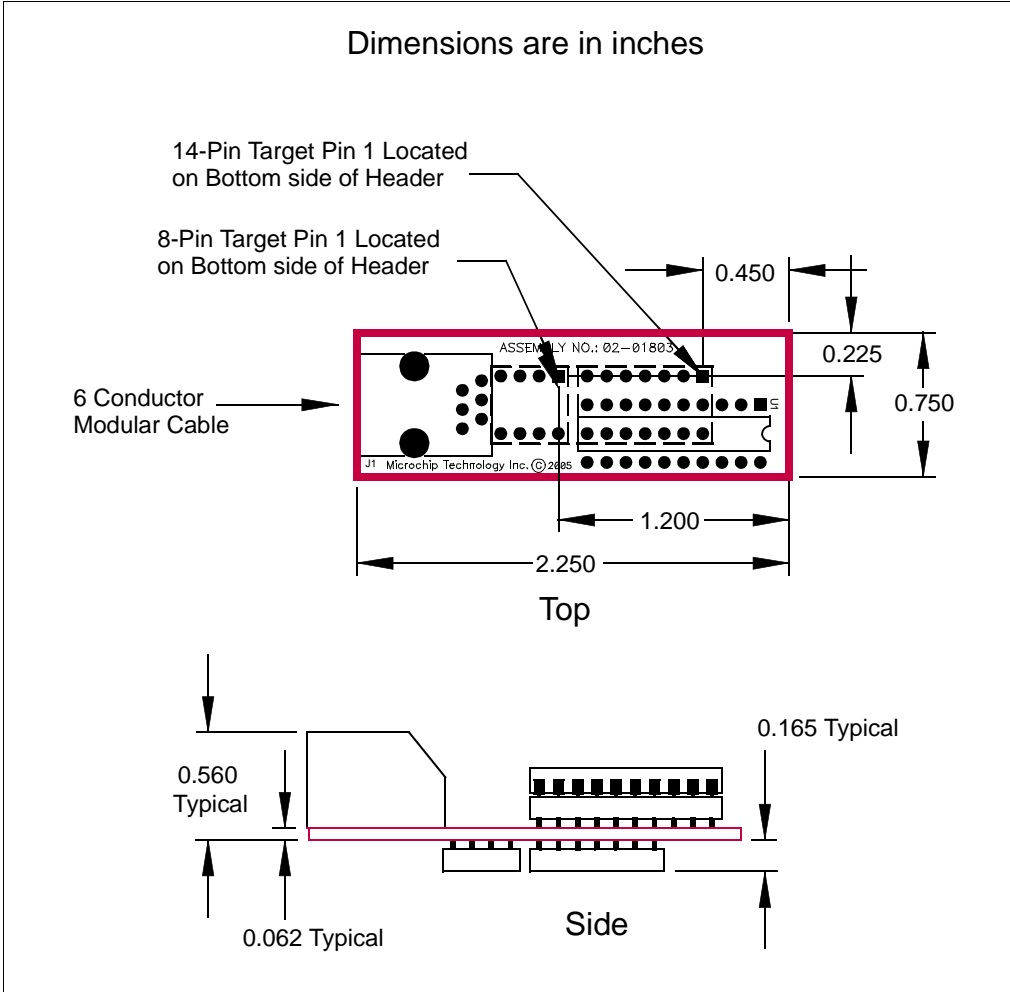
These headers support 8 and 14-pin devices (see Figure 9.) For the AC162059 and AC162070, there is an 8-pin and a 14-pin connector. For the AC162096, there is only a 14-pin connector. (The 8-pin connector is not populated.) Use the 14-pin connector for 8-pin devices, but make sure device pin 1 is placed at the 14-pin connector pin 1.

Header Dimensions

The figure below lists the dimensions for the header board. Dimensions are design values in inches.

If the length and/or width of the header board is too large a footprint for the target board, consider using stand-offs, transition sockets or other extenders in the header connection socket to raise the header above the target.

FIGURE 9: DIMENSIONS – AC162059, AC162070, AC162096



Required Headers

AC162060

Header Identification

The header AC number is used for ordering the header. However, this number is not on the header board, as the board is often used for multiple headers by inserting different ICE/ICD devices. To identify this header board, use the following information.

Header	ICE/ICD Device	Board Assembly Number
AC162060	PIC16F785-ICD	02-01820

Header Setup and Operation

For the PIC16F785 20-pin header, you will need to connect the jumper J2 to enable the shunt regulator.

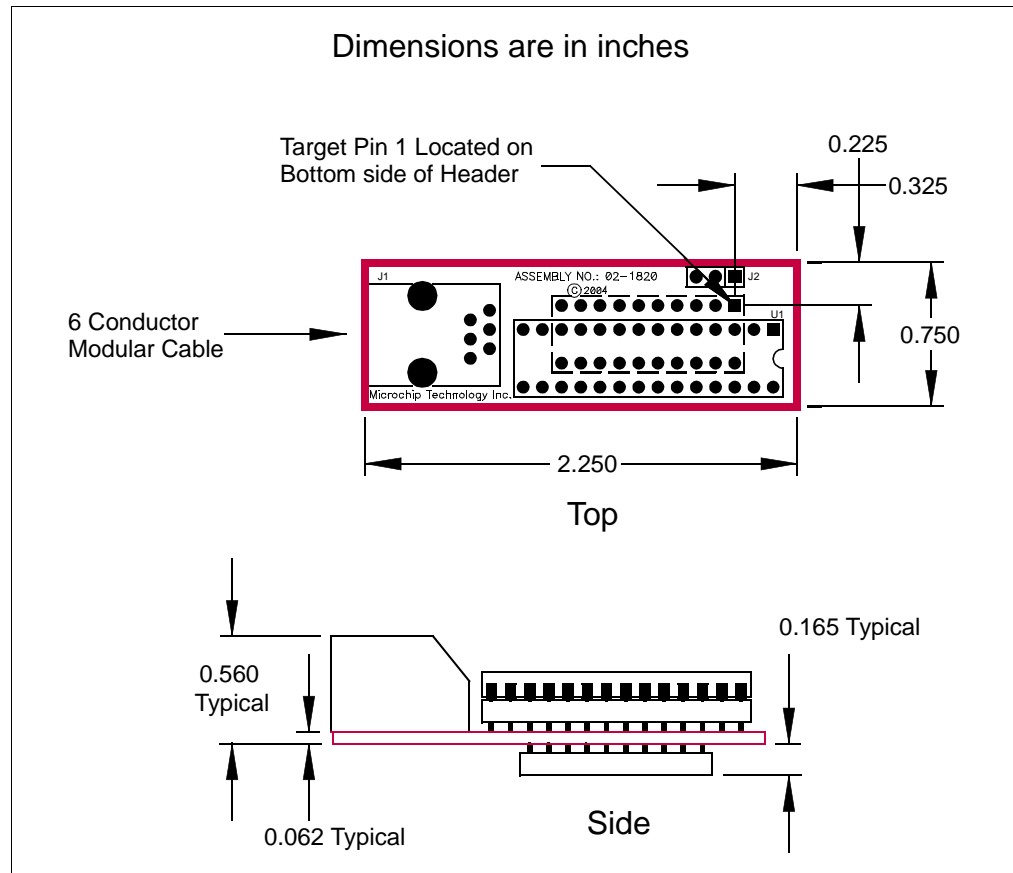
Device	Device Type	Jumper Setting	Function
PIC16F785	F	1-2	Disable shunt regulator
PIC16HV785	HV	2-3	Enable shunt regulator

Header Dimensions

The figure below lists the dimensions for the header board. Dimensions are design values in inches.

If the length and/or width of the header board is too large a footprint for the target board, consider using stand-offs, transition sockets or other extenders in the header connection socket to raise the header above the target.

FIGURE 10: DIMENSIONS – AC162060



AC162061

Header Identification

The header AC number is used for ordering the header. However, this number is not on the header board, as the board is often used for multiple headers by inserting different ICE/ICD devices. To identify this header board, use the following information.

Header	ICE/ICD Device	Board Assembly Number
AC162061	PIC16F690-ICD	02-01835

Header Setup and Operation

For the PIC16F690 20-pin header, you will need to set the S1 switches (Figure 11) to enable peripherals and choose devices (Table).

FIGURE 11: S1 SWITCH HARDWARE

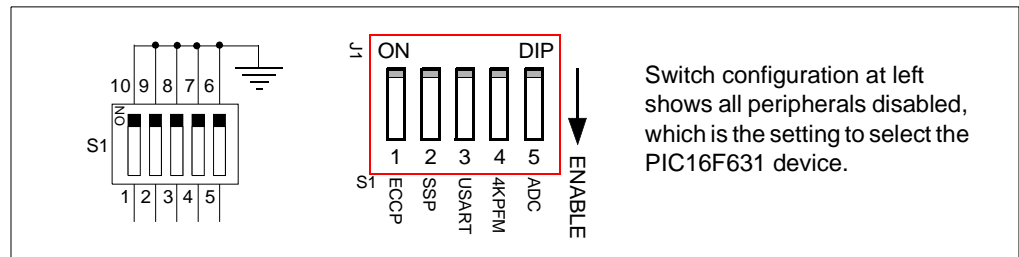


TABLE 2: S1 SWITCH DEVICE SELECTION

Device	Switches				
	1 ECCP	2 SSP	3 USART	4 4k PFM	5 ADC
PIC16F631	0	0	0	0*	0
PIC16F677	0	1	0	0**	1
PIC16F685	1	0	0	1	1
PIC16F687	0	1	1	0**	1
PIC16F689	0	1	1	1	1
PIC16F690	1	1	1	1	1

Legend: 1 = Enabled 0 = Disabled * = 1k PFM ** = 2k PFM

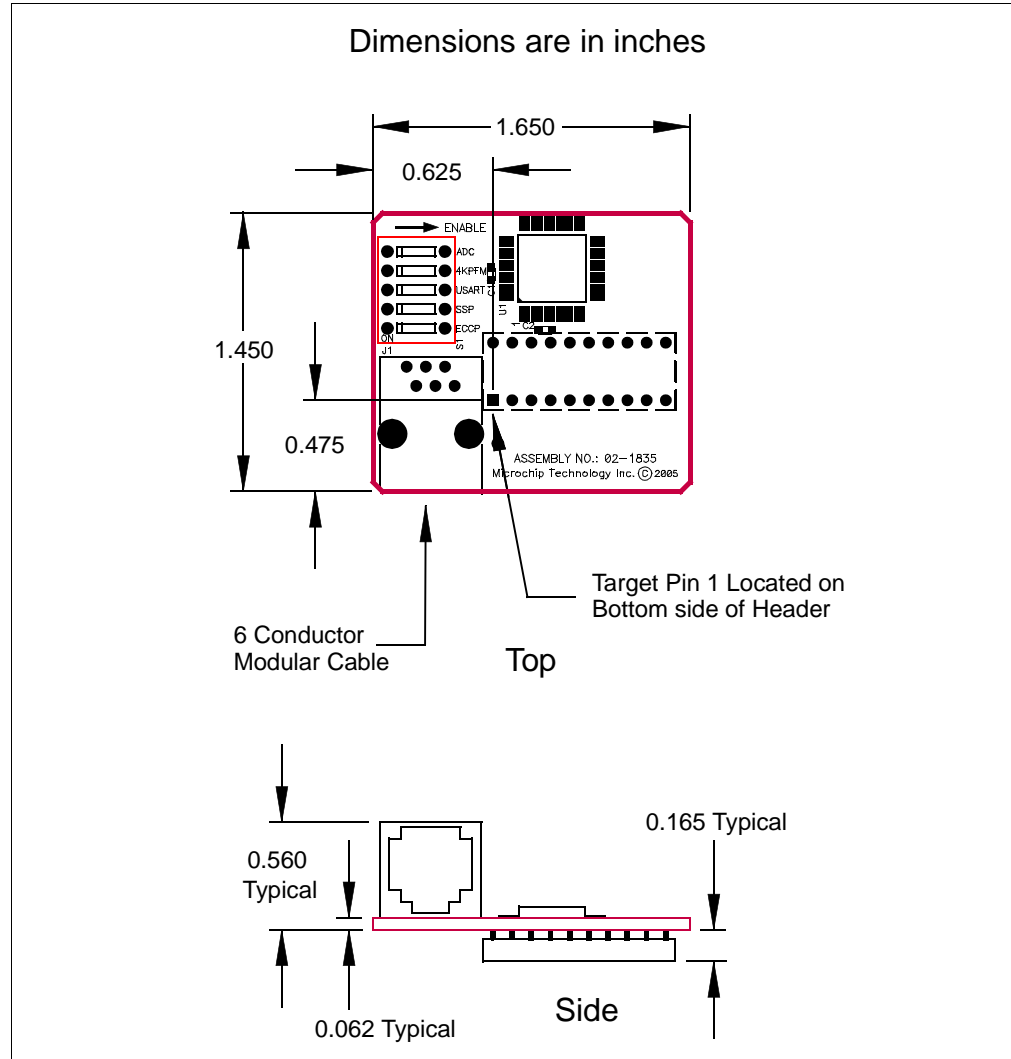
Required Headers

Header Dimensions

The figure below lists the dimensions for the header board. Dimensions are design values in inches.

If the length and/or width of the header board is too large a footprint for the target board, consider using stand-offs, transition sockets or other extenders in the header connection socket to raise the header above the target.

FIGURE 12: DIMENSIONS – AC162061



AC162066

Header Identification

The header AC number is used for ordering the header. However, this number is not on the header board, as the board is often used for multiple headers by inserting different ICE/ICD devices. To identify this header board, use the following information.

Header	ICE/ICD Device	Board Assembly Number
AC162066	PIC16F636-ICD	02-01832

Header Setup and Operation

For the PIC16F639 20-pin header, you will need to connect the jumper J3 as specified below.

Tool	Jumper Setting	Function
MPLAB [®] ICE 2000	1-2	Run/program as production device
In-circuit debuggers, next generation in-circuit emulators	2-3	Run/program as ICD device

In addition to being used with debug tools that normally use headers, this header is used with the PCM16YM0 processor module to emulate a PIC16F639 on the MPLAB ICE 2000 in-circuit emulator. Plug the end of the processor module into the header, and then plug the header into the transition socket or directly onto the target board.

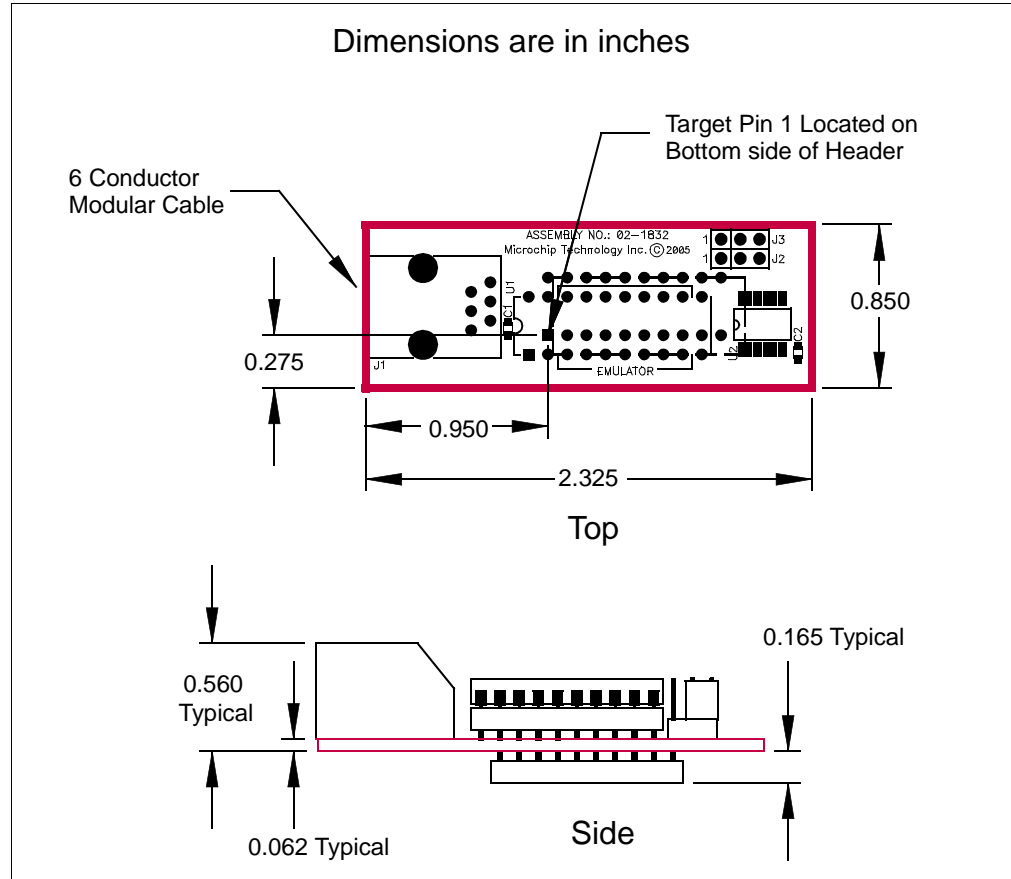
Required Headers

Header Dimensions

The figure below lists the dimensions for the header board. Dimensions are design values in inches.

If the length and/or width of the header board is too large a footprint for the target board, consider using stand-offs, transition sockets or other extenders in the header connection socket to raise the header above the target.

FIGURE 13: DIMENSIONS – AC162066



AC162083


Header Identification

The header AC number is used for ordering the header. However, this number is not on the header board, as the board is often used for multiple headers by inserting different ICE/ICD devices. To identify this header board, use the following information.

Header	ICE/ICD Device	Board Assembly Number
AC162083	PIC16F616-ICD	02-01976

Header Setup and Operation

CAUTION



Incorrect rotary switch (Figure 14) settings may irreparably damage the header. Ensure rotary switch settings are correct (Table 3) before powering or connecting the header. Do not change the rotary switch setting while the header is powered or connected to a debug tool. Do not power shunt regulator (HV) devices from the debug tool.

Test points are available on this header to check the following: Ground (TP1), VDD (TP2), ICD Clock (TP3), ICD Data (TP4) and ICD MCLR/VPP (TP5).

TABLE 3: ROTARY SWITCH SETTINGS

Switch Position	Device	Switch Position	Device
0	PIC12HV609	8	PIC12F609
1	PIC12HV615	9	PIC12F615
2	Reserved HV	A	Reserved F
3	PIC16HV610	B	PIC16F610
4	PIC16HV616	C	PIC16F616
5	Reserved HV	D	Reserved F
6	Reserved HV	E	Reserved F
7	Reserved HV	F	Reserved F

POTENTIAL ISSUES

HV device selected instead of F device

If you inadvertently select a shunt regulator (HV) device and attempt to use it in a target board designed for a non-shunt regulator (F) device, the shunt may draw excessive current due to the lack of current-limiting circuitry on the target board and damage the device mounted on the header.

F device selected instead of HV device

If you inadvertently select a non-shunt regulator (F) device and attempt to use it in a target board designed for a shunt regulator (HV) device, the device may draw excessive current due to the higher voltage used on a target board designed for HV devices and damage the device mounted on the header.

HV devices cannot be powered from debug tool

Do not select in MPLAB IDE to power the target (debug header) from the debug tool (if it supports powering the target) when using shunt regulator (HV) devices since this will also cause the shunt to draw excessive current.

Required Headers

DETERMINING DAMAGE

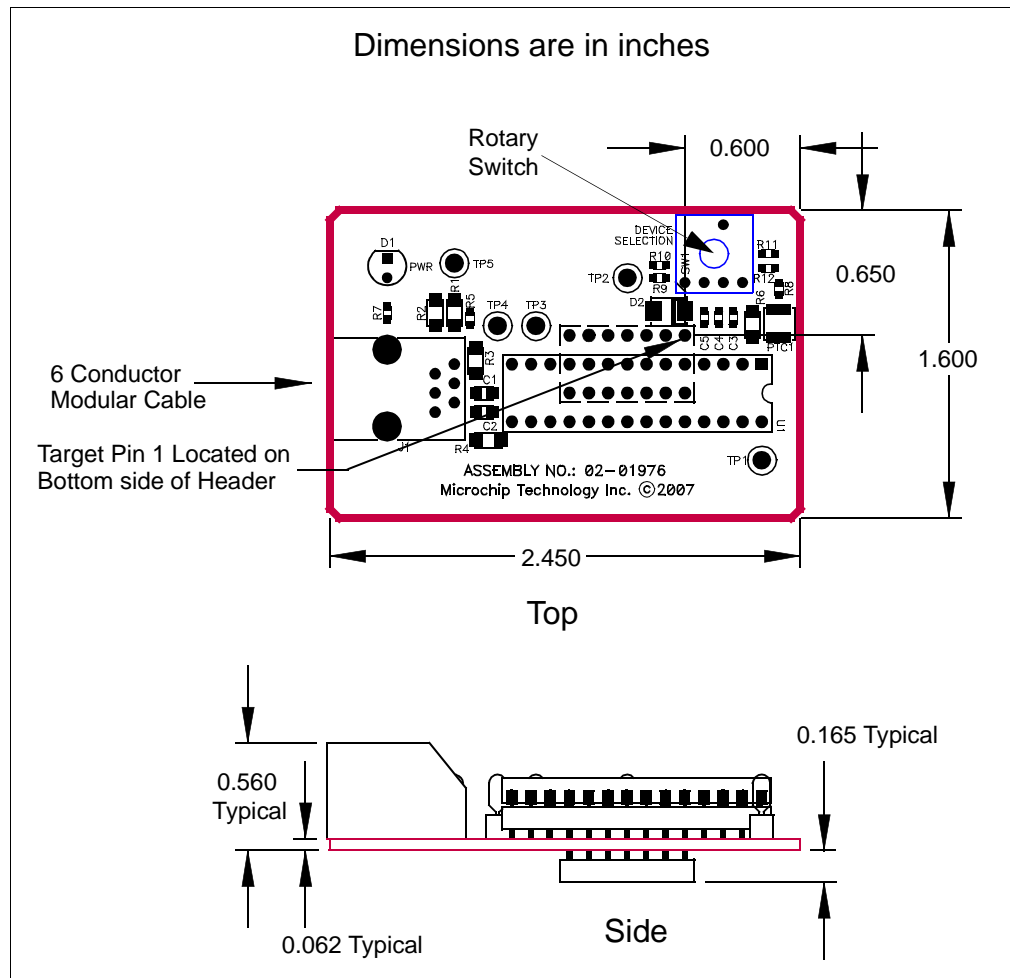
A damaged header will cause MPLAB IDE to report a device ID of 0. However, there are other issues that can cause the device ID to report as 0. Please consult your debug tool documentation on troubleshooting to identify the problem. If you believe you have a damaged header, please contact Microchip technical support at <http://support.microchip.com>.

Header Dimensions

The figure below lists the dimensions for the header board. Dimensions are design values in inches.

If the length and/or width of the header board is too large a footprint for the target board, consider using stand-offs, transition sockets or other extenders in the header connection socket to raise the header above the target.

FIGURE 14: DIMENSIONS – AC162083



AC244023, AC244024

Header Identification

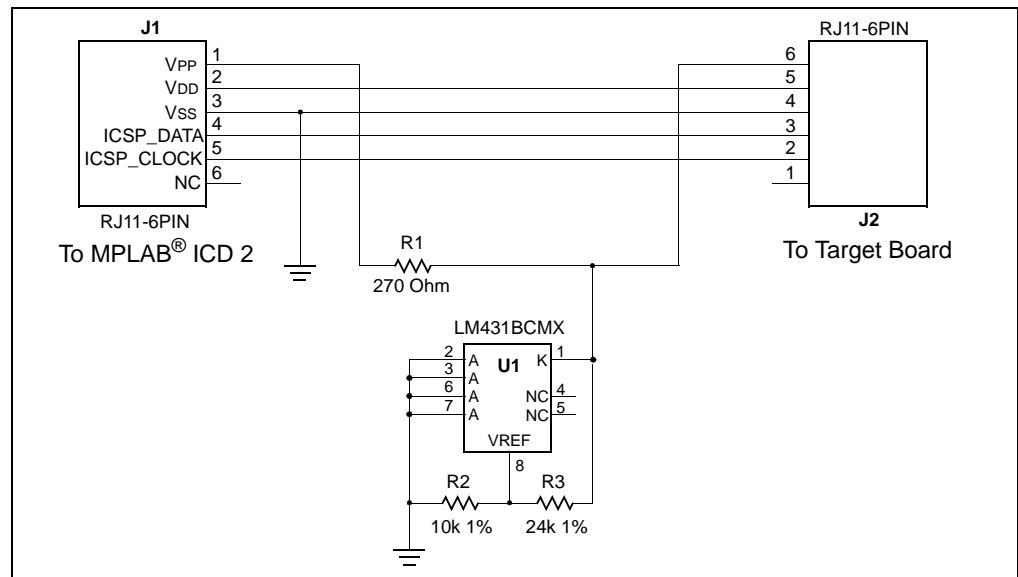
The header AC number is used for ordering the header. However, this number is not on the header board, as the board is often used for multiple headers by inserting different ICE/ICD devices. To identify these header boards, use the following information.

Header	ICE/ICD Device	Board Assembly Number
AC244023	PIC18F14K50-ICE	02-02031
AC244024	PIC18LF14K50-ICE	

Header Setup and Operation

When the MPLAB ICD 2 is used with this header, you must use the Vpp Limiter (AC164112).

FIGURE 15: VPP LIMITER SCHEMATIC



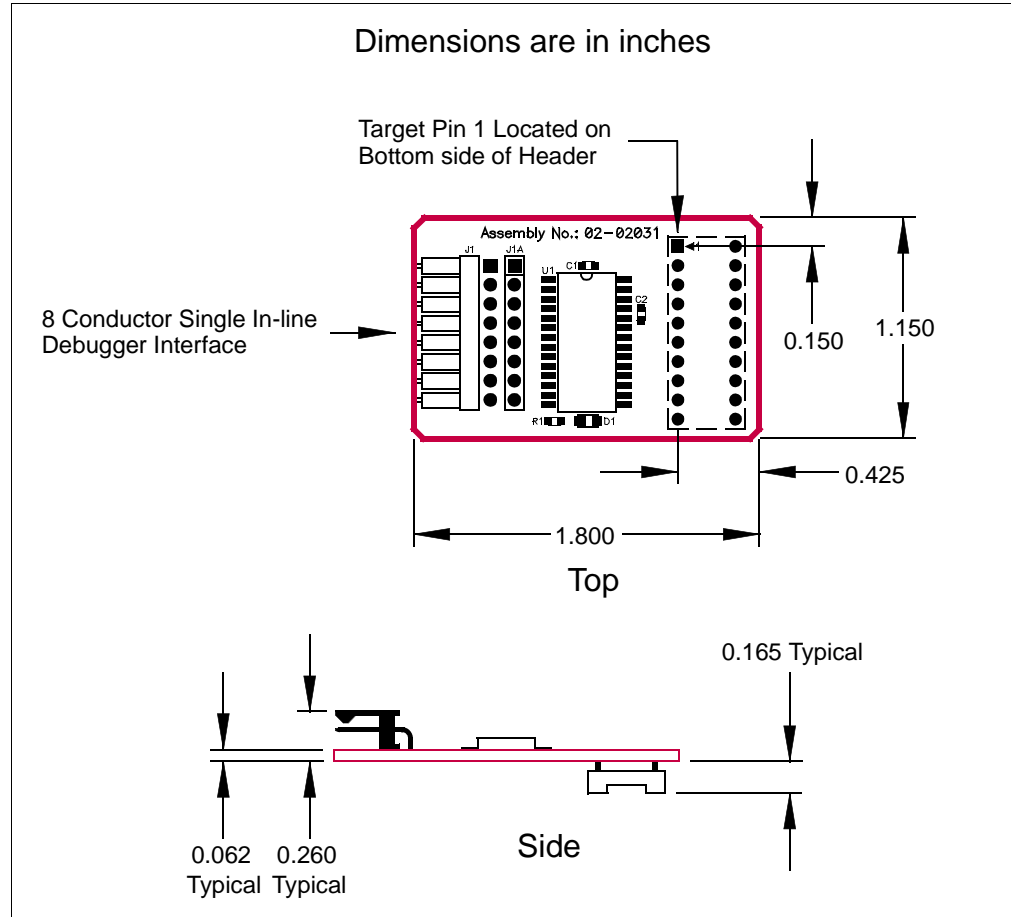
Required Headers

Header Dimensions

The figure below lists the dimensions for the header board. Dimensions are design values in inches.

If the length and/or width of the header board is too large a footprint for the target board, consider using stand-offs, transition sockets or other extenders in the header connection socket to raise the header above the target.

FIGURE 16: DIMENSIONS – AC244023, AC224024



Optional Headers

INTRODUCTION

Devices that have built-in debug circuitry do not require a header to use debug tools. However, some pins and memory must be used to support the debug function. Special ICE/ICD versions offering additional pins, memory and emulator functions can be used to provide superior emulating/debugging capabilities.

Currently available headers and their associated ICE/ICD devices are shown below by supported device.

TABLE 4: OPTIONAL HEADERS BY DEVICE

Device	Pin Count	Header Part Number	ICE/ICD Device Used	V _{DD} Max
PIC18F1230/1330	28	AC162078	PIC18F1330-ICD	5.5V
PIC18F24J10 PIC18F25J10	28/40	AC162067	PIC18F45J10-ICE	3.6V
PIC18LF24J10 PIC18LF25J10				3.6V 2.75V ⁽²⁾
PIC18F44J10 PIC18F45J10				3.6V
PIC18LF44J10 PIC18LF45J10				3.6V 2.75V ⁽²⁾
PIC18F44J10 PIC18F45J10				44
PIC18LF44J10 PIC18LF45J10	3.6V 2.75V ⁽²⁾			
PIC18F63J11 PIC18F63J90 PIC18F64J11 PIC18F64J16 PIC18F64J90 PIC18F64J95 PIC18F65J11 PIC18F65J90	64/80	AC162079	PIC18F85J90-ICE	
PIC18F83J11 PIC18F83J90 PIC18F84J11 PIC18F84J16 PIC18F84J90 PIC18F84J95 PIC18F85J11 PIC18F85J90				

Optional Headers

TABLE 4: OPTIONAL HEADERS BY DEVICE (CON'T)

Device	Pin Count	Header Part Number	ICE/ICD Device Used	V _{DD} Max
PIC18F65J10 PIC18F65J15 PIC18F66J10 PIC18F66J15 PIC18F67J10	64/80	AC162062	PIC18F87J10-ICE	3.6V
PIC18F85J10 PIC18F85J15 PIC18F86J10 PIC18F86J15 PIC18F87J10				
PIC18F65J16 PIC18F66J11 PIC18F66J16 PIC18F67J11	64/80	AC162091	PIC18F87J11-ICE	3.6V
PIC18F85J16 PIC18F86J11 PIC18F86J16 PIC18F87J11				
PIC18F65J50 PIC18F65J55 PIC18F66J50 PIC18F66J55 PIC18F67J50	64/80	AC162087	PIC18F87J50-ICE	3.6V
PIC18F85J50 PIC18F85J55 PIC18F86J50 PIC18F86J55 PIC18F87J50				
PIC18F66J60 PIC18F66J65 PIC18F67J60	64/80/ 100	AC162064	PIC18F97J60-ICE	3.6V
PIC18F86J60 PIC18F86J65 PIC18F87J60				
PIC18F96J60 PIC18F96J65 PIC18F97J60				

TABLE 4: OPTIONAL HEADERS BY DEVICE (CON'T)

Device	Pin Count	Header Part Number	ICE/ICD Device Used	V _{DD} Max
PIC24FJ16GA002 PIC24FJ32GA002 PIC24FJ48GA002 PIC24FJ64GA002	28	AC162088	PIC24FJ64GA004-ICE	3.6V
PIC24FJ16GA004 PIC24FJ32GA004 PIC24FJ48GA004 PIC24FJ64GA004	44	AC162094		
PIC24FJ64GA006 PIC24FJ64GA008 PIC24FJ64GA010	64/80/ 100	AC162065 AC244022	PIC24FJ128GA010-ICE ⁽¹⁾	3.6V
PIC24FJ96GA006 PIC24FJ96GA008 PIC24FJ96GA010				
PIC24FJ128GA006 PIC24FJ128GA008 PIC24FJ128GA010				

Note 1: These ICE/ICD devices have limitations. See header section for details.

2: V_{DDCORE} Max

Optional Headers

AC162062, AC162079, AC162087, AC162091

Header Identification

The header AC number is used for ordering the header. However, this number is not on the header board, as the board is often used for multiple headers by inserting different ICE/ICD devices. To identify these header boards, use the following information.

Header	ICE/ICD Device	Board Assembly Number
AC162062	PIC18F87J10-ICE	02-01830
AC162079	PIC18F85J90-ICE	
AC162087	PIC18F87J50-ICE	
AC162091	PIC18F87J11-ICE	

Header Setup and Operation – AC162062

For this header, there are no jumpers/switches. MPLAB IDE will use its selected device to choose the correct device to emulate.

Test points are available on this header to check the following: VDD, VDDCORE and ground.

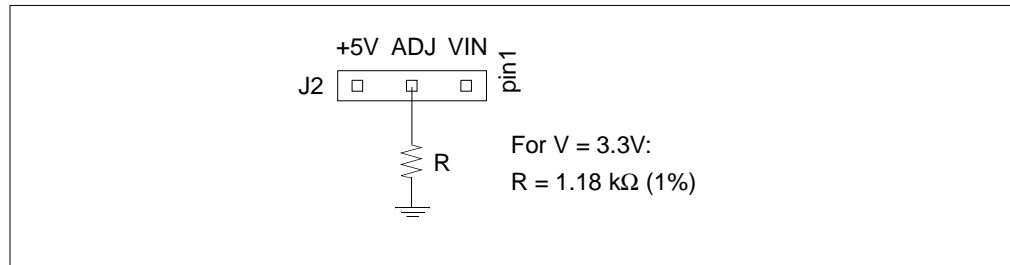
CAUTION

This header cannot be plugged directly into the PICDEM™ HPC Explorer Board or device damage will result.

The PICDEM™ HPC Explorer Board is 5V, whereas the ICD device on the header is 3.6V max. Therefore, modification to the demo board is necessary before the header can be used.

1. Switch S3 should be set to ICE.
2. Jumper J2 must be connected as shown in Figure 17 to modify the operating voltage. See demo board documentation for more information.

FIGURE 17: DEMO BOARD J2 CONNECTIONS



Header Setup and Operation – AC162079, AC162087, AC162091

For these headers, there are no jumpers/switches. MPLAB IDE will use its selected device to choose the correct device to emulate.

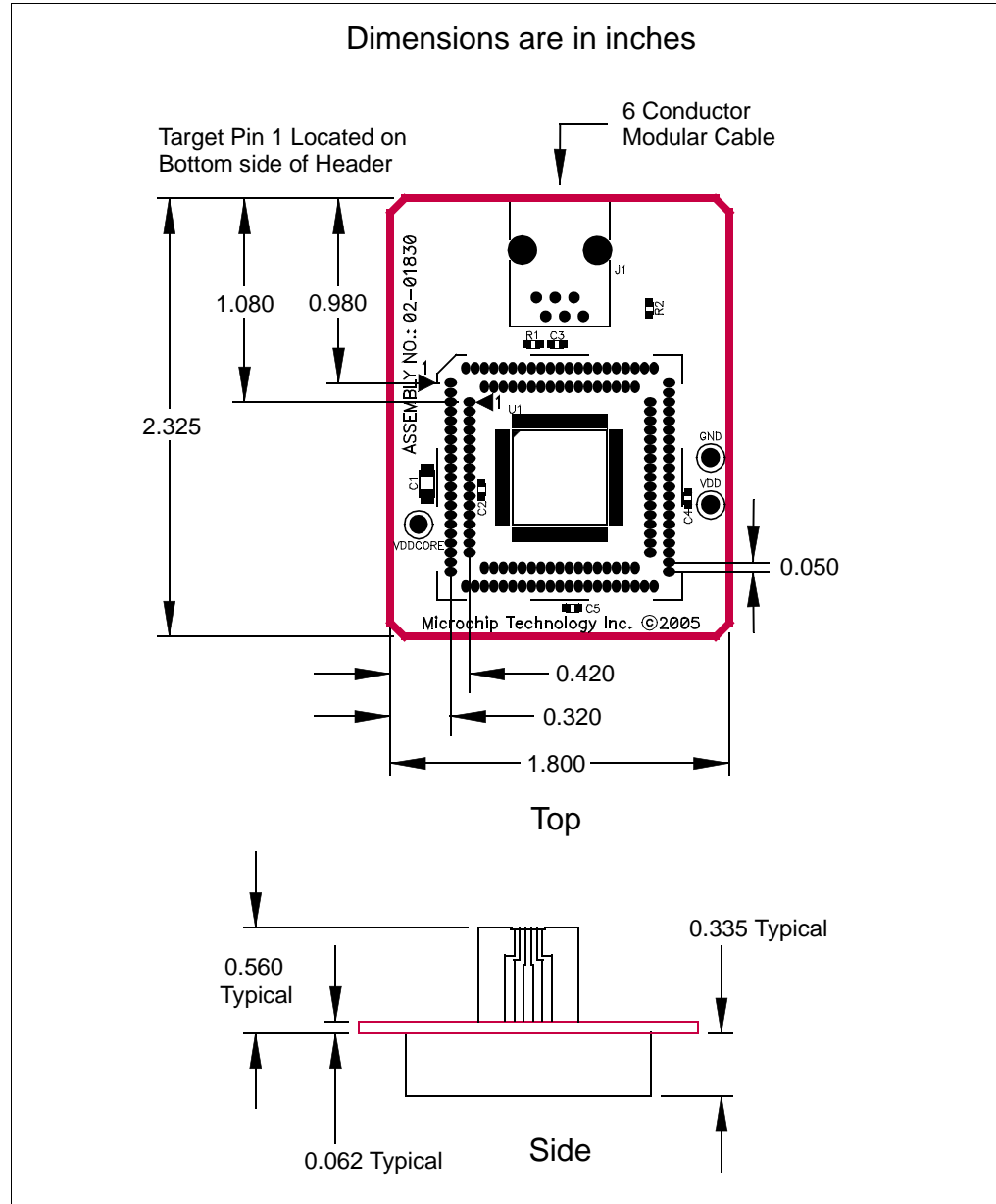
Test points are available on this header to check the following: VDD, VDDCORE and ground.

Header Dimensions

The figure below lists the dimensions for the header board. Dimensions are design values in inches.

If the length and/or width of the header board is too large a footprint for the target board, consider using stand-offs, transition sockets or other extenders in the header connection socket to raise the header above the target.

FIGURE 18: DIMENSIONS – AC162062, AC162079, AC162087, AC162091



Optional Headers

AC162064

Header Identification

The header AC number is used for ordering the header. However, this number is not on the header board, as the board is often used for multiple headers by inserting different ICE/ICD devices. To identify this header board, use the following information.

Header	ICE/ICD Device	Board Assembly Number
AC162064	PIC18F97J60-ICE	02-01853

Header Setup and Operation

For this header, there are no jumpers/switches. MPLAB IDE will use its selected device to choose the correct device to emulate.

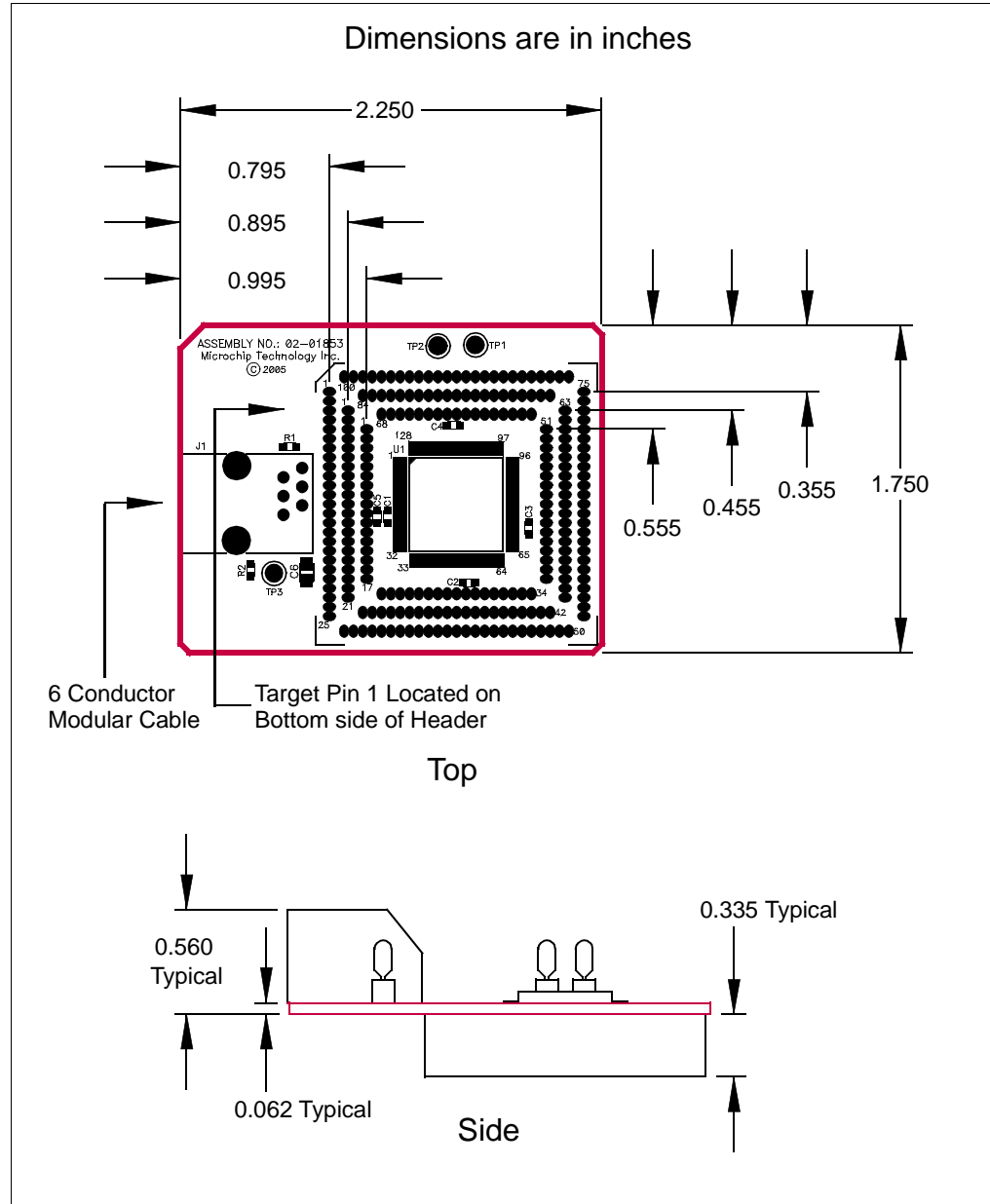
Test points are available on this header to check the following: VDD, VDDCORE and ground.

Header Dimensions

The figure below lists the dimensions for the header board. Dimensions are design values in inches.

If the length and/or width of the header board is too large a footprint for the target board, consider using stand-offs, transition sockets or other extenders in the header connection socket to raise the header above the target.

FIGURE 19: DIMENSIONS – AC162064



Optional Headers

AC162065, AC244022

Header Identification

The header AC number is used for ordering the header. However, this number is not on the header board, as the board is often used for multiple headers by inserting different ICE/ICD devices. To identify these header boards, use the following information.

Header	ICE/ICD Device	Board Assembly Number
AC162065	PIC24FJ128GA010-ICE	02-01856
AC244022		02-01985

Header Setup and Operation

For this header, there are no jumpers/switches. MPLAB IDE will use its selected device to choose the correct device to emulate.

Test points are available on this header to check the following: VDD, VDDCORE and ground.

Limitation: You must remove the $\overline{\text{MCLR}}$ pull-up resistor on your development board to use these headers.

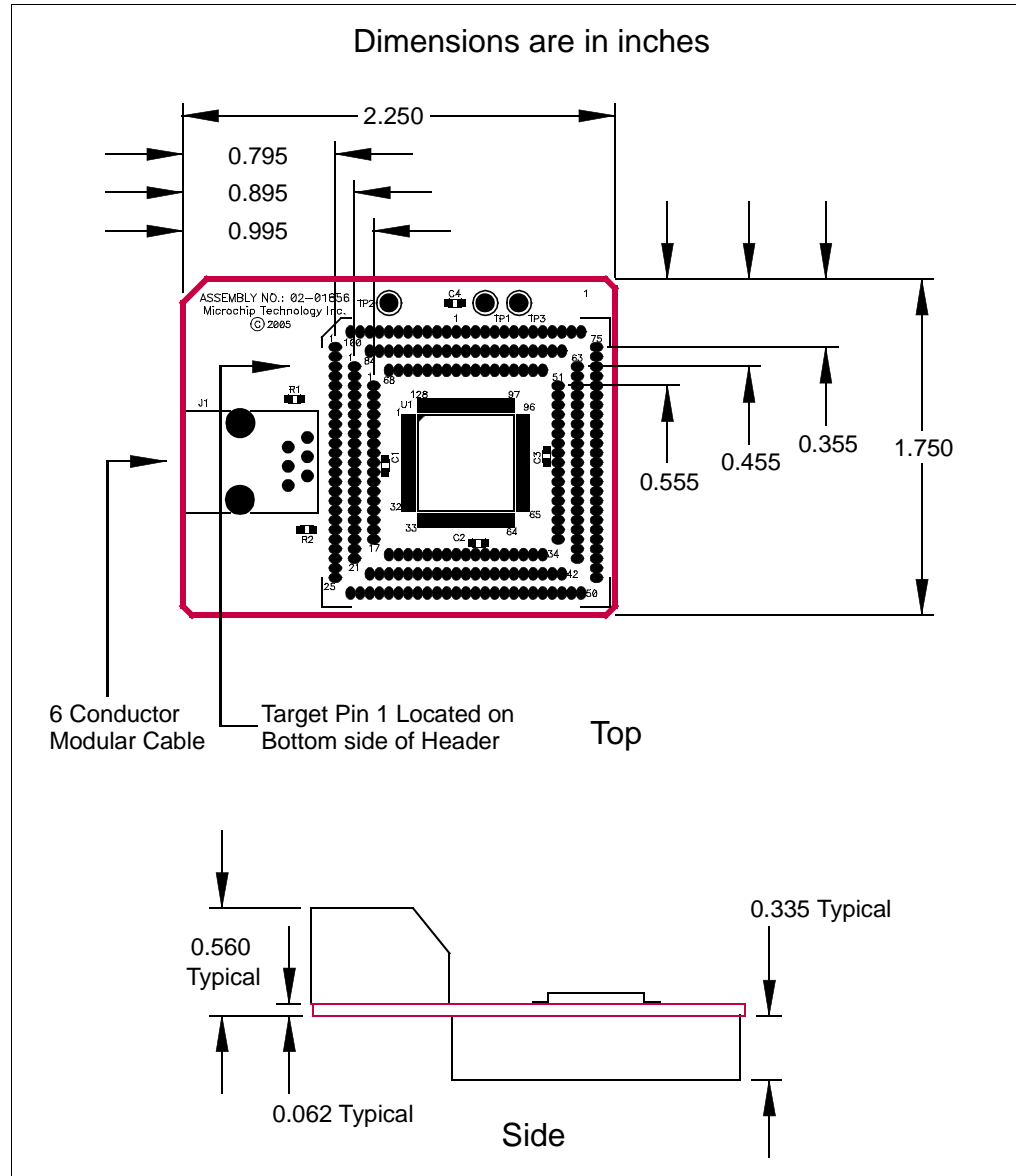
Header Dimensions

The figures below lists the dimensions for the header boards. Dimensions are design values in inches.

For this device family, header AC162065 will be sold until depleted. Then only header AC244022 will remain as a Performance Pak.

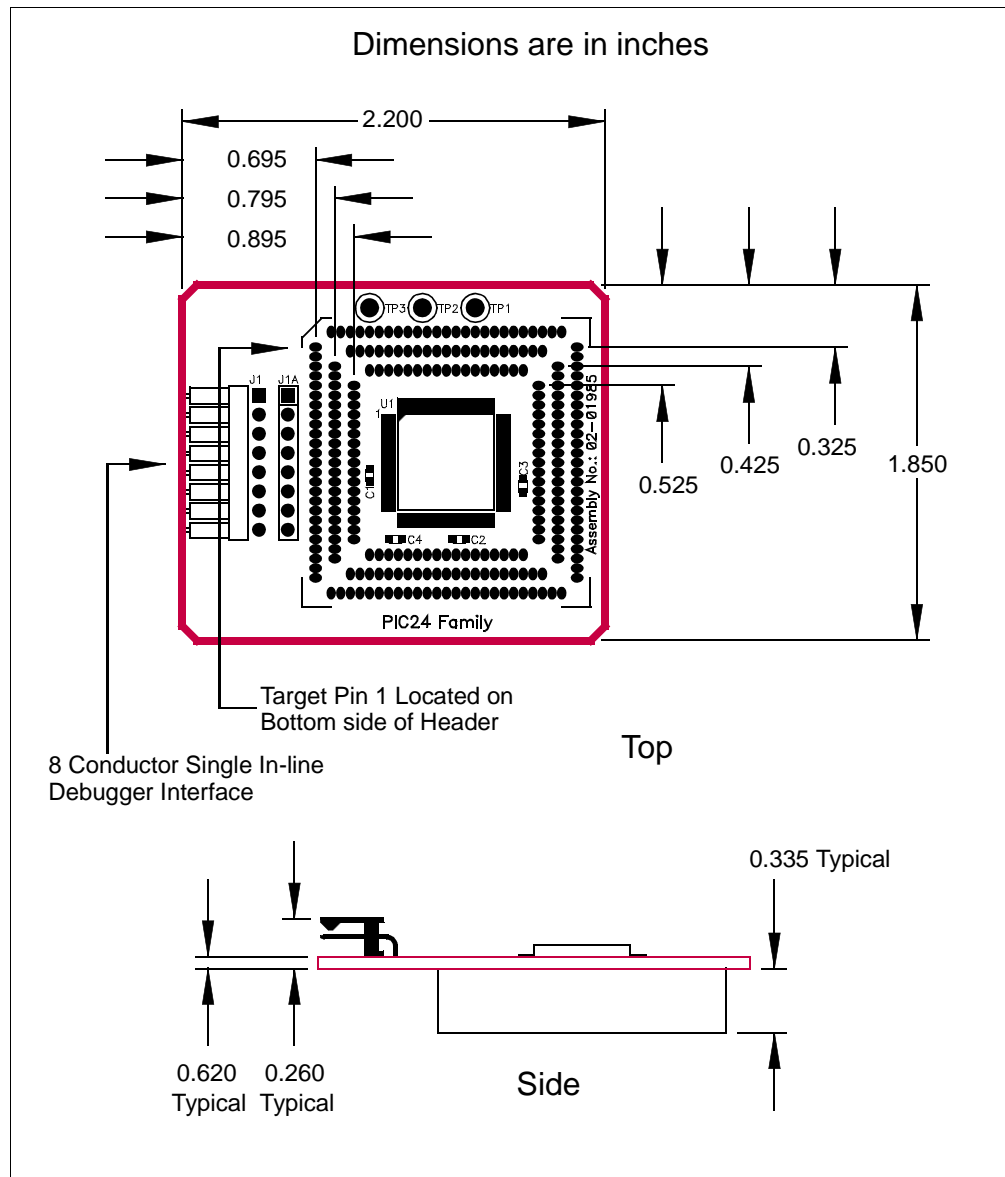
If the length and/or width of the header board is too large a footprint for the target board, consider using stand-offs, transition sockets or other extenders in the header connection socket to raise the header above the target.

FIGURE 20: DIMENSIONS – AC162065



Optional Headers

FIGURE 21: DIMENSIONS – AC244022



AC162067, AC162074

Header Identification

The header AC number is used for ordering the header. However, this number is not on the header board, as the board is often used for multiple headers by inserting different ICE/ICD devices. To identify these header boards, use the following information.

Header	ICE/ICD Device	Board Assembly Number
AC162067	PIC18F45J10-ICE	02-01854
AC162074		02-01929

Header Setup and Operation

For these headers, you will need to connect jumpers J2 and J3 to select between the LF and F versions of devices.

Device	Device Type	Jumper J2	Jumper J3	Function
PIC18LFXXJ10	LF	1-2	1-2	Disable voltage regulator*
PIC18FXXJ10	F	2-3	2-3	Enable voltage regulator

* VDDCORE must be supplied externally.

Header Dimensions

The figures below lists the dimensions for the header boards. Dimensions are design values in inches.

If the length and/or width of the header board is too large a footprint for the target board, consider using stand-offs, transition sockets or other extenders in the header connection socket to raise the header above the target.

Optional Headers

FIGURE 22: DIMENSIONS (28/40-PIN) – AC162067

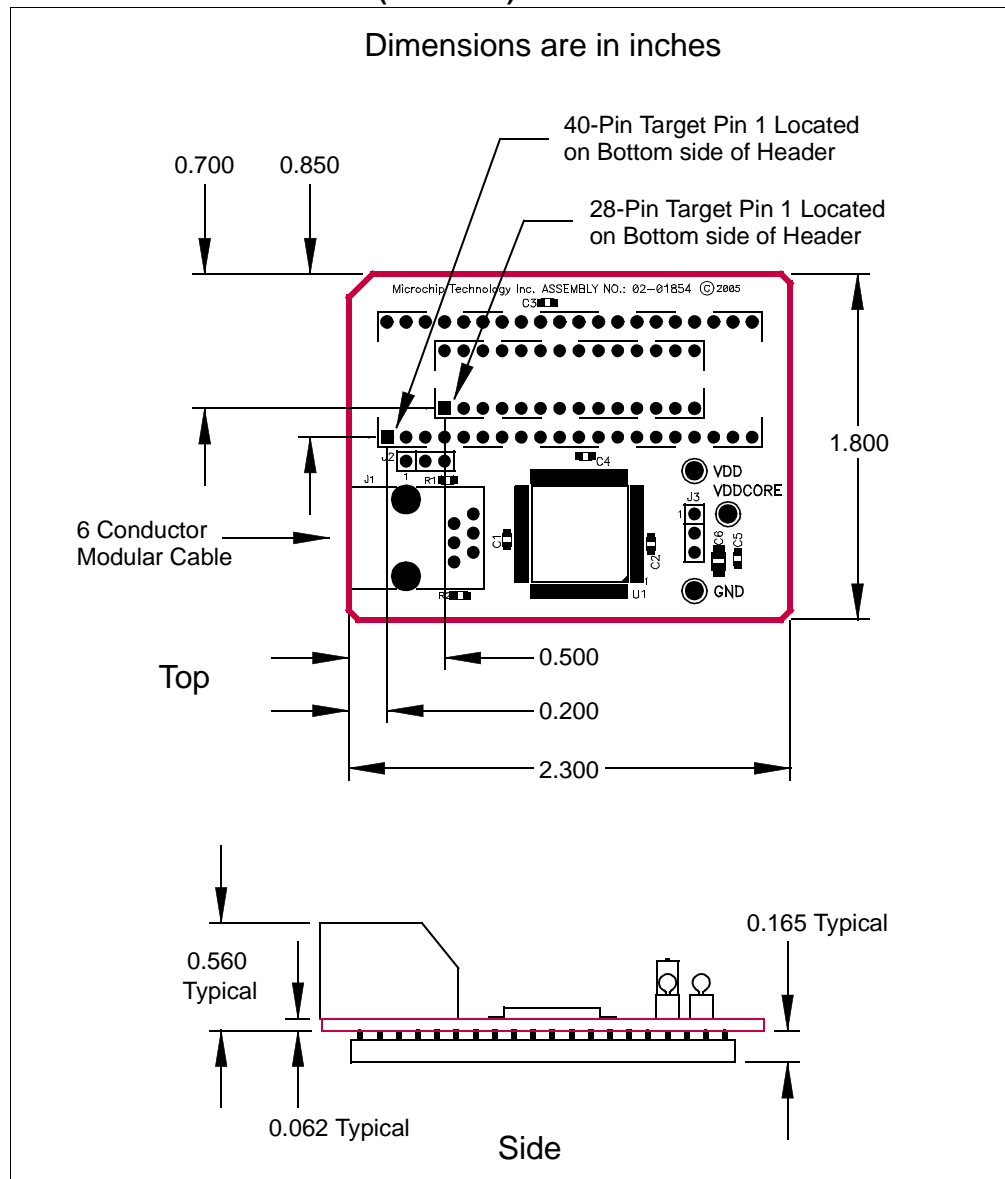
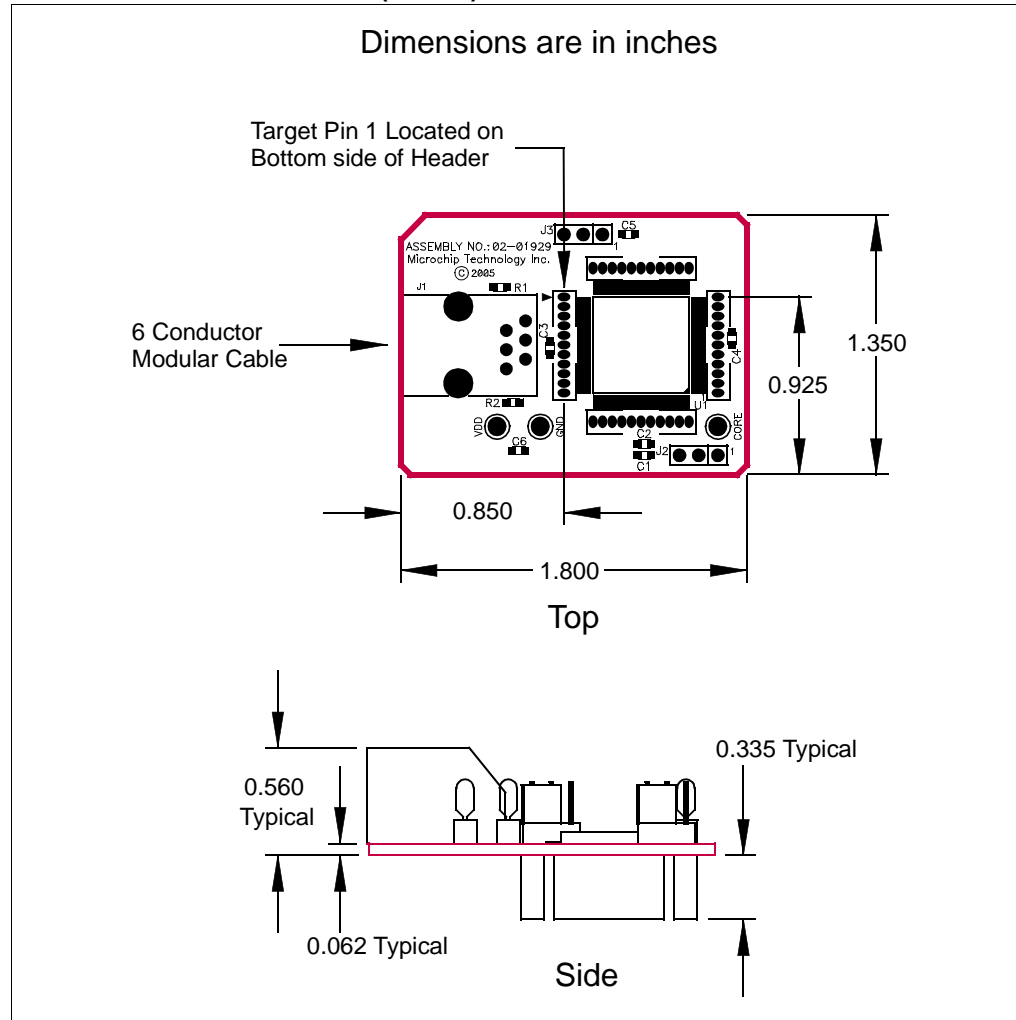


FIGURE 23: DIMENSIONS (44-PIN) – AC162074



Optional Headers

AC162078

Header Identification

The header AC number is used for ordering the header. However, this number is not on the header board, as the board is often used for multiple headers by inserting different ICE/ICD devices. To identify this header board, use the following information.

Header	ICE/ICD Device	Board Assembly Number
AC162078	PIC18F1330-ICD	02-01896

Header Setup and Operation

This header board can be used with the following targets:

1. Customer target board for PIC18F1230/1330 devices. The target board should have an 18-pin DIP socket to connect to the 18-pin DIP socket on the header board.
2. PICDEM MC motor control demo board. Use the 28-pin DIP socket on both boards to mount the header board on to the PICDEM MC board.
3. PICDEM MC LV motor control demo board. Use the 28-pin DIP socket on both boards to mount the header board on to the PICDEM MC LV board.

The following sections detail the configuration of the jumpers on the header board for use with the above mentioned boards.

CUSTOMER TARGET BOARD

The default configuration is to remove all jumpers.

S1 is not populated and should not be used.

After the header board is set up, do the following:

1. Connect the header to the target board.
2. Power the target board. You should see the red LED on the header board turn on.
3. Connect the debug tool to the header board.
4. Use MPLAB IDE and the debug tool to develop your application.

PICDEM MC/MC LV MOTOR CONTROL DEMO BOARDS

To run a BLDC motor on the PICDEM MC board or PICDEM MC LV board using the supplied firmware, use the following jumper setup:

Jumper	Jumper Setting	Jumper	Jumper Setting
J1	2-3	JP1	Open
J2	2-3		
J3	2-3	JP2	Open
J5	2-3		
J6	1-2	JP3	Open
J7	1-2		

S1 is not populated and should not be used.

After the header board is set up, do the following:

1. Connect the header to the PICDEM MC/MC LV target board.
2. Power the target board. You should see the red LED on the header board turn on.
3. Connect the debug tool to the header board.
4. Program the part with the demo code.
5. Run the program.
6. Press and release switch S2 on the target board to toggle the direction of the motor's rotation.
7. Press and release switch S1 on the target board to toggle between running and stopping the motor.
8. If the motor stops while reversing from a high speed, there could be an overcurrent condition detected by the system. Reset the system to run the program again.

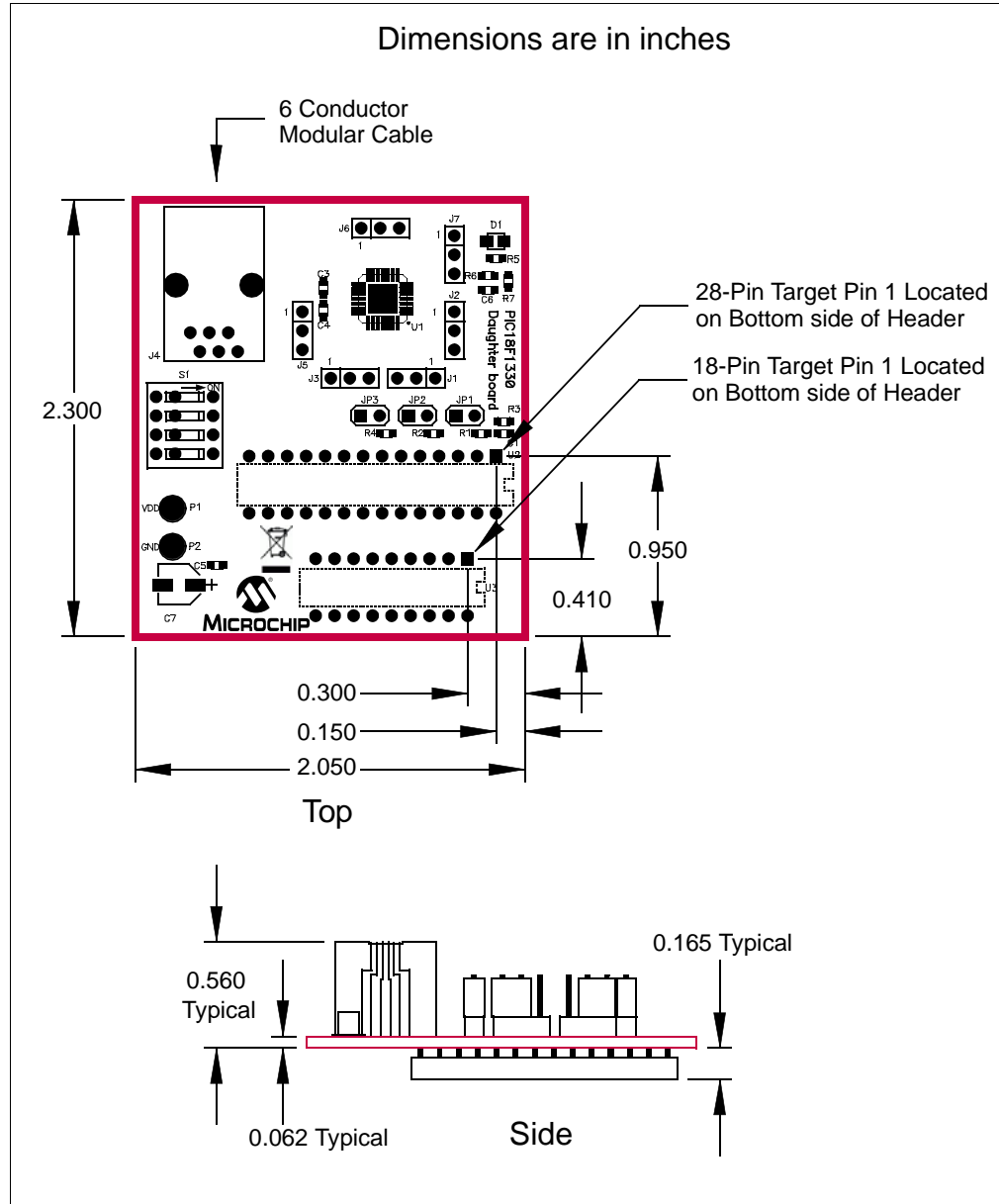
Optional Headers

Header Dimensions

The figure below lists the dimensions for the header board. Dimensions are design values in inches.

If the length and/or width of the header board is too large a footprint for the target board, consider using stand-offs, transition sockets or other extenders in the header connection socket to raise the header above the target.

FIGURE 24: DIMENSIONS – AC162078



AC162088, AC162094

Header Identification

The header AC number is used for ordering the header. However, this number is not on the header board, as the board is often used for multiple headers by inserting different ICE/ICD devices. To identify this header board, use the following information.

Header	ICE/ICD Device	Board Assembly Number
AC162088	PIC24FJ64GA004-ICE	02-01979
AC162094		02-01982

Header Setup and Operation

Both 28-pin and 44-pin device headers have jumpers related to the enabling or disabling of the on-chip 2.5 volt voltage regulator. Please see the section entitled “On-Chip Voltage Regulator” in the “*dsPIC33F Family Reference Manual*” (DS70165) for more details.

Jumper J2	Function
1-2	Disable voltage regulator
2-3	Enable voltage regulator
No connection	DISVREG controlled by target

Test points are available on this header to check the following:

Test Point	Color	Signal	Test Point	Color	Signal
TP1	Black	Ground	TP5	White	DISVREG
TP2	Red	VDD	TP6	White	PGC
TP3	Black	AVSS	TP7	White	PGD
TP4	Red	AVDD	TP8	Yellow	$\overline{\text{ICRST}}$

Header Dimensions

The figures below list the dimensions for the header boards. Dimensions are design values in inches.

If the length and/or width of the header board is too large a footprint for the target board, consider using stand-offs, transition sockets or other extenders in the header connection socket to raise the header above the target.

Optional Headers

FIGURE 25: DIMENSIONS (28 PIN) – AC162088

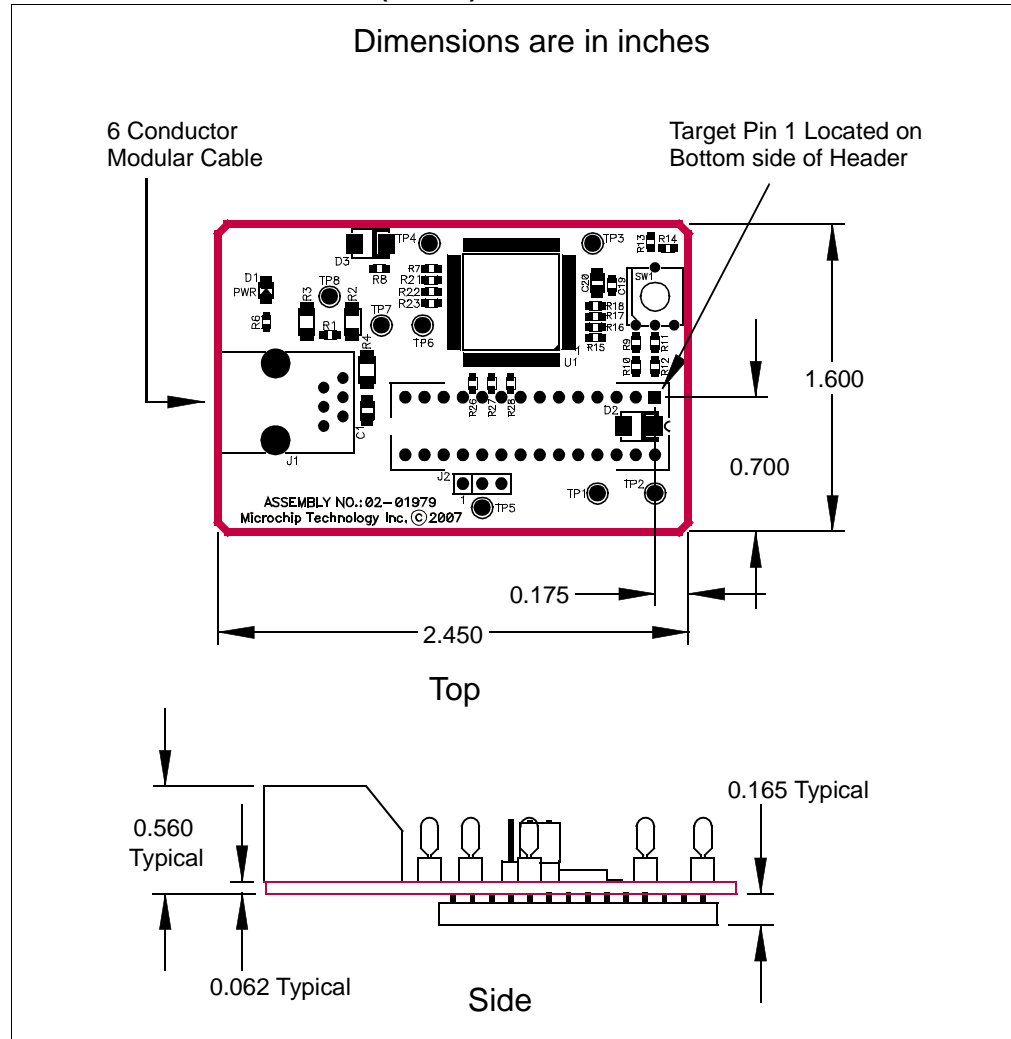
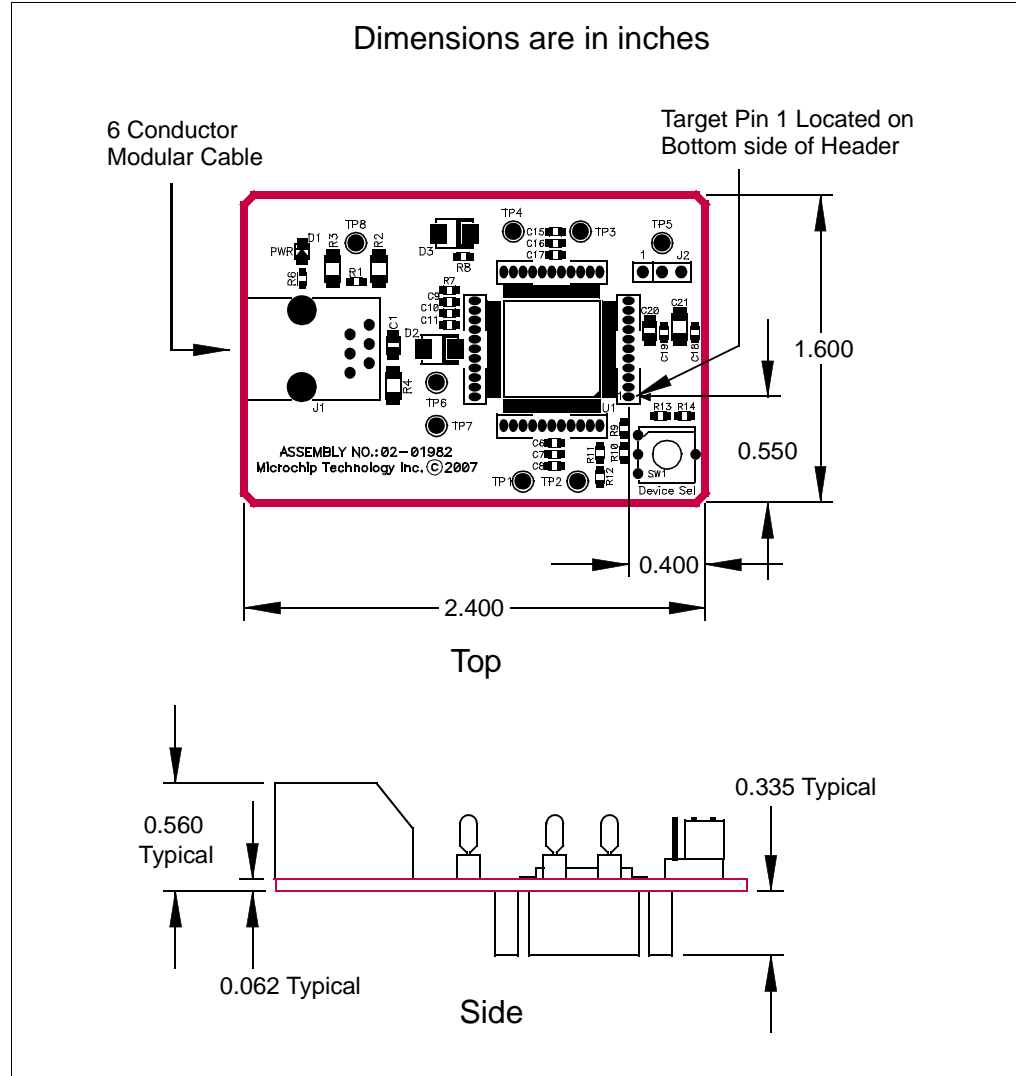


FIGURE 26: DIMENSIONS (44 PIN) – AC162094



Optional Headers

NOTES:

Appendix A. Header Target Footprints

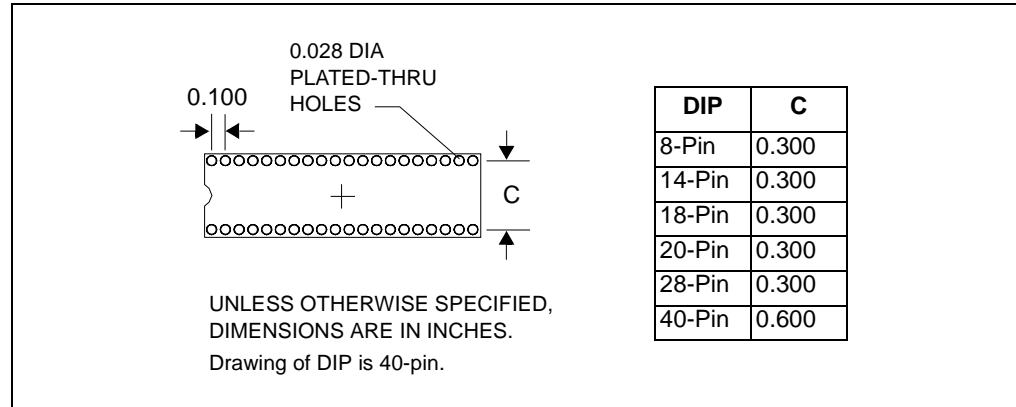
To connect a header board directly to a target board (without the use of a transition socket) the following information will be helpful.

- DIP Device Footprints
- TQFP/PLCC Device Footprints

DIP DEVICE FOOTPRINTS

DIP device adapter footprints shown will accept adapter plugs like Samtec series APA plugs. These plugs can be soldered in place during development/emulation and eliminate the need for any other sockets.

FIGURE 27: DIP FOOTPRINT



TQFP/PLCC DEVICE FOOTPRINTS

TQFP/PLCC device adapter footprints shown will accept board stackers like Samtec series DWM 0.050 Pitch Stackers. These stackers can be soldered in place during development/emulation and eliminate the need for any other sockets.

FIGURE 28: SINGLE-ROW TQFP/PLCC FOOTPRINT

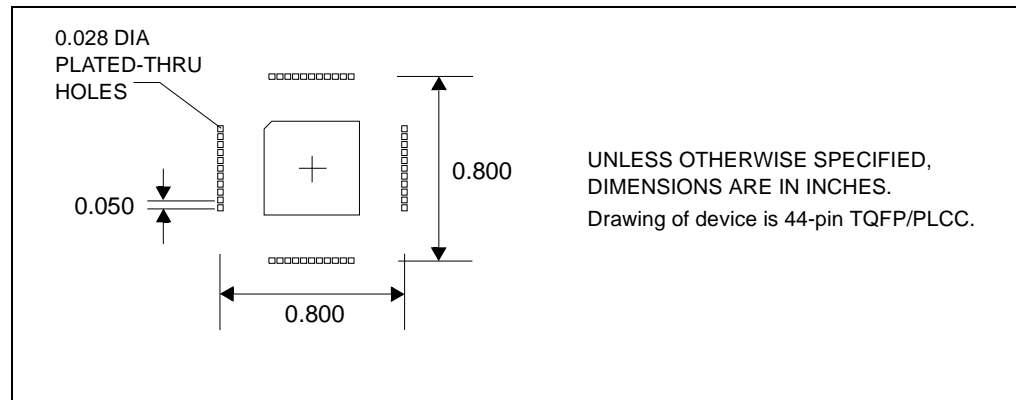
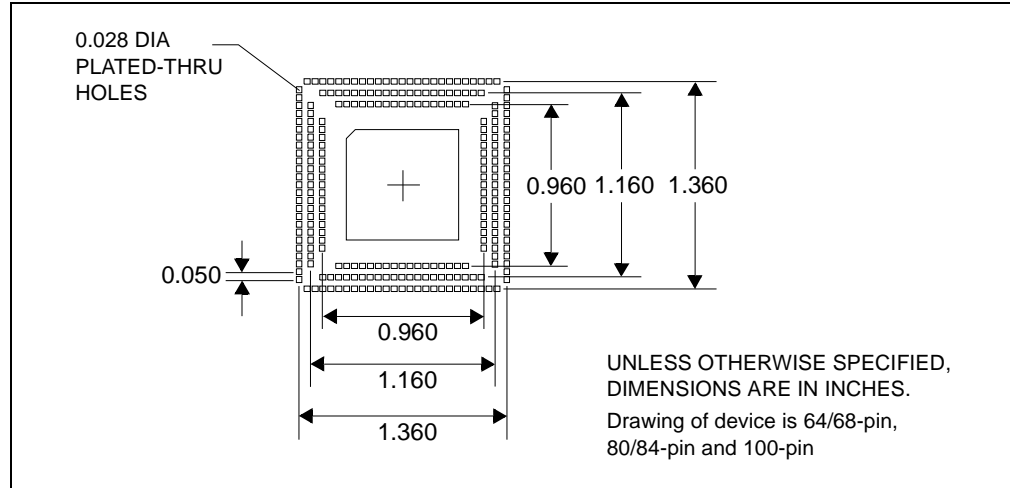


FIGURE 29: DOUBLE AND TRIPLE-ROW TQFP/PLCC FOOTPRINT



Header pin-out matches the PLCC package. PLCC will map to TQFP as follows:

- Header to 44-pin TQFP – one-to-one mapping.
- Header to 64-pin TQFP – see Figure 30 for mapping.
- Header to 80-pin TQFP – see Figure 31 for mapping.
- Header to 100-pin TQFP – one-to-one mapping.

FIGURE 30: HEADER TO 64-PIN TQFP

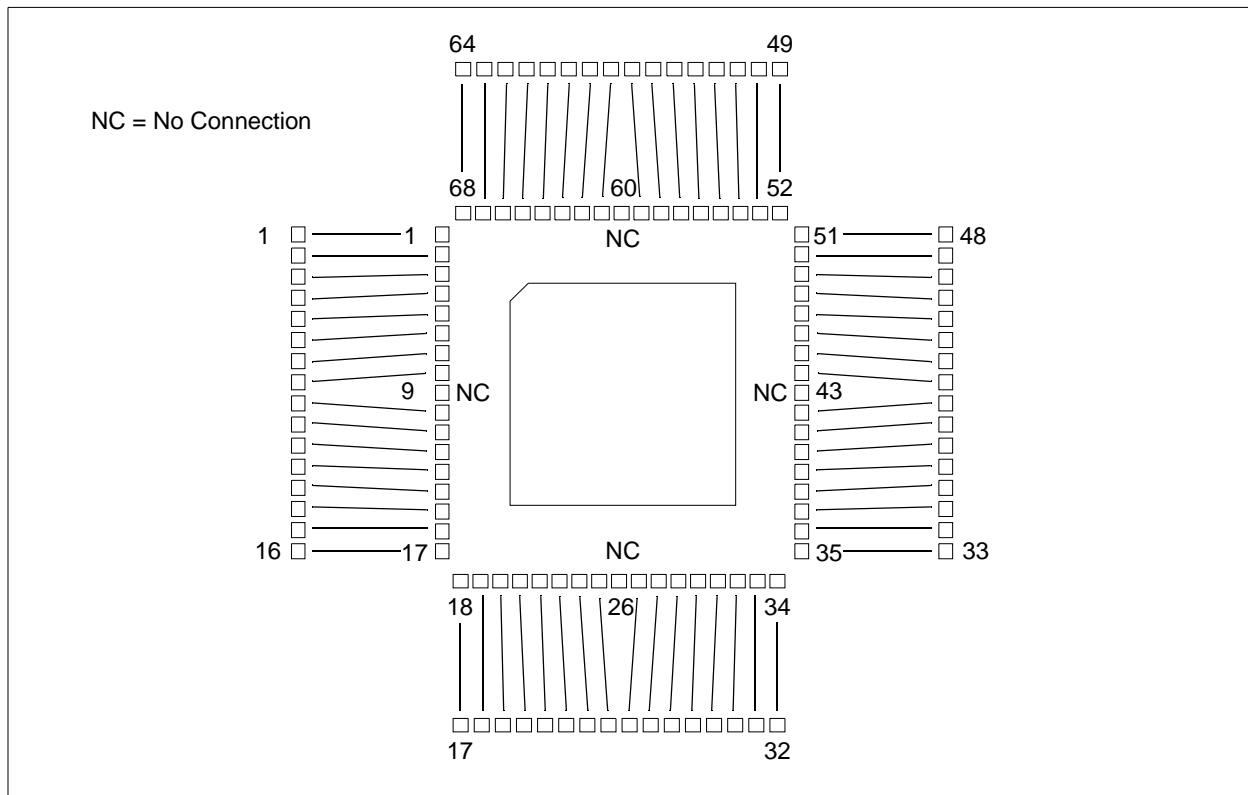
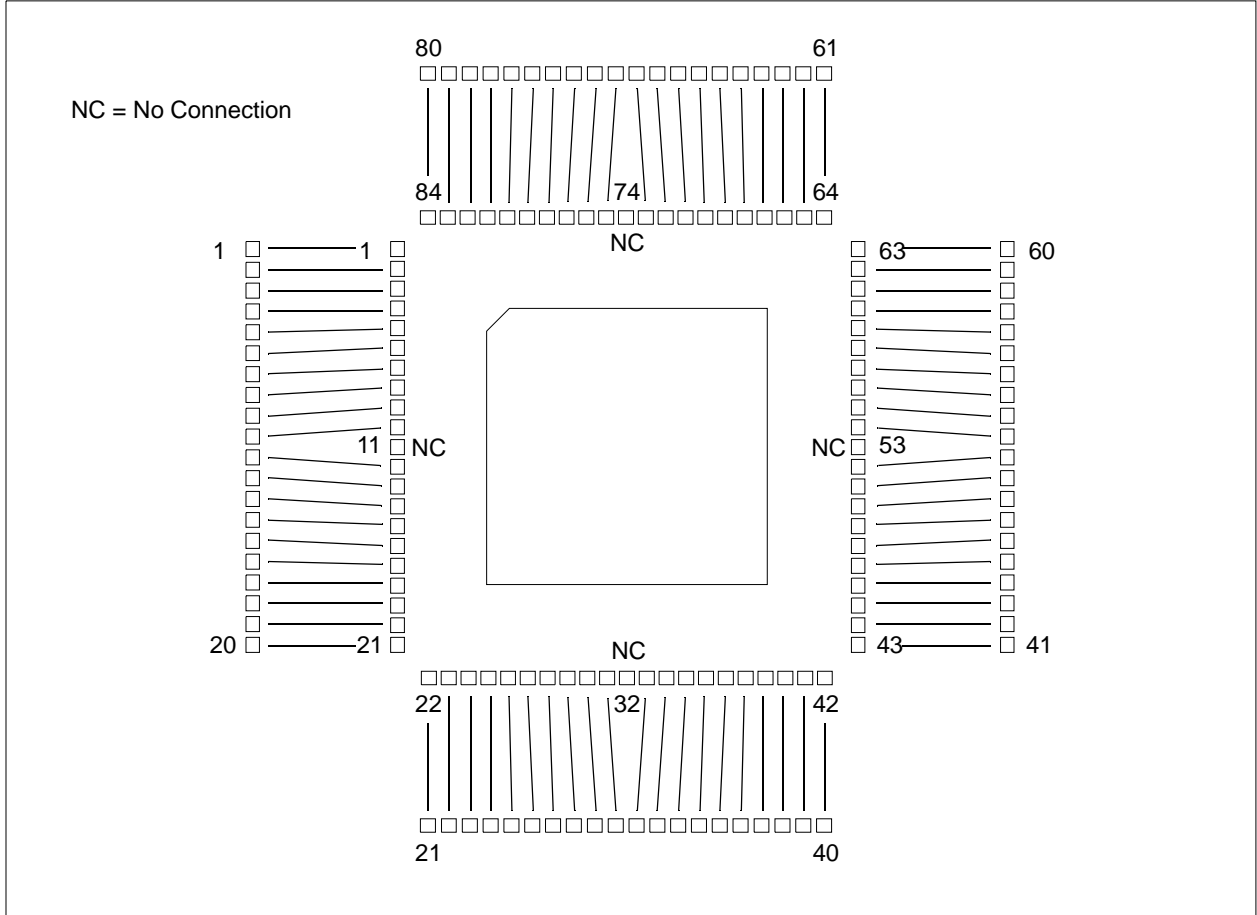


FIGURE 31: HEADER TO 80-PIN TQFP



NOTES:

APPENDIX B: REVISION HISTORY

B.1 Revision N (February 2006)

- Added Appendix A: Revision History
- Updated document to reflect support of additional tools
- Additional minor corrections throughout document text

B.2 Revision P (September 2007)

- Updated document to reflect support of additional tools
- Additional minor corrections throughout document text

B.3 Revision Q (December 2008)

- Added limitations to header setup sections as needed.
- Changed “ICD Headers” and “ICE Headers” to “Required Headers” and “Optional Headers” and move sections as necessary.
- Rearranged sections to organize by header (AC) number.

B.4 Revision R (April 2009)

- Added board dimensions
- Removed header pinouts
- Added board identification info
- Added “why use a header” section
- Added footprint appendix
- Changed MPLAB ICD 2 and MPLAB ICD 3 references to generic debug tool
- Added MPLAB REAL ICE in-circuit emulator and MPLAB ICD 3 as programmers

Header Board Specification

NOTES:

Index

A		PIC10F222	7
AC162050	8	PIC12F508	7
AC162052	10	PIC12F509	7
AC162053	12	PIC12F510	7
AC162054	12	PIC12F519	7
AC162055	10	PIC12F609	7, 21
AC162056	10	PIC12F615	21
AC162057	10	PIC12F629	7, 8
AC162058	8	PIC12F635	7, 10
AC162059	14	PIC12F675	7, 8
AC162060	16	PIC12F683	7, 8
AC162061	17	PIC12HV609	7, 21
AC162062	28	PIC12HV615	21
AC162064	30	PIC16F505	7
AC162065	32	PIC16F506	7
AC162066	19	PIC16F526	7
AC162067	35	PIC16F610	7, 21
AC162070	14	PIC16F616	7, 21
AC162074	35	PIC16F627A	7, 12
AC162078	38	PIC16F628A	7, 12
AC162079	28	PIC16F630	7, 10
AC162083	21	PIC16F631	7, 17
AC162087	28	PIC16F636	7, 10
AC162088	41	PIC16F639	7, 19
AC162091	28	PIC16F648A	7, 12
AC162094	41	PIC16F676	7, 10
AC162096	14	PIC16F677	7, 17
AC244022	32	PIC16F684	7, 10
AC244023	23	PIC16F685	7, 17
AC244024	23	PIC16F687	7, 17
Additional Information	5	PIC16F688	7, 10
C		PIC16F689	7, 17
Calibration Bits	5	PIC16F690	7, 17
I		PIC16F716	8, 12
ICE vs. ICD	2	PIC16F785	8, 16
J		PIC16HV610	7, 21
Jumper Settings	8, 10, 16, 19, 28, 35, 39, 41	PIC16HV616	7, 21
M		PIC16HV785	8, 16
MPLAB ICE 2000	19	PIC18F1230	25, 38
P		PIC18F1330	25, 38
PCM16YM0	19	PIC18F13K50	8
Performance	5	PIC18F14K50	8
PIC10F200	7	PIC18F24J10	25
PIC10F202	7	PIC18F25J10	25, 35
PIC10F204	7	PIC18F44J10	25
PIC10F206	7	PIC18F45J10	25, 35
PIC10F220	7	PIC18F63J11	25
		PIC18F63J90	25
		PIC18F64J11	25
		PIC18F64J16	25
		PIC18F64J90	25

Header Board Specification

PIC18F64J95	25	PIC24FJ32GA004	27
PIC18F65J10	26	PIC24FJ48GA002	27
PIC18F65J11	25	PIC24FJ48GA004	27
PIC18F65J15	26	PIC24FJ64GA002	27
PIC18F65J16	26	PIC24FJ64GA004	27
PIC18F65J50	26	PIC24FJ64GA006	27
PIC18F65J55	26	PIC24FJ64GA008	27
PIC18F65J90	25	PIC24FJ64GA010	27
PIC18F66J10	26	PIC24FJ96GA006	27
PIC18F66J11	26	PIC24FJ96GA008	27
PIC18F66J15	26	PIC24FJ96GA010	27
PIC18F66J16	26	PICDEM HPC Explorer Board.....	28
PIC18F66J50	26	Pin Count.....	7, 25
PIC18F66J55	26	Programming Non-ICD Devices	3
PIC18F66J60	26		
PIC18F66J65	26	S	
PIC18F67J10	26	Switch Settings.....	17
PIC18F67J11	26	Switch Settings, Rotary	21
PIC18F67J50	26		
PIC18F67J60	26	T	
PIC18F83J11	25	Transition Socket.....	4
PIC18F83J90	25		
PIC18F84J11	25	V	
PIC18F84J16	25	Vdd Max	7, 25
PIC18F84J90	25	Vddcore Max	7, 25
PIC18F84J95	25		
PIC18F85J10	26		
PIC18F85J11	25		
PIC18F85J15	26		
PIC18F85J16	26		
PIC18F85J50	26		
PIC18F85J55	26		
PIC18F85J90	25		
PIC18F86J10	26		
PIC18F86J11	26		
PIC18F86J15	26		
PIC18F86J16	26		
PIC18F86J50	26		
PIC18F86J55	26		
PIC18F86J60	26		
PIC18F86J65	26		
PIC18F87J10	26		
PIC18F87J11	26		
PIC18F87J50	26		
PIC18F87J60	26		
PIC18F96J60	26		
PIC18F96J65	26		
PIC18F97J60	26		
PIC18LF13K50.....	8		
PIC18LF14K50.....	8		
PIC18LF24J10	25		
PIC18LF25J10	25, 35		
PIC18LF44J10	25		
PIC18LF45J10	25, 35		
PIC24FJ128GA006	27		
PIC24FJ128GA008	27		
PIC24FJ128GA010	27		
PIC24FJ16GA002	27		
PIC24FJ16GA004	27		
PIC24FJ32GA002	27		

Note the following details of the code protection feature on Microchip devices:

- Microchip products meet the specification contained in their particular Microchip Data Sheet.
- Microchip believes that its family of products is one of the most secure families of its kind on the market today, when used in the intended manner and under normal conditions.
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